



Reachability in Two-Parametric Timed Automata with one Parameter is EXPSPACE-Complete

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Accepted: 28 January 2023
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Abstract

Parametric timed automata (PTA) have been introduced by Alur, Henzinger, and Vardi as an extension of timed automata in which clocks can be compared against parameters. The reachability problem asks for the existence of an assignment of the parameters to the non-negative integers such that reachability holds in the underlying timed automaton. The reachability problem for PTA is long known to be undecidable, already over three parametric clocks. A few years ago, Bundala and Ouaknine proved that for PTA over two parametric clocks and one parameter the reachability problem is decidable and also showed a lower bound for the complexity class $PSPACE^{NEXP}$. Our main result is that the reachability problem for two-parametric timed automata with one parameter is EXPSPACE-complete. Our contribution is two-fold. For the EXPSPACE lower bound, inspired by [13, 14], we make use of deep results from complexity theory, namely a serializability characterization of EXPSPACE (in turn based on Barrington's Theorem) and a logspace translation of numbers in Chinese remainder representation to binary representation due to Chiu, Davida, and Litow. It is shown that with small PTA over two parametric clocks and one parameter one can simulate serializability computations. For the EXPSPACE upper bound, we first give a careful exponential time reduction from PTA over two parametric clocks and one parameter to a (slight subclass of) parametric one-counter automata over one

This article belongs to the Topical Collection: *Theoretical Aspects of Computer Science (STACS 2021)*

Guest Editors: Markus Bläser and Benjamin Monmege

Stefan Göller was supported by the Agence nationale de la recherche grant no. ANR-17-CE40-0010

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parameter based on a minor adjustment of a construction due to Bundala and Ouaknine. For solving the reachability problem for parametric one-counter automata with one parameter, we provide a series of techniques to partition a fictitious run into several carefully chosen subruns that allow us to prove that it is sufficient to consider a parameter value of exponential magnitude only. This allows us to show a doubly-exponential upper bound on the value of the only parameter of a PTA over two parametric clocks and one parameter. We hope that extensions of our techniques lead to finally establishing decidability of the long-standing open problem of reachability in parametric timed automata with two parametric clocks (and arbitrarily many parameters) and, if decidability holds, determining its precise computational complexity.

Keywords Parametric timed automata · Computational complexity · Reachability · EXPSpace-complete

1 Introduction

Background In the 1990's *timed automata* have been introduced by Alur and Dill [2]. They extend finite automata by clocks that can be compared against integer constants and provide a popular formalism to reason about the behavior of real-time systems with desirable algorithmic properties; for instance the reachability/emptiness problem is decidable and in fact PSPACE-complete [1].

For a more general means to specify the behavior of under-specified systems, such as embedded systems, Alur, Henzinger and Vardi [3] have introduced *parametric timed automata (PTA)* only a few years later. By a PTA we mean a parametric timed automaton over discrete time whose guards are of the form $x \bowtie p$ and $x \bowtie k$, where x is a clock, p is a parameter ranging over unspecified non-negative integers and k is a constant ranging over the non-negative integers. A clock x is *parametric* if it appears in at least one guard of the form $x \bowtie p$ and *non-parametric* otherwise.

Towards the verification of safety properties, or loosely speaking ruling out the existence of an execution to a bad state, the *reachability problem for PTA* in turn asks for the existence of an assignment of the parameters to the non-negative integers such that reachability holds in the resulting timed automaton.

On the negative side, it has been shown in [3] that already for PTA that contain *three parametric clocks* reachability is undecidable — even in the presence of a single parameter [8].

On the positive side however, Alur, Henzinger and Vardi have shown in [3] that reachability is decidable for PTA that contain only *one parametric clock* but allowing arbitrarily many non-parametric clocks, yet by an algorithm whose running time is non-elementary.

For PTA over *one parametric clock* (and arbitrarily many non-parametric clocks), Bundala and Ouaknine have shown a first elementary complexity upper bound for the reachability problem; it is shown to be NEXP-hard and in 2NEXP [10]. The matching NEXP upper bound has been proven by Beneš et al. in [8] (also in the continuous

time setting), we refer to [9] for an alternative proof by Bollig, Quaas and Sangnier using alternating 2-way automata.

Bundala and Ouaknine [10] have recently advanced the decidability and complexity status of the reachability problem for PTA over *two parametric clocks* [10] (and arbitrarily many non-parametric clocks): it is shown that in the presence of *one parameter* the reachability problem is decidable and hard for the complexity class $\text{PSPACE}^{\text{NEXP}}$. To the best of our knowledge, this is in fact the largest subclass of PTA for which reachability is known to be decidable. For showing the above-mentioned decidability result [10] provides a reduction from PTA over two parametric clocks to a suitable formalism of *parametric one-counter automata*. Such an approach via parametric one-counter automata has already successfully been applied to model checking freeze-LTL as shown by Demri and Sangnier [12] and Lechner et al. [21], yet notably over a weaker model of parametric one-counter automata than the one introduced in [10]. On this note, it is worth mentioning that inter-reductions between the reachability problem of (non-parametric) timed automata involving two clocks and one-counter automata have already been established by Haase et al. [16, 17].

Decidability of reachability in PTA over two parametric clocks (with arbitrarily many non-parametric clocks and arbitrarily many parameters) is still considered to be a challenging open problem to the best of our knowledge. For instance, as already remarked in [3], there is an easy reduction from the existential fragment of Presburger Arithmetic with divisibility to reachability in PTA over two parametric clocks.

Our contribution Our main result (Theorem 5) states that reachability in parametric timed automata over two parametric clocks, arbitrarily many non-parametric clocks, and one integer-valued parameter, is EXPSPACE -complete. Our contribution is two-fold.

Inspired by [13, 14], for the EXPSPACE lower bound we make use of deep results from complexity theory, namely a serializability characterization of EXPSPACE (in turn originally based on Barrington's Theorem [7]) and a logspace translation of numbers in Chinese remainder representation to binary representation due to Chiu, Davida, and Litow [11]. It is shown that with small PTA over two parametric clocks and one parameter one can simulate serializability computations.

For the EXPSPACE upper bound, we first give a careful exponential time reduction from PTA over two parametric clocks and one parameter to a (slight subclass of) parametric one-counter automata over one parameter based on a minor adjustment of a construction due to Bundala and Ouaknine [10]. In solving the reachability problem for parametric one-counter automata with one parameter, we provide a series of techniques to partition a fictitious run into several carefully chosen subruns that allow us to prove that it is sufficient to consider a parameter value of exponential magnitude. This allows us to show a doubly-exponential upper bound on the value of the only parameter of PTA with two parametric clocks and one parameter. We hope that extensions of our techniques lead to finally establishing decidability of the long-standing open problem of reachability in parametric timed automata with two parametric clocks (and arbitrarily many parameters) and, if decidability holds, determining its precise computational complexity.

As the results in [3], our results hold for PTA over discrete time, where it is worth mentioning that in [3] parameters can both be integer-valued and rational-valued. Indeed, for PTA with closed (i.e. non-strict) clock constraints and parameters ranging over integers, techniques [19, 23] exist that allow to reduce the reachability problem over continuous time to discrete time. There is a plethora of variants of PTA that have recently been studied, we refer to [4] for an extensive overview by André.

Overview of this paper In Section 2 we introduce general notations and state our main result. Our EXPSPACE lower bound can be found in Section 3. Section 4 introduces parametric one-counter automata and states an exponential time reduction from reachability in parametric timed automata with two parametric clocks and one parameter to reachability in parametric one-counter automata. Section 5 states the EXPSPACE upper bound and the central Small Parameter Theorem (Theorem 18). It also gives an overview of the proof of the Small Parameter Theorem, which itself stretches over Sections 6,7,8, and 9.

2 Preliminaries

We assume the reader is familiar with Turing machines and standard complexity classes such as LOGSPACE, PSPACE and EXPSPACE. We refer to [5, 24] for further details on complexity. We also assume the reader is familiar with (deterministic) finite automata and regular languages, we refer to [18] for more details on this.

By \mathbb{Z} we denote the *integers* and by $\mathbb{N} = \{0, 1, \dots\}$ we denote the *non-negative integers*. For every $a, b \in \mathbb{Z}$ with $a \leq b$ we define $[a, b] = \{k \in \mathbb{Z} \mid a \leq k \leq b\}$. For every $n \geq 1$ we define $n\mathbb{Z} = \{n \cdot z \mid z \in \mathbb{Z}\}$. For every number $n \in \mathbb{N}$ we define $\log(n) = \min\{i + 1 \mid i \in \mathbb{N}, n \leq 2^i\}$, which is the smallest number of bits necessary to write down n in binary. For every finite alphabet A we denote by A^* the set of finite words over A and denote the empty word by ε . For all $a \in A$ and all $w \in A^*$ let $|w|_a$ denote the number of occurrences of the letter a in w . For every finite set $M \subset \mathbb{N} \setminus \{0\}$ let $\text{LCM}(M) = \min\{n \geq 1 \mid \forall m \in M \setminus \{0\} : m \mid n\}$ denote the least common multiple of the elements in M . For any $j \in \mathbb{N}$ let $\text{LCM}(j) = \text{LCM}([1, j])$ denote the least common multiple of the numbers $\{1, \dots, j\}$. For any two sets X and S , let X^S denote the set of all functions from S to X . For any set S let $\mathcal{P}(S) = \{X \mid X \subseteq S\}$ denote the power set of S .

A *guard* over a finite set of clocks Ω and a finite set of parameters P is a comparison of the form $g = \omega \bowtie e$, where $\omega \in \Omega$, $e \in P \cup \mathbb{N}$, and $\bowtie \in \{<, \leq, =, \geq, >\}$; in case $e \in P$ we call g *parametric*, and *non-parametric* otherwise. We denote by $\mathcal{G}(\Omega, P)$ the *set of guard* over the set of clocks Ω and the set of parameters P . The *size* $|g|$ of a guard $g = \omega \bowtie e$ is defined as

$$|g| = \begin{cases} \log(e) & \text{if } e \in \mathbb{N} \\ 1 & \text{otherwise.} \end{cases}$$

A *clock valuation* is a function from Ω to \mathbb{N} ; we write $\vec{0}$ to denote the clock valuation $\omega \mapsto 0$. For each clock valuation v and each $t \in \mathbb{N}$ we denote by $v + t$ the clock valuation $\omega \mapsto v(\omega) + t$. A *parameter valuation* is a function μ from P to \mathbb{N} . For

every guard $g = \omega \bowtie p$ with $p \in P$ (resp. $g = \omega \bowtie k$ with $k \in \mathbb{N}$) we write $v \models_{\mu} g$ if $v(\omega) \bowtie \mu(p)$ (resp. $v(\omega) \bowtie k$; in this case we may also simply write $v \models g$). We define an *empty guard* g_{ϵ} over a non-empty finite set of clocks Ω and a finite set of parameters P to be of the form $\omega \geq 0$ for some $\omega \in \Omega$. In particular, we define g_{ϵ} such that for all $v \in \mathbb{N}^{\Omega}$ and all $\mu \in \mathbb{N}^P$ we have $v \models_{\mu} g_{\epsilon}$, hence g_{ϵ} can be used as a guard that is always true.

A parametric timed automaton as introduced in [3] is a finite automaton extended with a finite set of parameters P and a finite set of clocks Ω that all progress at the same rate and that can be individually reset to zero. Moreover, every transition is labeled by a guard over Ω and P and by a set of clocks to be reset. Formally, a *parametric timed automaton* (PTA for short) is a tuple $\mathcal{A} = (Q, \Omega, P, R, q_{init}, F)$, where

- Q is a non-empty finite set of control states,
- Ω is a non-empty finite set of clocks,
- P is a finite set of parameters,
- $R \subseteq Q \times \mathcal{G}(\Omega, P) \times \mathcal{P}(\Omega) \times Q$ is a finite set of rules,
- $q_{init} \in Q$ is an initial control state, and
- $F \subseteq Q$ is a set of final control states.

A clock $\omega \in \Omega$ is called *parametric* if there exists some $(q, g, U, q') \in R$ such that the guard g is of the form $\omega \bowtie p$, with $\bowtie \in \{<, \leq, =, \geq, >\}$ and $p \in P$. We also refer to \mathcal{A} as a (m, n) -PTA if $m = |\{\omega \in \Omega \mid \omega \text{ is parametric}\}|$ is the number of parametric clocks and $n = |P|$ is the number of parameters of \mathcal{A} — sometimes we also just write $(m, *)$ -PTA (resp. $(*, n)$ -PTA) when n (resp. m) is a priori not fixed.

The *size* of \mathcal{A} is defined as

$$|\mathcal{A}| = |Q| + |\Omega| + |P| + |R| + \sum_{(q,g,U,q') \in R} |g|.$$

Let $\text{Consts}(\mathcal{A}) = \{c \in \mathbb{N} \mid \exists (q, g, U, q') \in R, \exists \omega \in \Omega : g = \omega \bowtie c\}$ denote the set of constants that appear in the guards of the rules of \mathcal{A} .

By $\text{Conf}(\mathcal{A}) = Q \times \mathbb{N}^{\Omega}$ we denote the set of *configurations* of \mathcal{A} . We prefer however to denote a configuration by $q(v)$ instead of (q, v) .

Definition 1 For each parameter valuation $\mu : P \rightarrow \mathbb{N}$ and each $(\delta, t) \in R \times \mathbb{N}$ with $\delta = (q, g, U, q') \in R$, let $\xrightarrow{\delta, t, \mu}$ denote the binary relation $\text{Conf}(\mathcal{A})$, where $q(v) \xrightarrow{\delta, t, \mu} q'(v')$ if $v + t \models_{\mu} g$, $v'(u) = 0$ for all $u \in U$ and $v'(\omega) = v(\omega) + t$ for all $\omega \in \Omega \setminus U$.

A μ -run from $q_0(v_0)$ to $q_n(v_n)$ is a sequence $q_0(v_0) \xrightarrow{\delta_1, t_1, \mu} q_1(v_1) \cdots \xrightarrow{\delta_n, t_n, \mu} q_n(v_n)$; it is called *reset-free* if the set appearing in the third component is empty for all δ_i . We sometimes use the abbreviation $q(v) \xrightarrow{\mu}^* q'(v')$ to denote a μ -run of arbitrary length from $q(v)$ to $q'(v')$.

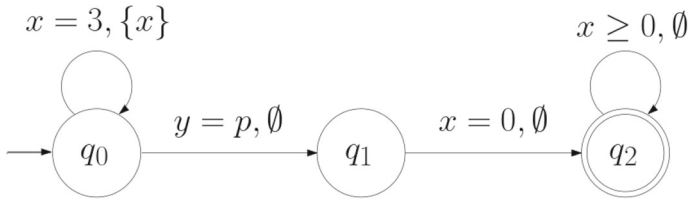


Fig. 1 An example of a PTA. The automaton consists of three states, the set of clocks is $\{x, y\}$, the set of parameters is $\{p\}$. The edges are represented by arrows labeled with the corresponding guard and the set of clocks U to be reset. A parameter valuation μ witnesses that reachability holds for this PTA, and only, if and only if, $\mu(p) \in 3\mathbb{Z}$

In case $P = \{p\}$ is a singleton and $\mu(p) = N$ we prefer to say N -run instead of μ -run. We say *reachability holds* for \mathcal{A} if there is a μ -run from $q_{init}(\vec{0})$ to some configuration $q(v)$ for some $q \in F$, some $v \in \mathbb{N}^\Omega$, and some $\mu \in \mathbb{N}^P$. We refer to Fig. 1 for an instance of a PTA for which reachability holds.

In the particular case where $P = \{p\}$ is a singleton for some parameter p and $\mu(p) = N$ we prefer to write $q(v) \xrightarrow{N} q'(v')$ (resp. $q(v) \xrightarrow{N^*} q'(v')$) to denote $q(v) \xrightarrow{\mu} q'(v')$ (resp. $q(v) \xrightarrow{\mu^*} q'(v')$) and prefer to write \models_N to denote \models_μ .

It is worth mentioning that there are further modes of time valuations and guards which exist in the literature, we refer to [4] for a recent overview.

We are interested in the following decision problem.

(m, n) -PTA-REACHABILITY

INPUT: A (m, n) -PTA \mathcal{A} .

QUESTION: Does reachability hold for \mathcal{A} ?

Alur et al. have already shown in their seminal paper that PTA-REACHABILITY is in general undecidable, already in presence of only three parametric clocks [3], Beneš et al. strengthened this when only one parameter is present [8].

Theorem 2 ([8]) $(3, 1)$ -PTA-REACHABILITY is undecidable.

To the contrary, $(1, *)$ -PTA-REACHABILITY has recently been shown to be complete for NEXP, where a non-elementary upper bound was initially given by Alur et al. [3].

Theorem 3 ([8–10]) $(1, *)$ -PTA-REACHABILITY is NEXP-complete.

On the other end, decidability of $(2, *)$ -PTA-REACHABILITY is still open to the best of our knowledge. In presence of one parameter the following is known.

Theorem 4 ([10]) $(2, 1)$ -PTA-REACHABILITY is decidable and PSPACE^{NEXP}-hard.

The following theorem states our main result.

Theorem 5 $(2, 1)$ -PTA-REACHABILITY is EXPSPACE-complete.

3 Lower Bounds

In this section we show an EXPSPACE lower bound for $(2, 1)$ -PTA-REACHABILITY. Section 3.1 introduces some auxiliary gadgets that show that on small $(2, 1)$ -PTA with two parametric clocks x and y and one parameter p one can perform both (i) PSPACE computations and (ii) compute $x - y \pmod p$ modulo numbers that are dynamically given in binary. Section 3.2 builds upon these auxiliary gadgets and shows how to implement serializability computations in a leaf language characterization of EXPSPACE [13], which is a simple padded variant of the leaf language characterization of PSPACE due to Hertrampf et al. [20].

3.1 PSPACE and Modulo Computations

For each $i, n \in \mathbb{N}$ let $\text{BIT}_i(n)$ denote the i -th least significant bit of the binary presentation of n , where the least significant bit is on the left, i.e. $n = \sum_{i \in \mathbb{N}} 2^i \cdot \text{BIT}_i(n)$. For each $m \geq 1$, by $\text{BIN}_m(n) = \text{BIT}_0(n) \cdots \text{BIT}_{m-1}(n)$ we denote the sequence of the first m least significant bits of the binary representation of n . Conversely, given a binary string $w = w_0 \cdots w_{m-1} \in \{0, 1\}^m$ of length m we denote by $\text{VAL}(w) = \sum_{i=0}^{m-1} 2^i \cdot w_i \in [0, 2^m - 1]$ the value of w interpreted as a non-negative integer.

Let \mathcal{A} be a parametric timed automaton over a set of clocks Ω with two parametric clocks x and y . We say a valuation $v : \Omega \rightarrow \mathbb{N}$ is *bit-compatible* if $v(\zeta) \in \{0, 1\}$ for all non-parametric clocks $\zeta \in \Omega$ of \mathcal{A} . Assume moreover that Ω contains *non-parametric clocks* $\Theta_+ \cup \Theta_-$, where Θ is some set and $\Theta_+ = \{\vartheta^+ \mid \vartheta \in \Theta\}$ and $\Theta_- = \{\vartheta^- \mid \vartheta \in \Theta\}$ are two disjoint corresponding copies of Θ ; in this case, for any valuation $v : \Omega \rightarrow \mathbb{N}$ we define the mapping $\widehat{v} : \Theta \rightarrow \{0, 1\}$ as

$$\widehat{v}(\vartheta) = \begin{cases} 0 & \text{if } v(\vartheta^+) = v(\vartheta^-) \\ 1 & \text{otherwise.} \end{cases}$$

In the following we call such non-parametric clocks $\{\vartheta^+, \vartheta^- \mid \vartheta \in \Theta\}$, appearing as implicit pairs, *bit clocks* since they are used to encode bits. The following definition expresses when a parametric timed automaton over two parametric clocks and one parameter computes a function from $\mathbb{N} \times \{0, 1\}^n$ to $\{0, 1\}^m$. Notably, both before execution it assumes, and after execution it guarantees, a bit-compatible valuation that assigns its two parametric clocks values in the interval $[0, N - 1]$, where N denotes the assigned value of its only parameter. In the following definition, it is important to note that the involved clocks are not (and in fact must not) assumed to be initially set to zero.

Definition 6 A $(2, 1)$ -PTA $\mathcal{A} = (Q, \Omega, \{p\}, R, q_{init}, \{q_{fin}\})$ whose parametric clocks are x and y and whose one parameter is p computes a function $f : \mathbb{N} \times \{0, 1\}^n \rightarrow \{0, 1\}^m$ if its set of clocks Ω contains two disjoint sets of

- non-parametric “input” bit clocks $\{in_0^+, in_0^-, \dots, in_{n-1}^+, in_{n-1}^-\}$,
- non-parametric “output” bit clocks $\{out_0^+, out_0^-, \dots, out_{m-1}^+, out_{m-1}^-\}$,

such that for all $N \in \mathbb{N}$ and all bit-compatible $v_0 : \Omega \rightarrow [0, N - 1]$ we have

1. $q_{init}(v_0) \xrightarrow{N^*} q_{fin}(v')$ for some bit-compatible $v' : \Omega \rightarrow [0, N - 1]$ and
2. for all $v' : \Omega \rightarrow \mathbb{N}$ for which $q_{init}(v_0) \xrightarrow{N^*} q_{fin}(v')$ we have
 - $v' \in [0, N - 1]^\Omega$ is bit-compatible,
 - $\widehat{v'}(in_i) = \widehat{v_0}(in_i)$ for all $i \in [0, n - 1]$,
 - $v'(x) - v'(y) \equiv v_0(x) - v_0(y) \pmod N$, and
 - $\prod_{j=0}^{m-1} \widehat{v'}(out_j) = f(v_0(x) - v_0(y) \pmod N, \prod_{i=0}^{n-1} \widehat{v_0}(in_i))$, where \prod denotes concatenation.

Importantly, the execution of any N -run $q_{init}(v_0) \xrightarrow{N} q_{fin}(v')$ does not have any side effects on the binary interpretation of the “input” bit clocks, i.e. the string $\prod_{i=0}^{n-1} \widehat{v_0}(in_i)$ equals $\prod_{i=0}^{n-1} \widehat{v'}(in_i)$.

The following lemma essentially has its roots in the PSPACE-hardness proof for the emptiness problem for timed automata (without parameters) introduced by Alur and Dill [2], however constructed to satisfy the carefully chosen interface given by Definition 6.

Lemma 7 *For every PSPACE-computable function $g : \{0, 1\}^n \rightarrow \{0, 1\}^m$ one can compute in polynomial time in $n + m$ a $(2, 1)$ -PTA computing the function $f : \mathbb{N} \times \{0, 1\}^n \rightarrow \{0, 1\}^m$, where $f(k, w) = g(w)$ for all $(k, w) \in \mathbb{N} \times \{0, 1\}^n$.*

Proof Let us fix some PSPACE-computable function $g : \{0, 1\}^n \rightarrow \{0, 1\}^m$. Let us moreover fix some $t(n)$ -space bounded deterministic Turing machine \mathcal{M} computing g , where t is some fixed polynomial.

We explicitly store the value of our input by making use of our non-parametric “input” bit clocks $\{in_0^+, in_0^-, \dots, in_{n-1}^+, in_{n-1}^-\}$. Similarly, we explicitly store the value of our output with the non-parametric “output” bit clocks $\{out_0^+, out_0^-, \dots, out_{m-1}^+, out_{m-1}^-\}$. Since $f(k, w) = g(w)$ we need to provide a computation that presents $g(w) \in \{0, 1\}^m$ using the “output” bit clocks. Let Ω denote the set of clocks of the $(2, 1)$ -PTA \mathcal{A} whose construction we discuss next.

For every non-parametric clock in \mathcal{A} we reset it once it has value 2; this is achieved by suitable self-loops in every state of the construction except for the final control state q_{fin} . Similarly, we establish that both of the parametric clocks x and y are being reset once they have reached value N . This way the difference between the values of x and y will stay unchanged modulo the valuation N of the only parameter p . Importantly, other than that neither x nor y will be modified during the following construction.

We will enforce that finally the values of all non-parametric clocks remain in $\{0, 1\}$ and that the two parametric clocks have a value in $[0, N - 1]$ as follows. A final

control state q_{fin} is preceded by a final gadget in which no time elapses that verifies via a sequence of suitable guards that the parametric and non-parametric clocks are as required.

Let us consider now any pair of bit clocks ϑ^+ and ϑ^- and any current bit-compatible valuation $v : \Omega \rightarrow \mathbb{N}$. We have $\widehat{v}(\vartheta) = 1$ if, and only if, either $v(\vartheta^+) = 0$ and $v(\vartheta^-) = 1$ or conversely $v(\vartheta^+) = 1$ and $v(\vartheta^-) = 0$. Similarly, when we want to set the value $\widehat{v}(\vartheta)$ to 0, we reset both clocks ϑ^+ and ϑ^- at the same time, and when we want to set the value $\widehat{v}(\vartheta)$ to 1, we reset ϑ^- when $v(\vartheta^+) = 1$ without resetting ϑ^+ .

For simulating \mathcal{M} our (2, 1)-PTA \mathcal{A} will also use suitable $O(t(n))$ bit clocks, to store in binary the working tape of \mathcal{M} .

Given the current bit-compatible valuation $v : \Omega \rightarrow \mathbb{N}$, it is thus possible to inspect the input bit string $\prod_{i=0}^{n-1} \widehat{v}(in_i)$, read and write the polynomially sized working tape, and to write the output $\prod_{j=0}^{m-1} \widehat{v}(out_j)$. Let us discuss this in more detail.

For simulating \mathcal{M} , we choose the control states of our (2, 1)-PTA \mathcal{A} as

$$S \times \{0, \dots, n - 1\} \times \{0, \dots, m - 1\} \times \{0, \dots, t(n) - 1\} \times \{0, 1\} \times \{0, 1\},$$

where S is the set of states of \mathcal{M} . We then simulate any step of \mathcal{M} from a state q , current position i on the input tape, current position j on the output tape, current position h on the working tape, reading letter a on the input tape, reading letter b on the working tape, changing to a new state q' , new input head position i' , new output head position j' , and new working head position h' . To do that, we add to \mathcal{A} sequences of suitable rules from control state (q, i, j, h, a, b) to control state (q', i', j', h', a', b') for all $a', b' \in \{0, 1\}$, by using suitable guards and reset operations that serve two purposes: first, checking whether a' and b' are indeed the values of the i' -th (resp. h' -th) cell of the input (resp. working) tape and second, writing on the j -th (resp. h -th) cell of the output (resp. working) tape.

Letting q_{init} denote some suitable initial state one can thus achieve that for all bit-compatible $v_0 : \Omega \rightarrow [0, N - 1]$ and all $v' : \Omega \rightarrow \mathbb{N}$, if $q_0(v_0) \xrightarrow{N^*} q_{fin}(v')$ then v' is again a bit-compatible valuation from Ω to $[0, N - 1]$. □

Remark 8 The proof of Lemma 7 shows that if $g : \mathbb{N} \times \{0, 1\}^n \rightarrow \{0, 1\}^m$ is computable by a (2, 1)-PTA, then so is the function $f : \mathbb{N} \times \{0, 1\}^{n+\ell} \rightarrow \{0, 1\}^m$, where $f(k, w) = g(k, w_1 \cdots w_n)$ for all $k \in \mathbb{N}$ and all $w = w_1 \cdots w_{n+\ell} \in \{0, 1\}^{n+\ell}$: indeed, one can manipulate the 2ℓ additional input bit clocks by repeatedly resetting them once they have value 2, enforcing that the associated \widehat{v} -values stay throughout unchanged and that their value is finally strictly smaller than 2.

The following lemma shows that (2, 1)-PTA can compute modulo dynamically given numbers in binary.

Lemma 9 *One can compute in polynomial time in $n + m$ a $(2, 1)$ -PTA that computes the function $f : \mathbb{N} \times \{0, 1\}^n \rightarrow \{0, 1\}^m$, where $f(k, w) = \text{BIN}_m(k \bmod \text{VAL}(w))$.*

Proof We need to show that in time polynomial in $n + m$ one can construct a $(2, 1)$ -PTA \mathcal{A} whose set of clocks Ω contains the “input” bit clocks $\{in_0^+, in_0^-, \dots, in_{n-1}^+, in_{n-1}^-\}$ and “output” bit clocks $\{out_0^+, out_0^-, \dots, out_{m-1}^+, out_{m-1}^-\}$ that computes f . Let us assume some parameter value $N \in \mathbb{N}$ and some bit-compatible valuation $v_0 : \Omega \rightarrow [0, N - 1]$ satisfying $w = \prod_{i=0}^{n-1} \widehat{v}_0(in_i)$.

Again, we establish here also that the parametric clocks x and y are being reset once they have reached value N — however we sometimes explicitly disallow x to reach value N in certain gadgets mentioned below. This will be the only modification of x and y . In the following, reading and writing the $\widehat{v}(\vartheta)$ -value for every pair of bit clocks ϑ^+, ϑ^- , guaranteeing that $v(\vartheta^+), v(\vartheta^-) \in \{0, 1\}$, and guaranteeing that the parametric clocks finally have values in $[0, N - 1]$ can be done as in the proof of Lemma 7.

We need the eventual output bit string $\prod_{j=0}^{m-1} \widehat{v}(out_j)$ to be equal to $f(v_0(x) - v_0(y) \bmod N, w) = \text{BIN}_m((v_0(x) - v_0(y) \bmod N) \bmod \text{VAL}(w))$.

Our automaton starts in some initial control state q_{init} . From q_{init} we introduce a gadget that nondeterministically writes some value $u \in \{0, 1\}^m$ in our “output” bit clocks that satisfies $\text{VAL}(u) < \text{VAL}(w)$. From the end of the latter gadget we have a rule that checks if our parametric clock x has value 0 (just after being reset with value N), leading to a control state q_{wait} . Assume our current valuation then is $v : \Omega \rightarrow \mathbb{N}$. From q_{wait} we have a rule to a state q_{sub} letting no time elapse from which we claim there is a gadget that allows us to loop in q_{sub} for precisely $\text{VAL}(w) = \text{VAL}(\prod_{i=0}^{n-1} \widehat{v}(in_i))$ time units. One constructs the latter gadget as follows. Subsequently for every $i \in [0, n - 1]$ one reads $\widehat{v}(in_i)$ and in case $\widehat{v}(in_i) = 1$ lets precisely 2^i time units elapse via a suitable auxiliary clock and in case $\widehat{v}(in_i) = 0$ lets 0 time units elapse. The gadget ends with a sequence of rules leading back to q_{sub} by letting 0 time units elapse that verify that the parametric clock x has a value strictly smaller than N . Importantly, the parametric clock x is exceptionally *not reset* inside this gadget.

Finally, we add a rule from q_{sub} to a suitable gadget that lets precisely $\text{VAL}(\prod_{j=0}^{m-1} \widehat{v}(out_j))$ time units elapse (analogously as done above), followed by a test that verifies that the value of y equals 0 (after just being reset at value N). In addition, we append this latter gadget with a final sequence of rules (again letting no time elapse) to our final control state q_{fin} that test if both x and y have a value strictly smaller than N and test if all non-parametric clocks have a value strictly smaller than 2. Thus, every valuation $v' : \Omega \rightarrow \mathbb{N}$ for which $q_{init}(v_0) \xrightarrow{N^*} q_{fin}(v')$ holds is a bit-compatible valuation from Ω to $[0, N - 1]$.

It is worth noting that by construction precisely $v_0(x) - v_0(y) \bmod N$ time units have passed in any computation q_{wait} to q_{fin} . Since we have repeatedly waited

VAL(w) time units and finally verified that the remaining time is the guessed value initially nondeterministically written to our “output” bit clocks, we have

$$\prod_{j=0}^{m-1} \widehat{v}'(out_j) = \text{BIN}_m((v_0(x) - v_0(y) \bmod N) \bmod \prod_{i=0}^{n-1} \widehat{v}_0(in_i))$$

$$= f(v_0(x) - v_0(y) \bmod N, \prod_{i=0}^{n-1} \widehat{v}_0(in_i))$$

for any valuation $v' : \Omega \rightarrow \mathbb{N}$ with $q_{init}(v_0) \xrightarrow{N^*} q_{fin}(v')$, as required. □

3.2 An EXPSPACE Lower Bound via serializability

This section is devoted to showing the following lower bound.

Theorem 10 (2, 1)-PTA-REACHABILITY is EXPSPACE-hard.

For each language $L \subseteq A^*$ let $\chi_L : A^* \rightarrow \{0, 1\}$ denote its characteristic function. By \leq_n we denote the lexicographic order on n -bit strings, thus $w \leq_n v$ if $\text{VAL}(w) \leq \text{VAL}(v)$, e.g. $0101 \leq_4 0011$.

Our EXPSPACE lower bound proof makes use of the following leaf language view of EXPSPACE from [13], which is a padded adjustment of the leaf-language characterization of PSPACE from [20], which in turn has its roots in Barrington’s Theorem [7].

Theorem 11 (Theorem 2 in [13]) For every language $L \subseteq \{0, 1\}^*$ in EXPSPACE there exists a polynomial $s : \mathbb{N} \rightarrow \mathbb{N}$, a regular language $\Lambda \subseteq \{0, 1\}^*$, and a language $K \in \text{LOGSPACE}$ such that for all $w \in \{0, 1\}^n$ we have

$$w \in L \iff \prod_{m=0}^{2^{2s(n)}-1} \chi_K(w \cdot \text{BIN}_{2s(n)}(m)) \in \Lambda, \tag{1}$$

where \cdot and \prod denote string concatenation.

Let us fix any language L in EXPSPACE and assume $L \subseteq \{0, 1\}^*$ without loss of generality. Applying Theorem 11, let us fix the regular language $\Lambda \subseteq \{0, 1\}^*$ along with some fixed deterministic finite automaton $D = (Q_D, \{0, 1\}, q_0, \delta_D, F_D)$ with $L(D) = \Lambda$, the fixed polynomial s and the fixed language $K \in \text{LOGSPACE}$. Let us moreover fix an input $w \in \{0, 1\}^n$ of length n for L . Figure 2 rephrases characterization (1) in Theorem 11 in terms of an execution of a program that returns 1 if, and only if, $w \in L$.

Making use of D , s and K we will translate our input $w \in \{0, 1\}^n$ in polynomial time (in $|w| = n$) to some (2, 1)-PTA $\mathcal{A} = (Q, \Omega, P, R, q_{init}, F)$ such that

- Ω will contain precisely two parametric clocks x and y and further clocks that are non-parametric,

```

(1)   var  $q \in Q_D$ 
(2)   var  $b \in \{0, 1\}$ 
(3)   var  $B \in \mathbb{N}$ 
(4)    $q := q_0$ 
(5)    $B := 0$ 
(6)   while  $B < 2^{2^n}$  loop
(7)        $b := \chi_K(w \cdot \text{BIN}_{2^{s(n)}}(B))$ 
(8)        $q := \delta_D(q, b)$ 
(9)        $B := B + 1$ 
(10)  end loop
(11)  return  $q \in F_D$ 
    
```

Fig. 2 A program returning 1 if, and only if, $w \in L$ (using the characterization in Theorem 11), where $D = (Q_D, \{0, 1\}, q_0, \delta_D, F_D)$ is some deterministic finite automaton such that $L(D) = \Lambda$

- $P = \{p\}$ is a singleton, and
- $w \in L$ if, and only, if reachability holds for \mathcal{A} .

The following lemma gives us a gadget $(2, 1)$ -PTA that allows us to enforce that the parameter p can only be evaluated to numbers that are larger than $2^{2^{s(n)}}$.

Lemma 12 *One can compute in polynomial time in n some parametric timed automaton $\mathcal{A}_{big} = (Q_{big}, \Omega_{big}, \{p\}, R_{big}, q_{big,init}, \{q_{big,fin}\})$ with two parametric clocks $x, y \in \Omega_{big}$ and one parameter p such that*

1. $q_{big,init}(\vec{0}) \xrightarrow{N^*} q_{big,fin}(v')$ for some $v' : \Omega_{big} \rightarrow \mathbb{N}$ for some $N \in \mathbb{N}$, and
2. for all $N \in \mathbb{N}$ and all $v' : \Omega_{big} \rightarrow \mathbb{N}$ we have $q_{big,init}(\vec{0}) \xrightarrow{N^*} q_{big,fin}(v')$ implies $N > 2^{2^{s(n)}}$.

Proof Without loss of generality we may assume $2^{s(n)+1} \geq 10$. Letting N denote the parameter value of its only parameter p , our $(2, 1)$ -PTA \mathcal{A}_{big} will test whether $N - 1$ is divisible by all numbers in the interval $[1, 2^{s(n)+1} - 1]$. This will be sufficient since $\text{LCM}([1, k]) \geq 2^k$ for all $k \geq 9$ by [22], thus implying $N > N - 1 \geq \text{LCM}([1, 2^{s(n)+1} - 1]) \geq 2^{2^{s(n)+1}-1} > 2^{2^{s(n)}}$. Consider the following program which returns 1 if, and only if, all numbers in $[1, 2^{s(n)+1} - 1]$ divide $N - 1$.

```

(1)  var  $I \in \{0, 1\}^{s(n)+1}$ 
(2)  var  $J \in \{0, 1\}^{s(n)+1}$ 
(3)   $I := 0^{s(n)+1}$ 
(4)  while  $I \neq 1^{s(n)+1}$  loop
(5)     $I := \text{BIN}_{s(n)+1}(\text{VAL}(I) + 1)$ 
(6)     $J := \text{BIN}_{s(n)+1}(N - 1 \bmod \text{VAL}(I))$ 
(7)    if  $J \neq 0^{s(n)+1}$  then return 0
(8)  end loop
(9)  return 1

```

It remains to show that the program can be implemented by a $(2, 1)$ -PTA \mathcal{A}_{big} with a suitable final control state $q_{big, fin}$.

It is straightforward to initialize our two parametric clocks x and y in such a way that one can enforce valuations v that satisfy $v(x) - v(y) = N - 1 \bmod N$: indeed, starting from the valuation $\vec{0}$, we can wait one unit of time after which we reset x but not y .

We will use $O(s(n))$ suitable bit clocks for storing the variables I and J respectively.

Lines (3), (4) and (7) can easily directly be achieved by reading and writing the $O(s(n))$ many bits clocks reserved for storing I and J . Line (5) boils down to incrementing I when viewed as $s(n) + 1$ bit integer and is thus obviously a polynomial space computable function from $\mathbb{N} \times \{0, 1\}^{s(n)}$ to $\{0, 1\}^{s(n)}$ and hence computable using a suitable PTA based on Lemma 7. Line (6) is a function from $\mathbb{N} \times \{0, 1\}^{s(n)+1}$ to $\{0, 1\}^{s(n)+1}$ that can be implemented using a suitable PTA based on Lemma 9.

As in the proofs of Lemmas 7 and 9 we reset the two parametric clocks x and y once they have reached value N but only in case we are outside any of the gadget PTA corresponding to line (5) and line (6), respectively. Similarly we realize the implementation of the bit clocks for I and J by resetting them once they have reached value 2. □

Recall that we aim at implementing the program in Fig. 2 by a $(2, 1)$ -PTA \mathcal{A} . The initial part of \mathcal{A} will consist of the gadget PTA \mathcal{A}_{big} which will allow us to enforce an assignment of p to some value $N > 2^{2^{s(n)}}$. We first explain how to encode its variables and then discuss how to implement the different lines of the program.

Encoding the Variables of the Program in Fig. 2 Our PTA \mathcal{A} will store in its control states the current state q of D and the boolean variable b . We cannot easily “explicitly” store the value of our variable B in binary as in the proof of Lemma 7 via polynomially many bit clocks in such a way that, given the current valuation $v : \Omega \rightarrow \mathbb{N}$, it suffices to simply inspect their \widehat{v} -value: indeed, there are only singly-exponentially many different combinations of such \widehat{v} -values, yet B is a number in $[0, 2^{2^{s(n)}}]$ and thus of doubly-exponential magnitude. We will rather store the value $B \in \mathbb{N}$ as the difference $v(x) - v(y) \bmod N$ between our only two parametric clocks x and y : this is possible since $N > 2^{2^{s(n)}}$ by our initial gadget PTA \mathcal{A}_{big} . However, when inspecting line (7) of Fig. 2 we need to access certain bits of the exponentially

long bit string $w \cdot \text{BIN}_{2^{s(n)}}(B)$. For this, we access B in a different representation, namely in Chinese Remainder Representation that we introduce next.

Definition 13 (Chinese Remainder Representation) Let p_i denote the i -th prime number and assume $\prod_{i=1}^m p_i > B$ for some $m \in \mathbb{N}$. Then $\text{CRR}_m(B)$ denotes the bit tuple $(b_{i,r})_{i \in [1,m], r \in [0, p_i-1]}$, where $b_{i,r} = 1$ if $B \bmod p_i = r$ and $b_{i,r} = 0$ otherwise.

Since B will need to take values in $[0, 2^{2^{s(n)}}]$ and for every $k \in \mathbb{N}$ we have $\prod_{i=1}^k p_i > 2^k$ there exists some $m \in O(\log(2^{2^{s(n)}})) = 2^{\text{poly}(n)}$ such that $\prod_{i=1}^m p_i > B$. In other words, one can present B as

$$\text{CRR}_m(B) = (b_{i,r})_{i \in [1,m], r \in [0, p_i-1]} \quad \text{for some } m \in 2^{\text{poly}(n)} \quad (2)$$

Since by the Prime Number Theorem the i -th prime p_i is bounded by $O(i \log i)$ there exists some $\ell \in O(\log(m \log m)) = O(\log(2^{\text{poly}(s(n))})) = \text{poly}(n)$ such that ℓ bits are sufficient to store in *binary* precisely one of the primes p_i . Thus, similarly $O(\ell) = \text{poly}(n)$ bits are sufficient to store in *binary* precisely one of the pairs of the form (i, r) , where $i \in [1, m]$ and $r \in [0, p_i - 1]$. Moreover we have $|\text{CRR}(B)| \in O(m^2 \log m) = 2^{\text{poly}(s(n))} = 2^{\text{poly}(n)}$.

Observe that in line (7) of our program in Fig. 2 we need to carry out LOGSPACE computations on our exponentially long string $w \cdot \text{BIN}_{2^{s(n)}}(B)$. Yet, if at all, we only have an on-the-fly mechanism for accessing the Chinese Remainder Representation of B , notably still of exponential size in n . To have a chance to access concrete bits of B , we apply the following theorem that states that, given a number in Chinese Remainder Representation, one can compute in LOGSPACE its binary representation.

Theorem 14 (Theorem 3.3. in [11]) *The following problem is computable in DLOGTIME-uniform NC¹ (and thus in LOGSPACE):*

INPUT: $\text{CRR}_m(B)$ and $j \in [1, m]$
OUTPUT: $\text{BIT}_j(B \bmod 2^m)$

Realization of line (7) in the Program in Fig. 2 Let us assume that we have $B < 2^{2^{s(n)}}$ and recall that we have stored B as the difference $v(x) - v(y) \bmod N$ of our two parametric clocks x and y , assuming v to be our current clock valuation. Let us show how to compute $\chi_K(w \cdot \text{BIN}_{2^{s(n)}}(B))$, where we recall that K is a language in LOGSPACE. Let us fix some logarithmically space bounded deterministic Turing machine \mathcal{M} for K .

For simulating \mathcal{M} our PTA \mathcal{A} will use $O(\log(n + 2^{s(n)})) = \text{poly}(n)$ auxiliary bit clocks \mathcal{J} to store in binary the position of the input head of \mathcal{M} and further $O(\log(n + 2^{s(n)})) = \text{poly}(n)$ auxiliary bit clocks \mathcal{W} in order to store the working tape \mathcal{M} . Reading and writing on the working tape as well as updating the position of the input head can done analogously as in the proof of Lemma 7. It only remains to show how

to access the cell content $\text{BIT}_j(w \cdot \text{BIN}_{2^s(n)}(B))$ of the input head of \mathcal{M} , where we recall that j itself is stored inside the above-mentioned bit clocks \mathcal{J} .

To compute $\text{BIT}_j(w \cdot \text{BIN}_{2^s(n)}(B))$ we apply Theorem 14 and simulate in turn a LOGSPACE machine \mathcal{M}' whose input is assumed to be

$$\text{CRR}(B) = (b_{i,r})_{i \in [1,m], r \in [0, p_i - 1]} \text{ and } j \in [1, m],$$

where we already have direct access to j via the bit clocks \mathcal{J} but need a special treatment in order to access the $b_{i,r}$ of $\text{CRR}(B)$. Importantly, during the to-be discussed simulation of \mathcal{M}' we never modify the \hat{v} -values associated with the bit clocks in \mathcal{J} and \mathcal{W} that are being used in the (outermost) simulation of \mathcal{M} . Before discussing the access to the $b_{i,r}$ let us first discuss the simulation of the working tape of \mathcal{M}' : this can be achieved by using $O(\log(|\text{CRR}(B)| + s(n))) = O(\log(m^2 \cdot \log m + s(n))) = \text{poly}(n)$ many auxiliary bit clocks \mathcal{W}' , say, where reading and writing the working tape is done again as in Lemma 7. It remains to discuss how to implement the input head in the simulation of \mathcal{M}' . As mentioned repeatedly above, input j can directly be accessed by the bit clocks \mathcal{J} . However, accessing $\text{CRR}(B) = (b_{i,r})_{i \in [1,m], r \in [0, p_i - 1]}$ cannot be done explicitly but on-the-fly: for this we reserve $O(\ell) = O(s(n)) = \text{poly}(n)$ additional auxiliary bit clocks \mathcal{J}' , say, to store in binary a pair of indices (i, r) , where $i \in [1, m]$ and $r \in [0, p_i - 1]$. Given the binary access to (i, r) via the bit clocks \mathcal{J}' , one can compute via further suitable $\text{poly}(\ell) = O(s(n)) = \text{poly}(n)$ bit clocks \mathcal{H} , say, the binary representation of the i -th prime number p_i in space polynomial in ℓ (and thus in n) by Lemma 7: indeed, given $i \in [1, m]$ in binary, i.e. using $\ell = \text{poly}(n)$ bits, it is straightforward to compute the i -th prime in space polynomial in ℓ . Having a binary representation of p_i via the bit clocks \mathcal{H} one can finally compute $(v(x) - v(y) \bmod N) \bmod p_i$ via a gadget by Lemma 9. Our $(2, 1)$ -PTA \mathcal{A} can thus indeed compute $B \bmod p_i$ and thus decide if r equals the latter, which in turn is nothing but computing the to-be-computed input bit $b_{i,r}$ of $\text{CRR}(B)$ for the simulation of \mathcal{M}' .

Concerning the implementation details of the simulation of \mathcal{M}' it is important to remark (recalling Remark 8) that both during the sub-computation computing the i -th prime p_i (using Lemma 7) as well as during the sub-computation computing $B \bmod p_i$ (using Lemma 9) one can guarantee that the \hat{v} -values associated with the bit clocks in $\mathcal{J}, \mathcal{W}, \mathcal{J}'$ and \mathcal{W}' are never being modified.

Realization of the Remaining Lines of the Program in Fig. 2 Lines (4), (8) and (11) can be done directly by the control states of \mathcal{A} . Line (5) boils down to resetting both x and y simultaneously. Line (6) will be done by checking if for the second time ever (the first time was when $v(x) = v(y) = 0$) we have that $\text{BIN}_{2^s(n)}(B) = 0^{2^s(n)}$, which in turn can be done analogously (but in fact simpler) as our above-mentioned implementation of line (7). Line (9) is letting time elapse till the parametric clock y has value 1 (i.e. one time unit after it had value N and was reset), and then resetting it. The latter implementation indeed correctly implements incrementation modulo N .

4 From Two-Parametric Timed Automata with one Parameter to Parametric One-Counter Automata

Being introduced by Bundala and Ouaknine in [10], we define parametric one-counter automata. These are automata that can manipulate a counter that can be incremented or decremented, parametrically or not, compared against constants or parameters, and with divisibility tests modulo constants. It is worth mentioning that the notion of parametric one-counter automata from [10] is slightly more expressive than ours, as we shall discuss further below.

After introducing parametric one-counter automata we state a theorem (Theorem 16), proven essentially already in [10] — again, however for a slightly more expressive model of parametric one-counter automata — that states that (2, 1)-PTA-REACHABILITY can be reduced in exponential time to the reachability problem of parametric one-counter automata over one parameter. Since the actual proof of Theorem 16 follows the approach of Bundala and Ouaknine from [10], it can be found in the Appendix.

4.1 Parametric One-Counter Automata

Given a set of parameters P we denote by $\text{Op}(P)$ the *set of operations* over the set of parameters P , being of the form $\text{Op}(P) = \text{Op}_{\pm} \cup \text{Op}_{\pm P} \cup \text{Op}_{\text{mod } \mathbb{N}} \cup \text{Op}_{\triangleright \mathbb{N}} \cup \text{Op}_{\triangleright P}$, where

- $\text{Op}_{\pm} = \{-1, 0, +1\}$,
- $\text{Op}_{\pm P} = \{+p, -p \mid p \in P\}$,
- $\text{Op}_{\text{mod } \mathbb{N}} = \{\text{mod } c \mid c \in \mathbb{N}\}$,
- $\text{Op}_{\triangleright \mathbb{N}} = \{\triangleright c \mid \triangleright \in \{<, \leq, =, \geq, >\}, c \in \mathbb{N}\}$, and
- $\text{Op}_{\triangleright P} = \{\triangleright p \mid \triangleright \in \{<, \leq, =, \geq, >\}, p \in P\}$.

The *size* $|op|$ of an operation op is defined as

$$|op| = \begin{cases} \log(c) & \text{if } op = \text{mod } c \text{ or } op = \triangleright c \text{ with } c \in \mathbb{N} \\ 1 & \text{otherwise.} \end{cases}$$

We denote by *updates* those operations that lie in $\text{Op}_{\pm} \cup \text{Op}_{\pm P}$ and by *tests* those operations that lie in $\text{Op}_{\text{mod } \mathbb{N}} \cup \text{Op}_{\triangleright \mathbb{N}} \cup \text{Op}_{\triangleright P}$. Previously, such as in [10] or [16], slightly different sets of operations have been used, such as operations to increment the counter by a constant represented in binary. Moreover, Bundala and Ouaknine [10] include for the purpose of their construction some operations of the form $+[0, p]$ that allow to nondeterministically add to the counter a value that lies in $[0, \mu(p)]$, where $\mu(p)$ is the parameter valuation of parameter p . As we shall show in this section, when reducing the reachability problem for parametric timed automata with two parametric clocks and one parameter to parametric one-counter automata one does not require these $+[0, p]$ -transitions.

A *parametric one-counter automaton* (POCA for short) is a tuple

$$\mathcal{C} = (Q, P, R, q_{init}, F),$$

where

- Q is a non-empty finite set of control states,
- P is a non-empty finite set of parameters that can take non-negative integer values,
- $R \subseteq Q \times \text{Op}(P) \times Q$ is a finite set of rules,
- q_{init} is an initial control state, and
- $F \subseteq Q$ is a set of final control states.

The size of \mathcal{C} is defined as

$$|\mathcal{C}| = |Q| + |P| + |R| + \sum_{(q,op,q') \in R} |op|.$$

Let $\text{Consts}(\mathcal{C})$ denote the constants that appear in the operations $op \in \text{Op}_{\text{mod } \mathbb{N}} \cup \text{Op}_{\bowtie \mathbb{N}}$ for some operation (q, op, q') in R . By $\text{Conf}(\mathcal{C}) = Q \times \mathbb{Z}$ we denote the set of configurations of \mathcal{C} . We prefer however to denote a configuration of $\text{Conf}(\mathcal{C})$ by $q(z)$ instead of (q, z) .

Being slightly non-standard we define configurations to take counter values over \mathbb{Z} rather than over \mathbb{N} for notational convenience. This does not cause any loss of generality as we allow guards that enable us to test if the value of the counter is greater or equal to zero.

Definition 15 (transition) For every $op \in \text{Op}(P)$, for every parameter valuation $\mu : P \rightarrow \mathbb{N}$, for every POCA \mathcal{C} , and for every two configurations $q(z)$ and $q'(z')$ in $\text{Conf}(\mathcal{C})$ we define the transition $q(z) \xrightarrow{op, \mu} q'(z')$ if there exists some $(q, op, q') \in R$ such that either of the following holds

1. $op = c \in \text{Op}_{\pm}$ and $z' = z + c$,
2. $op \in \text{Op}_{\pm P}$, and either
 - $op = +p$ and $z' = z + \mu(p)$, or
 - $op = -p$ and $z' = z - \mu(p)$.
3. $op = \text{mod } c \in \text{Op}_{\text{mod } \mathbb{N}}$, $z = z'$ and $z' \equiv 0 \pmod{c}$,
4. $op = \bowtie c \in \text{Op}_{\bowtie \mathbb{N}}$, $z = z'$ and $z' \bowtie c$, and
5. $op = \bowtie p \in \text{Op}_{\bowtie P}$, $z = z'$ and $z' \bowtie \mu(p)$.

Let $\mu : P \rightarrow \mathbb{N}$ be a parameter valuation. A μ -run in \mathcal{C} (from $q_0(z_0)$ to $q_n(z_n)$) is a sequence, possibly empty (i.e. $n = 0$), of the form

$$\pi = q_0(z_0) \xrightarrow{op_0, \mu} q_1(z_1) \dots \xrightarrow{op_{n-1}, \mu} q_n(z_n) \dots$$

We sometimes use the abbreviation $q(z) \xrightarrow{\mu}^* q'(z')$ to denote a μ -run of arbitrary length from $q(z)$ to $q'(z')$.

We say π is accepting if $q_0 = q_{init}$, $z_0 = 0$, and $q_n \in F$. We say reachability holds for the POCA \mathcal{C} if there exists an accepting μ -run for some $\mu \in \mathbb{N}^P$. We refer to Fig. 3 for an instance of a POCA for which reachability holds. For any two $c, d \in [0, n]$ we define the subrun $\pi[c, d]$ from $q_c(z_c)$ to $q_d(z_d)$ as the μ -run $q_c(z_c) \xrightarrow{\pi_c, \mu}$

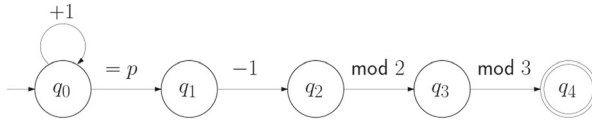


Fig. 3 An example of a POCA. The automaton consists of four states and the set of parameters is $\{p\}$. The edges are represented by arrows labeled with the corresponding operations. A parameter valuation $\mu : \{p\} \rightarrow \mathbb{N}$ witnesses that reachability holds for the above POCA if, and only, if $\mu(p) \equiv 5 \pmod 6$

$q_{c+1}(z_{c+1}) \cdots \xrightarrow{\pi_{d-1} \cdot \mu} q_d(z_d)$. As expected, a *prefix* (resp. *suffix*) of π is a μ -run of the form $\pi[0, c]$ (resp. $\pi[d, n]$).

We define the *concatenation* $\pi_1\pi_2$ of two μ -runs π_1 and π_2 when the source configuration of π_2 is equal to the target configuration of π_1 as expected.

We define $\Delta(\pi) = z_n - z_0$ as the *counter effect* of the run π and for each $i \in [0, n - 1]$ let $\Delta(\pi, i) = \Delta(\pi[i, i + 1])$ to denote the counter effect of the i -th transition of π . Its *length* is defined as $|\pi| = n$.

In the particular case where $P = \{p\}$ is a singleton for some parameter p and $\mu(p) = N$, we prefer to write $q(z) \xrightarrow{op, N} q'(z')$ (resp. $q(z) \xrightarrow{op, N}^* q'(z')$) to denote $q(z) \xrightarrow{op, \mu} q'(z')$ (resp. $q(z) \xrightarrow{op, \mu}^* q'(z')$) and prefer to call a μ -run an N -run.

We define $\text{VALUES}(\pi) = \{z_i \mid i \in [0, n]\}$ to denote the *set of counter values* of the configurations of π . We define a run π 's *maximum* as $\max(\pi) = \max(\text{VALUES}(\pi))$ and the *minimum* as $\min(\pi) = \min(\text{VALUES}(\pi))$.

The following theorem states an exponential time reduction from (2, 1)-PTA-REACHABILITY to the reachability problem of particular parametric one-counter automata over one parameter.

Theorem 16 *The following is computable in exponential time:*

INPUT: A (2, 1)-PTA \mathcal{A} .

OUTPUT: A POCA \mathcal{C} over one parameter

such that

1. *for all $N \in \mathbb{N}$ all accepting N -runs π in \mathcal{C} satisfy $\text{VALUES}(\pi) \subseteq [0, 4 \cdot \max(N, |\mathcal{C}|)]$, and*
2. *reachability holds for \mathcal{A} if, and only if, reachability holds for \mathcal{C} .*

The proof of Theorem 16 can be found in Appendix A.

5 Upper Bounds

In this section we state the Small Parameter Theorem (Theorem 18) which tells us that for every POCA over one parameter and every sufficiently large parameter value N , accepting N -runs with counter values all in $[0, 4N]$ can be turned into accepting N' -runs for some smaller N' . After having stated the theorem we will show that together with Theorem 16 it implies an EXPSPACE upper bound for (2, 1)-PTA-REACHABILITY.

We provide an overview of the proof of the Small Parameter Theorem in Section 5.1, whose actual proof will stretch over Sections 6, 7, 8, and 9.

For each POCA $\mathcal{C} = (Q, P, R, q_{init}, F)$ we define the following constants:

$$\begin{aligned} Z_{\mathcal{C}} &= \text{LCM}(\text{Consts}(\mathcal{C})) \\ \Gamma_{\mathcal{C}} &= \text{LCM}(17 \cdot |Q|) \cdot Z_{\mathcal{C}} \\ \Upsilon_{\mathcal{C}} &= 17 \cdot |Q| \cdot \text{LCM}(17 \cdot |Q|) \cdot (17 \cdot |Q| \cdot Z_{\mathcal{C}} + 2) \\ M_{\mathcal{C}} &= 30 \cdot (\Upsilon_{\mathcal{C}} + \Gamma_{\mathcal{C}} + 1) \end{aligned}$$

Since for every non-empty finite set $U \subseteq \mathbb{N} \setminus \{0\}$ we have $\text{LCM}(U) \leq \max(U)^{|U|}$, the following lemma is straightforward.

Lemma 17 *The above constants are asymptotically bounded by $2^{\text{poly}(|\mathcal{C}|)}$.*

The main result of this section is the following theorem.

Theorem 18 (Small Parameter Theorem) *Let $\mathcal{C} = (Q, \{p\}, R, q_{init}, F)$ be a POCA with one parameter p . If there exists an accepting N -run in \mathcal{C} with values all in $[0, 4N]$ for some $N > M_{\mathcal{C}}$, then there exists an accepting $(N - \Gamma_{\mathcal{C}})$ -run in \mathcal{C} .*

Let us first establish that this theorem is enough to prove the desired EXPSPACE upper bound.

Corollary 19 $(2, 1)$ -PTA-REACHABILITY is in EXPSPACE.

Proof Given a $(2, 1)$ -PTA \mathcal{A} , we apply Theorem 16 and translate \mathcal{A} in exponential time into a POCA $\mathcal{C} = (Q, P, R, q_0, F)$ with $P = \{p\}$, such that

1. all accepting N -runs π in \mathcal{C} satisfy $\text{VALUES}(\pi) \subseteq [0, 4 \cdot \max(N, |\mathcal{C}|)]$, and
2. reachability holds for \mathcal{A} if, and only if, reachability holds for \mathcal{C} .

We first claim that if there exists an accepting N -run π for \mathcal{C} , then there exists one satisfying $N \in [0, \max\{M_{\mathcal{C}}, |\mathcal{C}|\}]$ and $\text{VAL}(\pi) \subseteq [0, 4 \cdot \max\{M_{\mathcal{C}}, |\mathcal{C}|\}]$. All accepting N -runs π of \mathcal{C} satisfy $\text{VAL}(\pi) \subseteq [0, 4 \cdot \max\{N, |\mathcal{C}|\}]$ by Point 1, so if $N > \max\{M_{\mathcal{C}}, |\mathcal{C}|\}$, then $4N = 4 \cdot \max\{N, |\mathcal{C}|\}$ and hence there exists some accepting $(N - \Gamma_{\mathcal{C}})$ -run for \mathcal{C} by Theorem 18. Remarking that in case $N > \max\{M_{\mathcal{C}}, |\mathcal{C}|\}$ we have $N - \Gamma_{\mathcal{C}} > M_{\mathcal{C}} - \Gamma_{\mathcal{C}} > 0$, one can repeat the above argument for $N - \Gamma_{\mathcal{C}}$ and possibly for $N - 2\Gamma_{\mathcal{C}}$ and so on, thus implying the desired existence.

Thus by Point 2 it suffices to check in exponential space in $|\mathcal{A}|$ whether there exists some accepting N -run π for \mathcal{C} satisfying $\text{VALUES}(\pi) \subseteq [0, 4N]$ for some $N \in [0, \max\{M_{\mathcal{C}}, |\mathcal{C}|\}]$. Since $M_{\mathcal{C}} \in 2^{\text{poly}(|\mathcal{C}|)} = 2^{2^{\text{poly}(|\mathcal{A}|)}}$, the latter is simply a reachability question in a doubly-exponentially large finite graph all of whose vertices and edges can be represented using exponentially many bits, and thus decidable in exponential space. □

5.1 Overview of the Proof of the Small Parameter Theorem

For the proof of the Small Parameter Theorem (Theorem 18) we proceed as follows.

- In Section 6 we introduce the notion of N -semiruns. These generalize N -runs in that only modulo tests need to hold, not however comparison tests. We define some natural operations on them, like shifting them by some value or cutting out certain infixes. In Section 6.2 we prove two important lemmas on semiruns that will serve as base tools for subsequent steps in the proof:
 - The Depumping Lemma (Lemma 22) will be our main tool to depump certain semiruns, in the following sense: in case the difference between the number of $+p$ -transitions and $-p$ -transitions is bounded for all infixes and equal to 0 for the whole semirun and furthermore the absolute counter effect of the semirun is sufficiently large, then one can build — by applying the above-mentioned operations — a new semirun whose absolute counter effect is slightly smaller.
 - The Bracket Lemma (Lemma 23) states that in case the counter effect is sufficiently large and the counter values are all in $[0, 4N]$, then one can find an infix where the counter effect is also large and moreover the difference between the number of $+p$ -transitions and $-p$ -transitions is bounded for all infixes and equal to 0 for the whole semirun.
- In Section 7 we introduce the notion of hills and valleys. Hills are N -semiruns that start and end in configurations with low counter values but where all intermediate configurations have counter values above the source and target configuration. We introduce the dual notion of valleys. The main contribution of the section is the following.
 - The Hill and Valley Lemma (Lemma 28) allows to transform N -semiruns that are hills (resp. valleys) into $(N - \Gamma_{\mathcal{C}})$ -semiruns with the same source and target configuration.
- Making use of all of the above lemmas, we introduce in Section 8 the following lemma, which is a main technical ingredient in the proof of Theorem 18.
 - The 5/6-Lemma (Lemma 39) states that N -semiruns with counter effect smaller than $5/6 \cdot N$ can be turned in into $(N - \Gamma_{\mathcal{C}})$ -semiruns.
- Finally, in Section 9 we prove the Small Parameter Theorem (Theorem 18) by carefully factorizing a potential N -run into subsemiruns that can be treated by the above lemmas.

In Fig. 4 we give an overview of the dependencies of the above-mentioned lemmas.

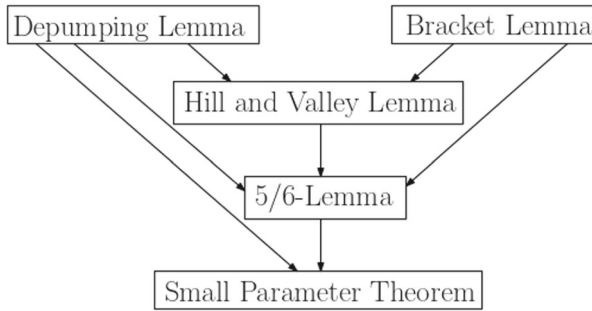


Fig. 4 Illustration of the dependencies between the lemmas. The presence of an arrow going from a lemma to another means that the lemma in question is used inside the proof of the lemma the arrow points to

6 Semiruns, Their Bracket Projection, and Embeddings

In this section we motivate and introduce the notion of semiruns by loosening the conditions on runs, and define basic operations on them. These basic operations possibly change their counter values, length, or counter effect.

The formalism of an N -run is a little bit too restrictive to define operations on them. For instance, subtracting Z_C from all counter values of an N -run produces an object, where conditions (1),(2), and (3) of Definition 15 indeed hold — as $Z_C = \text{LCM}(\text{Consts}(\mathcal{C}))$ — but where conditions (4) and (5) might not hold anymore, as comparison guards may be violated. Rather than certifying each time that the application of an operation preserves the property of being an N -run we prefer to loosen the definition in order to avoid tedious case distinctions. This motivates the notion of semitransitions (resp. semiruns), which are a generalization of transitions (resp. runs), in which the comparison tests need not hold.

We introduce semiruns and operations on them in Section 6.1. Section 6.2 introduces the bracket projection of semiruns, the Depumping Lemma (Lemma 22) and the Bracket Lemma (Lemma 23). Section 6.3 introduces the notion of embeddings, which provide a formal means to express when a semirun can structurally be found as a subsequence of another.

6.1 Semiruns and Operations on Them

Definition 20 (semitransition) Let $\mathcal{C} = (Q, P, R, q_{init}, F)$ be a POCA. For every operation $op \in \text{Op}(P)$, for every parameter valuation $\mu : P \rightarrow \mathbb{N}$, and for every two configurations $q(z)$ and $q'(z')$ in $\text{Conf}(\mathcal{C})$ we define the *semitransition*

$q(z) \xrightarrow{op, \mu} q'(z')$ if there exists some $(q, op, q') \in R$ such that conditions (1),(2), and (3) of Definition 15 hold but where conditions (4) and (5) are loosened by the following conditions (4') and (5') respectively

- (1) $op = c \in \text{Op}_{\pm}$, and $z' = z + c$,
- (2) $op \in \text{Op}_{\pm P}$ and either
 - $op = +p$ and $z' = z + \mu(p)$, or

- $op = -p$ and $z' = z - \mu(p)$.
- (3) $op = \text{mod } c \in \text{Op}_{\text{mod } \mathbb{N}}$, $z = z'$ and $z' \equiv 0 \pmod{c}$,
- (4') $op = \bowtie c \in \text{texts}f \text{Op}_{\bowtie \mathbb{N}}$ and $z = z'$, and
- (5') $op = \bowtie p \in \text{Op}_{\bowtie p}$, and $z = z'$.

Thus, in a nutshell, when writing $q(z) \xrightarrow{op, \mu} q'(z')$ we do not require that the comparison tests against parameters or against constants hold; however the updates and the modulo tests against constants must be respected. This naturally gives rise to the definition of μ -semiruns as expected. Note that in particular every μ -run is a μ -semirun. The abbreviation N -semirun, $q(z) \xrightarrow{op, N} q'(z')$, the counter effect Δ , VALUES, min, max, subsemirun, prefix, suffix are defined as for runs.

Note that in particular every N -run is an N -semirun. Importantly, note also that semitransitions involving comparison tests are still syntactically present in semiruns. By a careful analysis, one can therefore possibly perform operations on N -semiruns in order to show that they are in fact N -runs.

Example 21 The 2-semirun

$$\pi = q_0(0) \xrightarrow{+1,2} q_1(1) \xrightarrow{+1,2} q_1(2) \xrightarrow{+1,2} q_1(3) \xrightarrow{\leq p,2} q_2(3) \xrightarrow{\text{mod } 3,2} q_3(3)$$

is not a 2-run, as, in $q_1(3) \xrightarrow{\leq p,2} q_2(3)$, condition (4) of Definition 15 does not hold, however condition (4') of Definition 20 does.

Shifting and Gluing of Semiruns

Let us fix a POCA \mathcal{C} and some N -semirun

$$\pi = q_0(z_0) \xrightarrow{\pi_0, N} q_1(z_1) \cdots \xrightarrow{\pi_{n-2}, N} q_{n-1}(z_{n-1}) \xrightarrow{\pi_{n-1}, N} q_n(z_n).$$

We define the following operations, where we recall that $Z_{\mathcal{C}} = \text{LCM}(\text{Consts}(\mathcal{C}))$:

- For $D \in Z_{\mathcal{C}}\mathbb{Z}$, we define the *shifting* of π by D as

$$\pi + D = q_0(z_0 + D) \xrightarrow{\pi_0, N} q_1(z_1 + D) \cdots \xrightarrow{\pi_{n-1}, N} q_n(z_n + D).$$

Since there are no effective comparison tests and D is an integer that is divisible by all constants appearing in modulo tests in \mathcal{C} , it is clear that $\pi + D$ is again an N -semirun.

- For two configurations $q_i(z_i)$ and $q_j(z_j)$ with $0 \leq i < j \leq n$ and where $D = z_j - z_i \in Z_{\mathcal{C}}\mathbb{Z}$ is a multiple of $Z_{\mathcal{C}}$ and $q_i = q_j$, we define the *gluing* of the configurations as

$$\pi - [i, j] = q_0(z_0) \cdots \xrightarrow{\pi_{i-1}, N} q_i(z_i) \xrightarrow{\pi_j, N} q_{j+1}(z_{j+1} - D) \cdots \xrightarrow{\pi_{n-1}, N} q_n(z_n - D).$$

When gluing the leftmost and rightmost configurations of pairwise non-intersecting intervals $I_1 = [a_1, b_1], \dots, I_k = [a_k, b_k] \subseteq [0, n]$, assuming $b_i < a_{i+1}$

for all $1 \leq i < k$, and $q_{a_i} = q_{b_i}$ and $z_{b_i} - z_{a_i} \in Z\mathbb{Z}$ for all $1 \leq i \leq k$, we will use $\pi - I_1 - I_2 \cdots - I_k$ to denote the result corresponding to gluing each interval successively while shifting the others accordingly, instead of writing the more tedious $\pi^{(k)}$, where

$$\begin{aligned} \pi^{(1)} &= \pi - [a_1, b_1], \\ \pi^{(2)} &= \pi^{(1)} - [a_2 - (|I_1| - 1), b_2 - (|I_1| - 1)], \\ &\dots \\ \pi^{(k)} &= \pi^{(k-1)} - [a_k - \sum_{1 \leq j < k} (|I_j| - 1), b_k - \sum_{1 \leq j < k} (|I_j| - 1)]. \end{aligned}$$

6.2 The Bracket Projection of Semiruns

In this section we define a projection ϕ of semitransitions $\tau = q(z) \xrightarrow{op, N} q'(z')$ to a word over the binary alphabet $\{[,]\}$, where transitions with $op = +p$ are mapped to $[$, transitions with $op = -p$ are mapped to $]$, and all other transitions are mapped to the empty word ε . The projection ϕ is naturally extended to a morphism from semiruns to $\{[,]\}^*$. In this section we will show the following lemmas.

- The Depumping Lemma (Lemma 22) states that for each N -semirun whose ϕ -projection has bounded bracketing properties and that has a counter effect whose absolute value is sufficiently large there exists another N -semirun with a counter effect whose absolute value is slightly smaller. This latter resulting N -semirun has a particular form in that it can be obtained from the original N -semirun by applying the above-mentioned operations of shifting and gluing: notably, the sub-semiruns that are being glued themselves have a ϕ -projection that has bounded bracketing properties.
- The Bracket Lemma (Lemma 23) states that if an N -semirun has all its counter values in $[0, 4N]$, has an absolute counter effect that is sufficiently large and has a ϕ -projection satisfies a suitable threshold condition on the number of occurrences of $[$ and $]$, that there is a subsemirun where the absolute counter effect is also large and whose ϕ -projection has bounded bracketing properties.

Formally, we define a mapping ϕ such that for every semitransition $\tau = q(z) \xrightarrow{op, N} q'(z')$,

$$\phi(\tau) = \begin{cases} [& \text{if } op = +p \\] & \text{if } op = -p \\ \varepsilon & \text{otherwise.} \end{cases}$$

Note that an N -semirun π can contain several $+p$ -transitions and $-p$ transitions. We introduce the notation $\phi(\pi, i) = \phi(\pi[i, i + 1])$ to denote the ϕ -projection of the i -th transition of π for all $i \in [0, |\pi| - 1]$. The mapping ϕ is naturally extended to a morphism on semiruns to words over the binary alphabet $\{[,]\}$ as expected: $\phi(\pi) = \phi(\pi, 0)\phi(\pi, 1) \cdots \phi(\pi, |\pi| - 1)$.

We are particularly interested in N -semiruns whose projection by ϕ contains as many opening as closing brackets and only a few pending ones (when read from left

to right). To make this formal, for all $k \in \mathbb{N}$ we define the regular language

$$\Lambda_k = \{w \in \{[,]\}^* : |w|_[-] = |w|_[, \forall u, v \in \{[,]\}^*. uv = w \implies |u|_[-] - |u|_[,] \in [-k, k]\}.$$

We are interested in analyzing N -semiruns with counter values in $[0, 4N]$. Bounding the counter values like this limits the number of $+p$ (resp. $-p$) that can appear in a row. This will be the basis in the Bracket Lemma which amounts to showing the existence of subsemiruns whose ϕ -projection is in Λ_8 .

The now following Depumping Lemma will enable us to reduce the counter effect of N -semiruns whose ϕ -projection is in Λ_8 . It is worth remarking that $\Gamma_C \ll \Upsilon_C$, recalling the definition of our constants on page 18.

Lemma 22 (Depumping Lemma) *For all N -semiruns π satisfying $\phi(\pi) \in \Lambda_8$ and $|\Delta(\pi)| > \Upsilon_C$ there exists an N -semirun π' such that either*

- $\Delta(\pi) > \Upsilon_C$ and $\Delta(\pi') = \Delta(\pi) - \Gamma_C$, or
- $\Delta(\pi) < -\Upsilon_C$ and $\Delta(\pi') = \Delta(\pi) + \Gamma_C$.

Moreover, $\pi' = \pi - I_1 - I_2 \cdots - I_k$ for pairwise disjoint intervals $I_1, \dots, I_k \subseteq [0, |\pi|]$ such that we have $\phi(\pi[I_i]) \in \Lambda_{16}$ for all $i \in [1, k]$, and either $\Delta(\pi[I_i]) > 0$ for all $i \in [1, k]$ or $\Delta(\pi[I_i]) < 0$ for all $i \in [1, k]$.

Proof Let $\pi = q_0(z_0) \xrightarrow{\pi_0, N} q_1(z_1) \xrightarrow{\pi_1, N} \dots \xrightarrow{\pi_{n-1}, N} q_n(z_n)$ be an N -semirun such that $\phi(\pi) \in \Lambda_8$. We will assume without loss of generality that $\Delta(\pi) > \Upsilon_C$. The dual case when $\Delta(\pi) < -\Upsilon_C$ can be proven analogously.

For every position $i \in [0, n]$ let us define

$$\lambda(i) = |\phi(\pi[0, i])|_[-] - |\phi(\pi[0, i])|_[,] \quad \text{and} \quad \text{pot}(i) = z_i - z_0 - \lambda(i) \cdot N.$$

Note that since $\phi(\pi) \in \Lambda_8$ we have for all $i \in [0, n]$,

$$\lambda(i) \in [-8, 8], \tag{3}$$

and moreover

$$\phi(\pi[0, i]) \in \Lambda_8 \iff \lambda(i) = 0. \tag{4}$$

We note the following important properties of pot ,

1. $|\text{pot}(i - 1) - \text{pot}(i)| \leq 1$ for all $i \in [1, n]$,
2. $\text{pot}(0) = 0$,
3. for all $0 \leq i < j \leq n$, if $\lambda(i) = \lambda(j)$, then $\text{pot}(j) - \text{pot}(i) = z_j - z_i$, and
4. $\text{pot}(n) = z_n - z_0 = \Delta(\pi)$ since $\lambda(0) = \lambda(n) = 0$.

The following claim states that if in a subsemirun the pot increases sufficiently large, then one can find a subsemirun therein that can potentially be glued.

Claim 1 *For each subsemirun $\pi[a, b]$ that satisfies $\text{pot}(b) - \text{pot}(a) > 17 \cdot |Q| \cdot Z_C$ there exist positions $a \leq s < t \leq b$, such that*

- $q_s = q_t$,
- $\lambda(s) = \lambda(t)$, and

- $z_t - z_s = dZ_C$ for some $d \in [1, 17 \cdot |Q|]$.

Proof of the Claim. Since by assumption $\text{pot}(b) - \text{pot}(a) > 17 \cdot |Q| \cdot Z_C$, by the pigeonhole principle and Point 1 above, there exist two indices $a \leq s < t \leq b$ such that $q_s = q_t, \lambda(s) \in [-8, 8]$ and $\lambda(t) \in [-8, 8]$ are equal, and $\text{pot}(t) - \text{pot}(s) = dZ_C$ for some $d \in [1, 17 \cdot |Q|]$. By Point 3 above, from $\lambda(t) = \lambda(s)$, it follows $z_t - z_s = \text{pot}(t) - \text{pot}(s) = dZ_C$.

(End of the proof of the Claim) □

Since $\text{pot}(i) - \text{pot}(i - 1) \leq 1$ for all $i \in [1, n]$ by Point 1 above and

$$\begin{aligned} \text{pot}(n) - \text{pot}(0) &= z_n - z_0 \\ &= \Delta(\pi) \\ &> \Upsilon_C \\ &\stackrel{\text{page 15}}{=} 17 \cdot |Q| \cdot \text{LCM}(17 \cdot |Q|) \cdot (17 \cdot |Q| \cdot Z_C + 2), \end{aligned}$$

by the pigeonhole principle, there exist at least

$$17 \cdot |Q| \cdot \text{LCM}(17 \cdot |Q|)$$

pairwise disjoint subsemiruns $\pi[a, b]$ satisfying $\text{pot}(b) - \text{pot}(a) > 17 \cdot |Q| \cdot Z_C$. Let

$$L = \text{LCM}(17 \cdot |Q|),$$

and let $\pi[a_1, b_1], \dots, \pi[a_{17 \cdot |Q| \cdot L}, b_{17 \cdot |Q| \cdot L}]$ be an enumeration of these latter subsemiruns. We apply the above Claim to all of these $\pi[a_i, b_i]$: there exist positions $a_i \leq s_i \leq t_i \leq b_i$ such that $\lambda(s_i) = \lambda(t_i), q_{s_i} = q_{t_i}$, and $z_{t_i} = z_{s_i} + d_i Z_C$ for some $d_i \in [1, 17 \cdot |Q|]$. From $\lambda(s_i) = \lambda(t_i)$ and (3) it follows $\phi(\pi[s_i, t_i]) \in \Lambda_{16}$. Recall that $\Gamma_C = \text{LCM}(17 \cdot |Q|) \cdot Z_C = L \cdot Z_C$, cf. page 18. By the pigeonhole principle, among these $17 \cdot |Q| \cdot L$ pairwise disjoint subsemiruns $\pi[a_i, b_i]$, there exists some $d \in [1, 17 \cdot |Q|]$ such that there are L/d many different $\pi[a_i, b_i]$ all satisfying $d_i = d$. Let $\pi[a_{i_1}, b_{i_1}], \dots, \pi[a_{i_{L/d}}, b_{i_{L/d}}]$ be an enumeration of these latter $\pi[a_i, b_i]$. Note that for all of these $\pi[a_i, b_i]$ we have $\Delta(\pi[s_{i_j}, t_{i_j}]) = d \cdot Z_C$. Since moreover $q_{s_{i_j}} = q_{t_{i_j}}$ we know that, for all $j \in [1, L/d]$, the gluing $\pi - [s_{i_j}, t_{i_j}]$ is an N -semirun with $\Delta(\pi - [s_{i_j}, t_{i_j}]) = \Delta(\pi) - dZ_C$. Thus,

$$\pi' = \pi - [s_{i_1}, t_{i_1}] - \dots - [s_{i_{L/d}}, t_{i_{L/d}}]$$

is an N -semirun satisfying $\Delta(\pi') = \Delta(\pi) - d \cdot (L/d) \cdot Z_C = \Delta(\pi) - \Gamma_C$ as required. □

Let us now introduce the Bracket Lemma, which states that in case the absolute value of the counter effect of an N -semirun is sufficiently large, the counter values are all in $[0, 4N]$ and a majority condition holds on the number of occurrences of [and] in its ϕ -projection, that there is a subsemirun where the counter effect is also large and that moreover has good bracketing properties (in the sense of the Depumping Lemma). Roughly speaking, it is based on the idea that if the values of a semirun are all in $[0, 4N]$, there cannot be five $+p$ -transitions in a row. Technically speaking, the Bracket Lemma can be applied to $(N - \Gamma_C)$ -semiruns, where N is sufficiently

large: the reason is that the Bracket Lemma will later be applied to N -semiruns in which some of the $+p/ - p$ -transitions have already been modified (“by hand”) to have an effect $(N - \Gamma_C)/ - (N - \Gamma_C)$ instead of $N/ - N$.

Lemma 23 (Bracket Lemma) *For all $N > M_C$, all $(N - \Gamma_C)$ -semiruns π satisfying $\text{VALUES}(\pi) \subseteq [0, 4N]$, $\Delta(\pi) < -\Upsilon_C$ (resp. $\Delta(\pi) > \Upsilon_C$) and where $\phi(\pi)$ contains at least as many occurrences of $[$ as occurrences of $]$ (resp. at least as many occurrences of $]$ as occurrences of $[$) there exists a subsemirun $\pi[c, d]$ satisfying $\phi(\pi[c, d]) \in \Lambda_8$ and $\Delta(\pi[c, d]) < -\Upsilon_C$ (resp. $\Delta(\pi[c, d]) > \Upsilon_C$).*

Proof We only prove the case where $\Delta(\pi) < -\Upsilon_C$ and $\phi(\pi)$ contains at least as many occurrences of $[$ as of $]$. The dual case when $\Delta(\pi) > \Upsilon_C$ and $\phi(\pi)$ contains at least as many $]$ as of $[$ can be proven analogously.

As in the proof of Lemma 22, for any word $u \in \{[,]\}^*$ let $\lambda(u) = |u|_[- |u|_]$. For the rest of the proof assume by contradiction that there is no such subsemirun $\pi[c, d]$ satisfying $\Delta(\pi[c, d]) < -\Upsilon_C$ and $\phi(\pi[c, d]) \in \Lambda_8$, or, equivalently, that every subsemirun $\pi[c, d]$ with $\phi(\pi[c, d]) \in \Lambda_8$ satisfies $\Delta(\pi[c, d]) \geq -\Upsilon_C$.

For all $k \geq 0$ let

$$\Psi_k = \{w \in \{[,]\}^* \mid \forall uv = w : \lambda(u) \in [-k, k]\}$$

denote the set of all words over the alphabet $\{[,]\}$, where for each prefix the absolute difference between the number of occurrences of $[$ and of $]$ is at most k . Note that

$$\Lambda_k = \Psi_k \cap \lambda^{-1}(0). \tag{5}$$

Under the above assumptions on π , for the sake of contradiction, we have three claims on properties on the image of ϕ applied to π and subsemiruns thereof.

Claim 1. $\phi(\pi) \in \Psi_4$.

Proof of Claim 1. Let us write $\pi = \pi[0, n]$. Assume by contradiction that $\phi(\pi) \notin \Psi_4$. Let u be a shortest prefix of $\phi(\pi)$ such that $\lambda(u) \notin [-4, 4]$. Let us first consider the case when $\lambda(u) > 4$.

By definition of u we have $\lambda(u) = 4 + 1 = 5$ and there are indices $0 \leq t_1 < \dots < t_5 < n$ such that

- $\phi(\pi, t_1) = \dots = \phi(\pi, t_5) = [$, and
- $\phi(\pi[t_i + 1, t_{i+1}]) \in \Lambda_4$ for all $i \in [1, 4]$.

Recall that by our assumption every subsemirun $\pi[c, d]$ of π with $\phi(\pi[c, d]) \in \Lambda_8$ satisfies $\Delta(\pi[c, d]) \geq -\Upsilon_C$. Since $\bigcup_{i \in [1, 8]} \Lambda_i = \Lambda_8$ it follows $\Delta(\pi[t_i + 1, t_{i+1}]) \geq -\Upsilon_C$ for all $i \in [1, 4]$. Moreover, bearing in mind that π is an $(N - \Gamma_C)$ -semirun, we obtain $\Delta(\pi, t_i) = N - \Gamma_C$. Altogether, as $N > M_C$ by assumption, we obtain

$$\begin{aligned} \Delta(\pi[t_1, t_5 + 1]) &\geq -4 \cdot \Upsilon_C + 5 \cdot (N - \Gamma_C) \\ &> 4N + N - 5 \cdot (\Upsilon_C + \Gamma_C) \\ &> 4N + M_C - 5 \cdot (\Upsilon_C + \Gamma_C) \\ &> 4N, \end{aligned}$$

where the last inequality follows from M_C 's definition on page 18, hence contradicting $\text{VALUES}(\pi) \subseteq [0, 4N]$.

Let us now consider the case when $\lambda(u) < -4$. Again, by definition of u , we have $\lambda(u) = -5$. There are hence indices $0 \leq t_1 < \dots < t_5 < n$ such that

$$\phi(\pi, t_1) = \dots = \phi(\pi, t_5) =],$$

and moreover $\phi(\pi[0, t_1]) \in \Lambda_4$ and $\phi(\pi[t_i + 1, t_{i+1}]) \in \Lambda_4$ for all $i \in [1, 4]$. By assumption $\phi(\pi)$ contains at least as many $[$ as $]$. Therefore there must exist 5 further positions t'_1, \dots, t'_5 in π satisfying $0 \leq t_1 < \dots < t_5 < t'_1 < t'_2 < \dots < t'_5 < n$ such that

$$\phi(\pi, t'_1) = \dots = \phi(\pi, t'_5) = [$$

and $\phi(\pi[t'_i + 1, t'_{i+1}]) \in \Lambda_4$ for all $i \in [1, 4]$. Again taking into account our assumption that $\Delta(\pi[c, d]) \geq -\Upsilon_C$ for all subsemiruns $\pi[c, d]$ with $\phi(\pi[c, d]) \in \Lambda_8$, it follows as above, that $\Delta(\pi[t'_1, t'_5 + 1]) > 4N$, contradicting $\text{VALUES}(\pi) \subseteq [0, 4N]$. \square

Claim 2. $\phi(\pi[a, b]) \in \Psi_8$ for all subsemiruns $\pi[a, b]$ of π .

Proof of Claim 2. This is an immediate consequence of Claim 1. Indeed, any subsemirun $\pi[a, b]$ of π satisfying $\phi(\pi[a, b]) \notin \Psi_8$ gives rise to a prefix u of $\phi(\pi)$ such that $u \notin \Psi_4$ and hence $\phi(\pi) \notin \Psi_4$. \square

Claim 3. For all subsemiruns $\pi[a, b]$ of π , if $\lambda(\phi(\pi[a, b])) > 0$, then $\Delta(\pi[a, b]) > \Upsilon_C$.

Proof of Claim 3. We prove the statement by induction on $\lambda(\phi(\pi[a, b]))$.

For the induction base, assume $\lambda(\phi([a, b])) = 1$. Thus, there exists a position $t \in [a, b]$ such that $\phi(\pi, t) = [$ and $\lambda(\pi[a, t]) = \lambda(\phi(\pi[t + 1, b])) = 0$. By Claim 2 and (5) we have $\phi(\pi[a, t]), \phi(\pi[t + 1, b]) \in \Lambda_8$. Thus, $\Delta(\pi[a, t]), \Delta(\pi[t + 1, b]) > -\Upsilon_C$ by our assumption. Hence, we obtain

$$\begin{aligned} \Delta(\pi[a, b]) &= \Delta(\pi[a, t]) + \Delta(\pi, t) + \Delta(\pi[t + 1, b]) \\ &\geq -\Upsilon_C + (N - \Gamma_C) - \Upsilon_C \\ &> M_C - 2\Upsilon_C - \Gamma_C \\ &> \Upsilon_C, \end{aligned}$$

where the last strict inequality follows from definition of M_C on page 18.

Assume $\lambda(\phi(\pi[a, b])) > 1$. Consider the smallest position $t \in [a, b]$ such that $\lambda(\phi(\pi[a, t])) = 0$ and $\phi(\pi, t) = [$. By Claim 2 and (5) it follows that $\phi(\pi[a, t]) \in \Lambda_8$ and hence $\Delta(\pi[a, t]) \geq -\Upsilon_C$ by our assumption. Moreover, $\lambda(\phi(\pi[t + 1, b])) = \lambda(\phi(\pi[a, b])) - 1$. We can thus apply induction hypothesis to $\pi[t + 1, b]$ and obtain

$$\begin{aligned} \Delta(\pi[a, b]) &= \Delta(\pi[a, t]) + \Delta(\pi, t) + \Delta(\pi[t + 1, b]) \\ &> \Delta(\pi[a, t]) + \Delta(\pi, t) + \Upsilon_C \\ &\geq -\Upsilon_C + (N - \Gamma_C) + \Upsilon_C \\ &> M_C - \Gamma_C, \\ &> \Upsilon_C, \end{aligned}$$

where the first strict inequality follows from induction hypothesis on $\pi[t + 1, b]$ and the last strict inequality follows from definition of M_C on page 18 constant definitions. \square

We will now contradict our initial assumption that there is no subsemirun $\pi[c, d]$ satisfying $\phi(\pi[c, d]) \in \Lambda_8$ and $\Delta(\pi[c, d]) < -\Upsilon_C$ by making use of the above claims.

Since π itself satisfies $\Delta(\pi) < -\Upsilon_C$, it follows $\phi(\pi) \notin \Lambda_8 = \Psi_8 \cap \lambda^{-1}(0)$ by our assumption and (5). But since $\phi(\pi) \in \Psi_8$ by Claim 2, it follows $\lambda(\phi(\pi)) \neq 0$.

As $\phi(\pi)$ contains at least as many occurrences of [as occurrences of] by assumption, $\phi(\pi)$ must contain strictly more occurrences of [than of], i.e. $\lambda(\phi(\pi)) > 0$. By Claim 3 it follows $\Delta(\pi) > \Upsilon_C$, contradicting our assumption that $\Delta(\pi) < -\Upsilon_C$. \square

6.3 Embeddings of Semiruns

The Small Parameter Theorem (Theorem 18) turns N -runs with values in $[0, 4N]$ into $(N - \Gamma_C)$ -runs. In proving this, we prefer to view N -runs as N -semiruns. Indeed, we first view any N -run as an N -semirun and then apply certain of the above-mentioned operations on them to obtain some $(N - \Gamma_C)$ -semirun. However, we would then like to claim that the resulting $(N - \Gamma_C)$ -semirun is in fact an $(N - \Gamma_C)$ -run as desired, in particular the comparison tests need to hold. To do so, we introduce a notion when an N -semirun can be embedded into an M -semirun (possibly $N \neq M$) in the sense that operations are being preserved, source and target control states are being preserved, and that with respect to some line $\ell \in \mathbb{Z}$ the counter value of each configuration of the embedding has the same orientation with respect to ℓ as the counter value of the configuration it corresponds to.

Definition 24 (ℓ -embedding) Let $\ell \in \mathbb{Z}$. An N -semirun

$$\sigma = s_0(y_0) \xrightarrow{\sigma_0, N} s_1(y_1) \cdots \xrightarrow{\sigma_{n-1}, N} s_n(y_n)$$

is an ℓ -embedding of an M -semirun

$$\pi = q_0(z_0) \xrightarrow{\pi_0, M} q_1(z_1) \cdots \xrightarrow{\pi_{m-1}, M} q_m(z_m)$$

if $s_0 = q_0, s_n = q_m$ and there exists an order-preserving injective mapping $\psi : [0, n] \rightarrow [0, m]$ such that

- $\sigma_i = \pi_{\psi(i)}$ for all $i \in [0, n - 1]$, and
- $\ell \bowtie y_i$ if, and only if, $\ell \bowtie z_{\psi(i)}$ for all $\bowtie \in \{<, =, >\}$ and all $i \in [0, n]$.

Moreover we say σ is

- max-falling (w.r.t π) if $\max(\sigma) \leq \max(\pi)$, and
- min-rising (w.r.t. π) if $\min(\sigma) \geq \min(\pi)$.

Example 25 Consider the semiruns π, σ and τ in Fig. 5, where neither concrete counter values nor the control states of σ and τ are mentioned. The semirun σ can

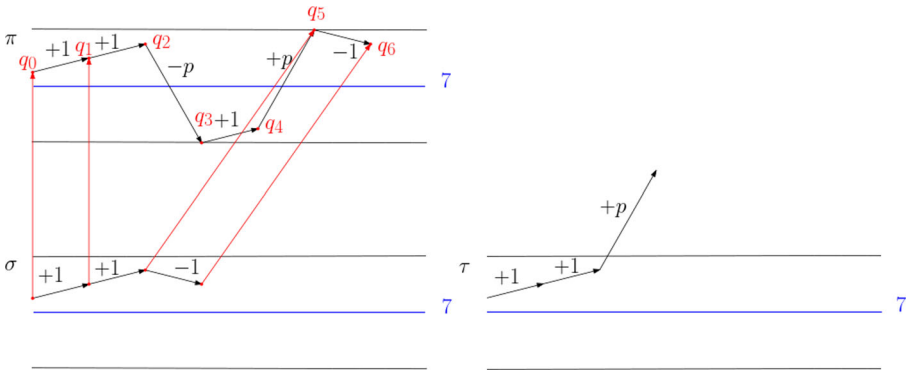


Fig. 5 Example of a semirun σ that could possibly be an embedding of the semirun π and a semirun τ that cannot

possibly be a 7-embedding of π (if its source control is q_0 and its target control state is q_6). However, τ cannot be a 7-embedding of π . Indeed, for every possible ψ such that $\tau_2 = +p = \pi_{\psi(2)}$, the counter value of τ at position 2 is strictly larger than 7, whereas the counter value of π at position $\psi(2)$ is strictly below 7.

The following remark is implicitly being used in subsequent sections.

Remark 26 Embeddings possess some useful properties that all follow immediately from definition.

- *Transitivity.* Let π , ρ and σ be semiruns such that π is an ℓ -embedding of ρ and ρ is an ℓ -embedding of σ . Then π is an ℓ -embedding of σ . Moreover, if π was max-falling (resp. min-rising) w.r.t. ρ and ρ was max-falling (resp. min-rising) w.r.t. σ , then π is max-falling (resp. min-rising) w.r.t. σ .
- *Closure under concatenation.*
Let π be an N -semirun from $q(x)$ to $r(y)$ and let ρ be N -semirun from $r(y)$ to $s(z)$. Moreover, let π' be an N' -semirun from $q(x')$ to $r(y')$ that is an ℓ -embedding of π and let ρ' be an N' -semirun from $r(y')$ to $s(z')$ that is an ℓ -embedding of ρ . Then $\pi'\rho'$ is an ℓ -embedding of $\pi\rho$. If furthermore, π' was max-falling (resp. min-rising) w.r.t. π and ρ' was max-falling (resp. min-rising) w.r.t. ρ , then $\pi'\rho'$ is max-falling (resp. min-rising) w.r.t. $\pi\rho$.
- *Shifting distant embeddings.* Let $D \in \mathbb{Z}_C\mathbb{Z}$ be a multiple of Z_C , let π be a semirun and let ρ be an ℓ -embedding of π such that for all configurations $q(z)$ in ρ we have $|z - \ell| > |D|$. Then both $\rho + D$ and $\rho - D$ are ℓ -embeddings of π .

7 On Hills and Valleys

In this section we introduce the notions of hills and valleys. Hills are semiruns that start and end in configurations with low counter values but where all intermediate configurations have counter values above these source and target configurations, and

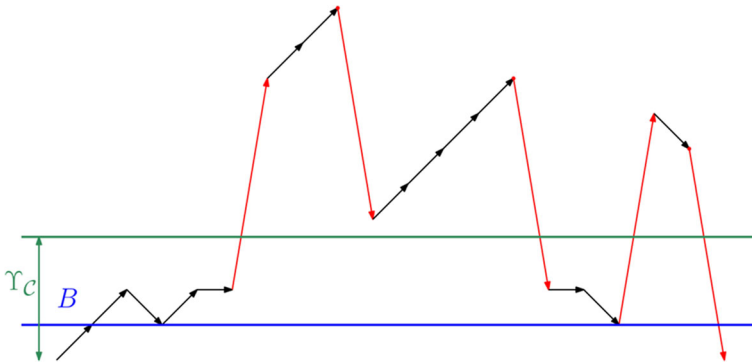


Fig. 6 Illustration of a B -hill

where moreover $+p$ -transitions (resp. $-p$ -transitions) are followed (resp. preceded) by semiruns with absolute counter effect larger than Υ_C (we refer to Fig. 6 for an illustration of the concept). We also introduce the dual notion of valleys. We then prove that an N -semirun that is either a hill or a valley can be turned into an $(N - \Gamma_C)$ -semirun with the same source and target configuration that is an embedding. This lowering process serves as a building block in the proof of the 5/6-Lemma (Lemma 39).

Definition 27 (Hills and Valleys) An N -semirun

$$q_0(z_0) \xrightarrow{\pi_0, N} q_1(z_1) \xrightarrow{\pi_1, N} q_2(z_2) \quad \dots \quad \xrightarrow{\pi_{n-1}, N} q_n(z_n)$$

is a

- B -hill if

- $z_0, z_n < B$,
- $z_i \geq B$ for all $i \in [1, n - 1]$,
- $\pi_i = -p$ implies $z_i > z_0 + \Upsilon_C$ for all $i \in [0, n - 1]$, and
- $\pi_i = +p$ implies $z_{i+1} > z_n + \Upsilon_C$ for all $i \in [0, n - 1]$.

- B -valley if

- $z_0, z_n > B$,
- $z_i \leq B$ for all $i \in [1, n - 1]$,
- $\pi_i = -p$ implies $z_{i+1} < z_n - \Upsilon_C$ for all $i \in [0, n - 1]$, and
- $\pi_i = +p$ implies $z_i < z_0 - \Upsilon_C$ for all $i \in [0, n - 1]$.

The Hill and Valley Lemma states that an N -semirun π that is either a B -hill or a B -valley can be turned into an $(N - \Gamma_C)$ -semirun with the same source and target configuration that is moreover both a min-rising and max-falling B' -embedding of π , where B' is close to B .

Lemma 28 (Hill and Valley Lemma) *For all $N, B \in \mathbb{N}$, all N -semiruns π from $q_0(z_0)$ to $q_n(z_n)$ with $N > M_C$ and $\text{VALUES}(\pi) \subseteq [0, 4N]$ such that moreover π is either a B -hill or a B -valley, there exists an $(N - \Gamma_C)$ -semirun from $q_0(z_0)$ to $q_n(z_n)$ that is both a min-rising and max-falling $(B - \Upsilon_C - \Gamma_C - 1)$ -embedding of π (in case π is a B -hill), or both a min-rising and max-falling $(B + \Upsilon_C + \Gamma_C + 1)$ -embedding of π (in case π is a B -valley).*

We remark that the resulting $(N - \Gamma_C)$ -semirun satisfies further properties — these are being discussed in Section 7.2.

Before proving the Hill and Valley Lemma let us explain why the finding of the resulting embedding is delicate. Let us fix any N -semirun

$$\pi = q_0(z_0) \xrightarrow{\pi_0, N} q_1(z_1) \xrightarrow{\pi_1, N} \dots \xrightarrow{\pi_{n-1}, N} q_n(z_n)$$

from $q_0(z_0)$ to $q_n(z_n)$ with $\text{VALUES}(\pi) \subseteq [0, 4N]$ and $N > M_C$. Let us moreover assume that π is a B -hill for some $B \in \mathbb{N}$. We need to show the existence of some $(N - \Gamma_C)$ -semirun from $q_0(z_0)$ to $q_n(z_n)$ that is moreover both a min-rising and max-falling $(B - \Upsilon_C - \Gamma_C - 1)$ -embedding of π .

We are particularly interested in those transitions τ with absolute counter effect $|\Delta(\tau)| = N$, i.e. transitions with operation $+p$ or $-p$ that we will denote as *unlowered* $+p$ -transitions and $-p$ -transitions respectively. Note that if there is no such transition in π , then π is already an $(N - \Gamma_C)$ -semirun. Let us therefore assume there is at least one transition with absolute counter effect N in π . For obtaining only an $(N - \Gamma_C)$ -semirun it would simply suffice to lower the absolute counter effect of these transitions by Γ_C . Indeed, if the transition $\tau = q(z) \xrightarrow{+p, N} q'(z')$ is an N -semirun, then the *lowered transition* $\widehat{\tau} = q(z) \xrightarrow{+p, N - \Gamma_C} q'(z' - \Gamma_C)$ is an $(N - \Gamma_C)$ -semirun. Dually, if $\tau = q(z) \xrightarrow{-p, N} q'(z')$ is an N -semirun, then $\widehat{\tau} = q(z) \xrightarrow{-p, N - \Gamma_C} q'(z' + \Gamma_C)$ is an $(N - \Gamma_C)$ -semirun.

Thus, applying such a lowering to all transitions of π whose absolute counter effect is N yields an $(N - \Gamma_C)$ -semirun with target configuration shifted by a multiple of Γ_C , according to the operations seen in Section 6. However, the Hill and Valley Lemma not only requires the resulting semirun to be an $(N - \Gamma_C)$ -semirun but also to have same source and target configurations as the original semirun (and to be a min-rising and max-falling $(B - \Upsilon_C - \Gamma_C - 1)$ -embedding). Hence, simply lowering all transitions with a large counter effect as described above is not enough to prove the result as the following example illustrates. Let us assume an N -semirun π containing precisely one transition τ whose absolute counter effect is N , say $\pi_j = +p$ for some position j . That is,

$$\pi = q_0(z_0) \xrightarrow{\pi_0, N} \dots q_j(z_j) \xrightarrow{+p, N} q_{j+1}(z_{j+1}) \dots \xrightarrow{\pi_{n-1}, N} q_n(z_n) .$$

If we replace directly this j -th transition by a transition with $\Delta(\tau') = N - \Gamma_C$, and, starting with the $(j + 1)$ -th configuration, shift all following counter values by $-\Gamma_C$, we indeed obtain an $(N - \Gamma_C)$ -semirun

$$q_0(z_0) \xrightarrow{\pi_0, N-\Gamma_C} \dots q_j(z_j) \xrightarrow{+p, N-\Gamma_C} q_{j+1}(z_{j+1} - \Gamma_C) \dots \xrightarrow{\pi_{n-1}, N-\Gamma_C} q_n(z_n - \Gamma_C) .$$

However, this $(N - \Gamma_C)$ -semirun does not have the same source and target configuration as the original semirun, as the target configuration's counter value has been shifted by $-\Gamma_C$. Worse yet, if our initial N -semirun π were to possess several $+p$ -transitions, then the accumulated counter value shifts could potentially yield that the resulting $(N - \Gamma_C)$ -semirun is not a $(B - \Upsilon_C - \Gamma_C - 1)$ -embedding of π : indeed, such a shifted semirun could contain intermediate configurations with counter values less than $B - \Upsilon_C - \Gamma_C - 1$.

In order to account for those transitions whose absolute counter effect is N that have already been lowered or not we will introduce the notion of hybrid semiruns, which can be seen as sequences of N -semiruns and $(N - \Gamma_C)$ -semiruns whose source and target configurations are suitably connected.

Definition 29 A hybrid semirun is a sequence $\eta = \alpha^{(0)}\beta^{(1)}\alpha^{(1)} \dots \beta^{(k)}\alpha^{(k)}$, where

- each $\alpha^{(i)}$ is an $(N - \Gamma_C)$ -semirun (possibly empty) of the form

$$\alpha^{(i)} = p_0(y_0) \xrightarrow{\alpha_0^{(i)}, N-\Gamma_C} p_1(y_1) \dots \xrightarrow{\alpha_{m_i}^{(i)}, N-\Gamma_C} p_{m_i}(y_{m_i})$$

- each $\beta^{(i)}$ is a single transition with $|\Delta(\beta^{(i)})| = N$,
- the target configuration of $\alpha^{(i-1)}$ is the source configuration of $\beta^{(i)}$ for all $i \in [1, k]$, and
- the source configuration of $\alpha^{(i)}$ is the target configuration of $\beta^{(i)}$ for all $i \in [1, k]$.

We call k the *breadth* of η .

Remark 30 In case our initial N -semirun π contains k transitions of absolute counter effect N , we observe that π can naturally be viewed as an initial hybrid semirun of breadth k .

Several of the notions (such as counter effect, length and maximum) that we have defined for runs and semiruns can naturally be extended to hybrid semiruns. As expected, the projection $\phi(\eta)$ is defined as $\phi(\eta) = \phi(\alpha^{(0)})\phi(\beta^{(1)})\phi(\alpha^{(1)}) \dots \phi(\beta^{(k)})\phi(\alpha^{(k)})$. We moreover introduce the particular projection ϕ_{\uparrow} of ϕ restricted to the $\alpha^{(i)}$, i.e. $\phi_{\uparrow}(\eta) = \phi(\alpha^{(0)})\phi(\alpha^{(1)}) \dots \phi(\alpha^{(k)})$.

Moreover, we view the $\alpha^{(i)}$ themselves as sequences (not as atomic objects) of length m_i and the $\beta^{(i)}$ as sequences of length one. Using this convention, the notions of prefixes, infixes and suffixes are as expected. More importantly, we extend naturally the notion of (max-falling and min-rising) ℓ -embedding to hybrid semiruns as in Definition 24 when treating them as such sequences.

We prove the Hill and Valley Lemma (Lemma 28) in Section 7.1. We summarize important further consequences of the proof in Section 7.2.

7.1 Proof of the Hill and Valley Lemma

Let us fix any N -semirun

$$\pi = q_0(z_0) \xrightarrow{\pi_{0,N}} q_1(z_1) \xrightarrow{\pi_{1,N}} \dots \xrightarrow{\pi_{n-1,N}} q_n(z_n)$$

from $q_0(z_0)$ to $q_n(z_n)$ with $\text{VALUES}(\pi) \subseteq [0, 4N]$ and $N > M_C$. Let us moreover assume that π is a B -hill for some $B \in \mathbb{N}$. The case when π is a B -valley can be proven analogously.

For reasons of simplicity we separate the proof into two cases, namely if there is a $+p$ -transition or $-p$ -transition whose source and target configurations have counter values that are both at most $B + \Upsilon_C + \Gamma_C$ or not. Section 7.1.1 deals with the latter case, Section 7.1.2 with the former. It is worth mentioning that Section 7.1.2 depends on Section 7.1.1.

7.1.1 π Does not Contain any $\pm p$ -Transition Whose Source and Target Configuration Both Have Counter Value at Most $B + \Upsilon_C + \Gamma_C$

In the following let us denote by \mathcal{L} the *critical level*, i.e. the constant

$$\mathcal{L} = B + \Gamma_C.$$

Moreover, for a hybrid semirun $\eta = \alpha^{(0)}\beta^{(1)}\alpha^{(1)} \dots \beta^{(k)}\alpha^{(k)}$, for every $\beta^{(j)}$ that is an unlowered $+p$ -transition, we define the *critical descending infix with respect to $\beta^{(j)}$* as the shortest prefix (when viewed as a sequence, as mentioned above) of $\alpha^{(j)}\beta^{(j+1)}\alpha^{(j+1)} \dots \beta^{(k)}\alpha^{(k)}$ that ends in a configuration with counter value at most \mathcal{L} . In particular, this critical descending infix could possibly end in a configuration inside some (strict prefix of) $\alpha^{(i)}$, where $i \in [j, k]$. Dually, for every $\beta^{(j)}$ that is an unlowered $-p$ -transition, we define the *critical ascending infix with respect to $\beta^{(j)}$* as the shortest suffix of $\alpha^{(0)}\beta^{(1)} \dots \alpha^{(j-1)}$ that starts in a configuration with counter value at most \mathcal{L} . The following remark is central.

Remark 31 For a hybrid semirun $\eta = \alpha^{(0)}\beta^{(1)}\alpha^{(1)} \dots \beta^{(k)}\alpha^{(k)}$, if some unlowered $-p$ -transition (resp. $+p$ -transition) $\beta^{(j)}$ appears in the critical descending infix (resp. critical ascending infix) of some unlowered $+p$ -transition (resp. $-p$ -transition) $\beta^{(i)}$, then so does $\beta^{(i)}$ appear in the critical ascending infix (resp. critical descending infix) of $\beta^{(j)}$.

Viewing our initial semirun π as a hybrid semirun, we will now introduce two phases that successively lower unlowered $+p$ -transitions and unlowered $-p$ -transitions yielding hybrid semiruns that retain an approximation invariant (Definition 32).

In phase one, we are interested in unlowered $+p$ -transitions. We want to progressively lower these, going from right to left. Moreover, we want to inspect the critical descending infix in order to obtain successive min-rising and max-falling embeddings with the same source and target configuration. In case the rightmost unlowered $+p$ -transition has the property that its critical descending infix contains

some unlowered $-p$ -transition we lower the leftmost such directly, together with the $+p$ -transition. Otherwise, we want to make use of the Bracket Lemma (Lemma 23) and the Depumping Lemma (Lemma 22) in order to retain some nice bracketing properties.

Having successively lowered all unlowered $+p$ -transitions in phase one, we finally lower the remaining unlowered $-p$ -transitions in phase two. For these we take their critical ascending infix and their φ_{\uparrow} -projection into account, again yielding some carefully chosen bracketing property.

The following definition formalizes the above-mentioned bracketing property.

Definition 32 A hybrid semirun η approximates π with respect to level $\ell \in \mathbb{Z}$ if

1. $\eta = \alpha^{(0)}\beta^{(1)}\alpha^{(1)} \dots \beta^{(k)}\alpha^{(k)}$ is a hybrid semirun of some breadth k ,
2. π can be factorized as $\pi = \chi^{(0)}\zeta^{(1)}\chi^{(1)} \dots \zeta^{(k)}\chi^{(k)}$, where the $\zeta^{(i)}$ are transitions with operation either $+p$ or $-p$,
3. η is a min-rising and max-falling ℓ -embedding of π ,
4. $\alpha^{(i)}$ is a max-falling ℓ -embedding of $\chi^{(i)}$ for all $i \in [0, k]$ with the same source and target configuration as $\chi^{(i)}$,
5. every prefix of $\phi_{\uparrow}(\gamma^{(i)})$ contains at least as many occurrences of $[$ as of $]$, where $\gamma^{(i)}$ is the critical descending infix of $\beta^{(i)}$ for all $i \in [1, k]$ for which $\beta^{(i)}$ has operation $+p$, and
6. every suffix of $\phi_{\uparrow}(\gamma^{(i)})$ contains at least as many occurrences of $]$ as of $[$, where $\gamma^{(i)}$ is the critical ascending infix of $\beta^{(i)}$ for all $i \in [1, k]$ for which $\beta^{(i)}$ has operation $-p$.

By completing phase one and then phase two we will show the existence of a hybrid semirun that approximates π with respect to level B and does not contain any unlowered $+p$ -transition nor any unlowered $-p$ -transition (and is hence an $(N - \Gamma_C)$ -semirun). Observe first that by Point 4 any such hybrid semirun η has the same source and target configuration as π . Second, any such η is in particular a min-rising and max-falling $(N - \Upsilon_C - \Gamma_C - 1)$ -embedding of π since π is assumed to be a B -hill. Thus, the lemma follows. We will obtain the desired $(N - \Gamma_C)$ -semirun and variants thereof by first systematically lowering $+p$ -transitions from the rightmost to the leftmost in phase one and secondly systematically lowering the possibly remaining $-p$ -transitions from the leftmost to the rightmost in phase two. We denote such a process — whose details are given below — by the so-called $(+p, -p)$ -lowering process. As mentioned in Remark 35 we will also define a dual variant, namely the $(-p, +p)$ -lowering process: here phase one will consist of systematically lowering the $-p$ -transitions from the leftmost to the rightmost, whereas phase two will systematically lower the possibly remaining $+p$ -transitions from the rightmost to the leftmost.

Remark 35 finally discusses a variant of a $(+p, -p)$ -lowering process (resp. $(-p, +p)$ -process) which ends in a hybrid semirun that contains precisely one unlowered transition.

Let us discuss the $(+p, -p)$ -lowering process in detail.

Phase one of the $(+p, -p)$ -Lowering Process: Lowering $+p$ -Transitions We can view our initial N -semirun π as a hybrid semirun $\eta^{(0)}$ of breadth k_0 , i.e.

$$\eta^{(0)} = \alpha^{(0,0)}\beta^{(0,1)}\alpha^{(0,1)} \dots \beta^{(0,k_0)}\alpha^{(0,k_0)} .$$

In phase one we will inductively show the existence of a sequence of hybrid semiruns $\eta^{(0)}, \eta^{(1)}, \dots, \eta^{(r)}$, where each $\eta^{(i)}$ has breadth k_i and approximates π with respect to level B , $\eta^{(r)}$ does not contain any unlowered $+p$ -transition, and $k_{i-1} > k_i$ for all $i \in [1, r]$. Let us assume that we have inductively already defined the sequence $\eta^{(0)}, \dots, \eta^{(i-1)}$ of hybrid semiruns for some $i \geq 1$ and where $\eta^{(i-1)}$ has breadth $k_{i-1} > 0$ and approximates π with respect to level B and contains at least one unlowered $+p$ -transition. Towards extending the sequence we need to show the existence of some hybrid semirun $\eta^{(i)}$ of breadth $k_i < k_{i-1}$ that approximates π with respect to level B .

Let $\eta^{(i-1)} = \alpha^{(i-1,0)}\beta^{(i-1,1)}\alpha^{(i-1,1)} \dots \beta^{(i-1,k_{i-1})}\alpha^{(i-1,k_{i-1})}$. Let $j \in [1, k_{i-1}]$ be maximal such that $\beta^{(i-1,j)}$ is an unlowered $+p$ -transition. For defining $\eta^{(i)}$ we make the following case distinction.

1. The critical descending infix with respect to the $+p$ -transition $\beta^{(i-1,j)}$ contains at least one unlowered $-p$ -transition. That is, the critical descending infix is of the form

$$\alpha^{(i-1,j)}\beta^{(i-1,j+1)}\alpha^{(i-1,j+1)} \dots \beta^{(i-1,h)}\xi,$$

where ξ is a prefix (possibly empty) of $\alpha^{(i-1,h)}$, $\beta^{(i-1,j+1)}$ is an unlowered $-p$ -transition and where $h \geq j + 1$. We refer to Fig. 7 for an illustration. Our desired hybrid semirun $\eta^{(i)}$ is obtained from $\eta^{(i-1)}$ by simply lowering both $\beta^{(i-1,j)}$ and $\beta^{(i-1,j+1)}$, i.e. replacing $\beta^{(i-1,j)}$ by $\widehat{\beta^{(i-1,j)}}$ satisfying $\Delta(\widehat{\beta^{(i-1,j)}}) = N - \Gamma_C$ and replacing $\beta^{(i-1,j+1)}$ by a suitable $\widehat{\beta^{(i-1,j+1)}}$ satisfying $\Delta(\widehat{\beta^{(i-1,j+1)}}) = -N + \Gamma_C$ and moreover suitably shifting the part after $\widehat{\beta^{(i-1,j)}}$ and until

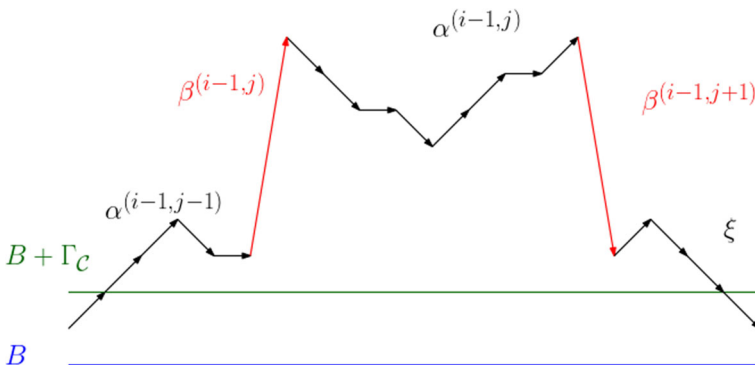


Fig. 7 Illustration of phase one case 1, i.e. the unlowered $+p$ -transition $\beta^{(i-1,j)}$ can be lowered by lowering it with the leftmost unlowered $-p$ -transition on its critical descending infix, i.e. $\beta^{(i-1,j+1)}$

(including) $\widehat{\beta^{(i-1,j+1)}}$ by $-\Gamma_C$. More precisely, the part $\alpha^{(i,j-1)}$ in $\eta^{(i)}$ is chosen to be of the form

$$\alpha^{(i,j-1)} = \alpha^{(i-1,j-1)} \widehat{\beta^{(i-1,j)}} \left(\alpha^{(i-1,j)} - \Gamma_C \right) \left(\widehat{\beta^{(i-1,j+1)}} - \Gamma_C \right) \alpha^{(i-1,j+1)}.$$

Moreover, observe that $\alpha^{(i,j-1)}$ and the infix $\alpha^{(i-1,j-1)} \beta^{(i-1,j)} \alpha^{(i-1,j)} \beta^{(i-1,j+1)} \alpha^{(i-1,j+1)}$ of $\eta^{(i-1)}$ connect the same source and target configurations. Thus, it easily follows that $\eta^{(i)}$ also approximates π with respect to level B . Finally, observe that the breadth of $\eta^{(i)}$ equals $k_{i-1} - 2$.

- The critical descending infix with respect to the $+p$ -transition $\beta^{(i-1,j)}$ does not contain any unlabeled $-p$ -transition. It follows that the critical descending infix with respect to $\beta^{(i-1,j)}$ is a non-empty prefix ξ of $\alpha^{(i-1,j)}$. We refer to Fig. 8 for an illustration. Recall that $\eta^{(i-1)}$ approximates π with respect to level B . Firstly, since by assumption $\text{VALUES}(\pi) \subseteq [0, 4N]$, it follows from Point 3 of Definition 32 that $\text{VALUES}(\xi) \subseteq [0, 4N]$. Secondly, from Point 5 of Definition 32 every prefix of $\phi(\alpha^{(i-1,j)})$ contains at least as many occurrences of $[$ as of $]$. Hence, the latter must also hold for every prefix of $\phi(\xi)$. Thirdly, since by the case of this subsection the target configuration of every transition with operation $+p$ in π has counter value strictly larger than $B + \Upsilon_C + \Gamma_C$, it follows from Points 2 and 4 of Definition 32 that the target configuration of $\beta^{(i-1,j)}$ ends in a configuration with counter value strictly larger than $B + \Upsilon_C + \Gamma_C$. Since ξ is the critical descending infix with respect to $\beta^{(i-1,j)}$ (in particular ending in a configuration with counter value at most $B + \Gamma_C$), it follows $\Delta(\xi) < -\Upsilon_C$. Hence one can apply Lemma 23 to the $(N - \Gamma_C)$ -semirun ξ yielding an infix $\xi[c, d]$ satisfying $\phi(\xi[c, d]) \in \Lambda_8$ and $\Delta(\xi[c, d]) < -\Upsilon_C$. Applying Lemma 22 to $\xi[c, d]$ implies the existence of an $(N - \Gamma_C)$ -semirun $\xi' = \xi[c, d] - I_1 - I_2 \cdots - I_s$ satisfying $\Delta(\xi') = \Delta(\xi[c, d]) + \Gamma_C$ and where I_1, \dots, I_s are pairwise disjoint intervals of positions in $\xi[c, d]$ such that moreover $\phi(\xi[c, d][I_t]) \in \Lambda_{16}$ and $\Delta(\xi[c, d][I_t]) < 0$ for all $t \in [1, s]$. Assume that $\xi = \xi[0, m]$ consisted of m transitions; thus in particular $c, d \in [0, m]$. By combining the above properties it immediately follows that

$$\xi'' = \xi[0, c] \xi' (\xi[d, m] + \Gamma_C)$$

is an $(N - \Gamma_C)$ -semirun with $\Delta(\xi'') = \Delta(\xi) + \Gamma_C$ and that $\xi'' - \Gamma_C$ is a max-falling B -embedding of ξ . We define the desired $\eta^{(i)}$ to be obtained from $\eta^{(i-1)}$ by lowering $\beta^{(i-1,j)}$ to $\widehat{\beta^{(i-1,j)}}$ satisfying $\Delta(\widehat{\beta^{(i-1,j)}}) = \Delta(\beta^{(i-1,j)}) - \Gamma_C$ and moreover replacing ξ by $\xi'' - \Gamma_C$. Observe that $\eta^{(i)}$ and $\eta^{(i-1)}$ only differ in the infix $\alpha^{(i,j-1)}$ of $\eta^{(i)}$. The latter is hence of the form

$$\alpha^{(i,j-1)} = \alpha^{(i-1,j-1)} \widehat{\beta^{(i-1,j)}} (\xi'' - \Gamma_C) \alpha^{(i-1,j)} [m, |\alpha^{(i-1,j)}|].$$

By construction $\eta^{(i-1)}$'s infix

$$\alpha^{(i-1,j-1)} \beta^{(i-1,j)} \alpha^{(i-1,j)}$$

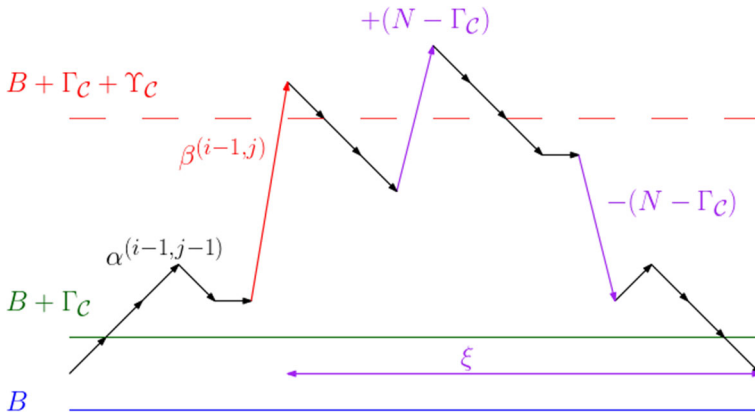


Fig. 8 Illustration of phase one case 2, i.e. the suffix of the to be lowered $+p$ transition $\beta^{(i-1,j)}$ does not contain any unlowered $-p$ -transition, i.e. any transition with counter effect $-N$, inside its critical descending infix

has the same source and target configuration as the part $\alpha^{(i,j-1)}$ of $\eta^{(i)}$. Since moreover

- $\phi(\xi[c, d][I_t]) \in \Lambda_{16}$ contains precisely as many occurrences of [as of] and $\Delta(\xi[c, d][I_t]) < 0$ for each $t \in [1, s]$ and
- $\Delta(\xi'') = \Delta(\xi) + \Gamma_C$

it follows that indeed $\eta^{(i)}$ approximates π with respect to level B . Finally, observe that the breadth of $\eta^{(i)}$ is $k_{i-1} - 1$.

Recall that in phase one we have repeatedly lowered unlowered $+p$ -transitions from right to left. In doing so we have hereby possibly lowered certain $-p$ -transitions. The final hybrid semirun $\eta^{(r)}$ of phase one notably does not contain any unlowered $+p$ -transition. However, $\eta^{(r)}$ may still contain unlowered $-p$ -transitions. Lowering these will be subject of phase two. Yet, these unlowered $-p$ -transitions will be lowered rather from leftmost to rightmost (instead of from rightmost to leftmost as in phase one).

Phase two of the $(+p, -p)$ -Lowering Process: Lowering $-p$ -Transitions That Remain After Phase one Recall that $\mathcal{L} = B + \Gamma_C$ denotes our critical level. Also recall that $\eta^{(r)}$ is the final hybrid semirun in the sequence $\eta^{(0)}, \dots, \eta^{(r)}$ of phase one and approximates π with respect to level B . Note that by construction $\eta^{(r)}$ does not contain any unlowered $+p$ -transition. That is, all unlowered transitions of $\eta^{(r)}$ have operation $-p$ and there are as many of them as the breadth of $\eta^{(r)}$. Setting $\eta^{(0)'} = \eta^{(r)}$, phase two consists in showing the existence of a sequence of hybrid semiruns $\eta^{(1)'}, \dots, \eta^{(t)'}$ all of which do not contain any unlowered $+p$ -transition and in which each $\eta^{(i)'}$ has breadth k'_i satisfying $k'_i < k'_{i-1}$, where each $\eta^{(i)'}$ approximates π with respect to level B , and finally $\eta^{(t)'}$ is of breadth 0 (and is therefore already an $(N - \Gamma_C)$ -semirun).

Let us inductively assume that we have already defined the sequence $\eta^{(0)'}, \dots, \eta^{(i-1)'}$ for some $i \geq 1$ and that the breadth k'_{i-1} of $\eta^{(i-1)'}$ satisfies $k'_{i-1} > 0$.

Let $\eta^{(i-1)'}$ be $\alpha^{(i-1,0)'}\beta^{(i-1,1)'}\alpha^{(i-1,1)'} \dots \beta^{(i-1,k_{i-1})'}\alpha^{(i-1,k_{i-1})}'$. There is only one possible case for this phase since the critical ascending infix with respect to the leftmost unlowered $-p$ -transition $\beta^{(i-1,1)'}$ does not contain any unlowered $+p$ -transition since $\eta^{(i-1)'}$ does not. The construction of $\eta^{(i)'}$, as well as the proof that $\eta^{(i)'}$ approximates π with respect to level B , is completely dual to the proof of the second case of phase one and therefore omitted.

Example 33 Figure 9 illustrates an example of an application of the $(+p, -p)$ -lowering process. The topmost figure on the left is the starting hybrid semirun π . We begin the process by lowering the two unlowered $+p$ -transitions each by compensating them with a $-p$ -transition, then we enter phase two with one unlowered $-p$ -transition remaining, which we lower and compensate by shifting and cutting out portions inside the critical ascending infix by applying the Depumping Lemma.

Remark 34 In case π is a B -valley instead of a B -hill there is a dual variant of the $(+p, -p)$ -lowering process. The critical level would be adjusted to $\mathcal{L} = B - \Gamma_C$, for unlowered $+p$ -transitions one would define the critical descending infix to be the shortest suffix of $\alpha^{(0)}\beta^{(1)} \dots \alpha^{(j-1)}$ that starts in a configuration with counter

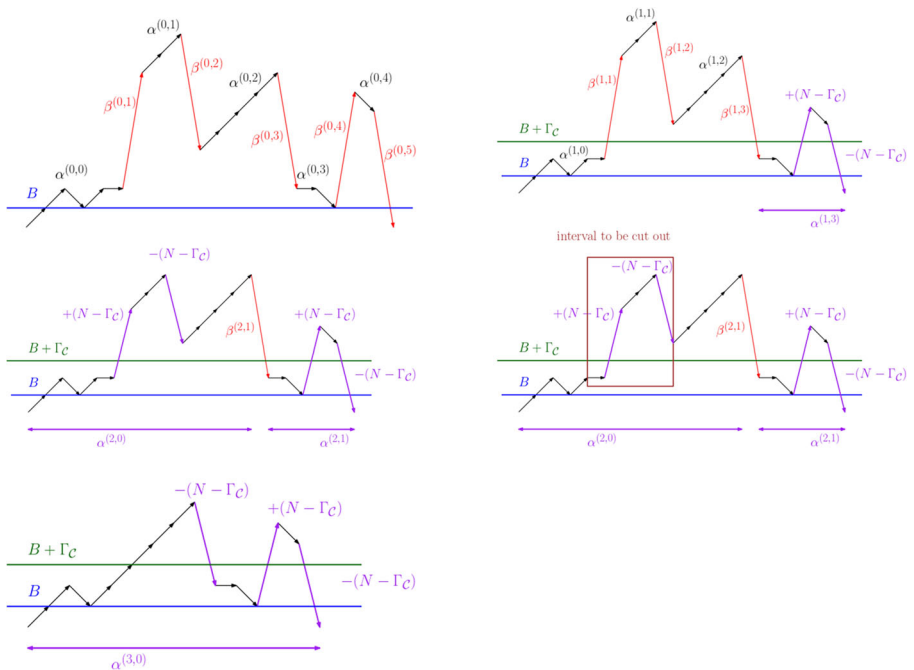


Fig. 9 Illustration of the $(+p, -p)$ -lowering process from Example 33 to be read from upper left to lower right

value at least \mathcal{L} , whereas for unlowered $-p$ -transitions one would define the critical ascending infix to be the shortest prefix of $\alpha^{(j)}\beta^{(j+1)}\alpha^{(j+1)} \dots \beta^{(k)}\alpha^{(k)}$ that ends in a configuration with counter value at least \mathcal{L} . The definition when a hybrid semirun approximates π with respect to level B would be defined analogously as in Definition 32, but where in Point 4 $\alpha^{(i)}$ is rather required to be a min-rising B -embedding of $\chi^{(i)}$ with the same source and target configuration as $\chi^{(i)}$, Point 5 (resp. Point 6) of Definition 32def approximates would rather require that every suffix (resp. every prefix) of $\phi_{\uparrow}(\gamma^{(i)})$ contains at least as many occurrences of [(resp.] as of] (resp. [).

Remark 35 Consider the following variants of the $(+p, -p)$ -lowering process for our B -hill π (dual variants can be formulated in the case when π is B -valley):

1. Consider the dual $(-p, +p)$ -lowering process: In phase one we lower the $-p$ -transitions from the leftmost to the rightmost and in phase two lower the $+p$ -transitions from the rightmost to the leftmost. That is, such a $(-p, +p)$ -lowering process produces a sequence of hybrid semiruns

$$\eta^{(0)}, \eta^{(1)}, \dots, \eta^{(s)}, \eta^{(0)'}, \eta^{(1)'}, \dots, \eta^{(t)'}$$

that all approximate π with respect to level B where $\eta^{(0)} = \pi$, $\eta^{(i)}$ is obtained from $\eta^{(i-1)}$ by lowering the leftmost unlowered $-p$ -transition of $\eta^{(i)}$, $\eta^{(0)'} = \eta^{(s)}$, $\eta^{(i+1)'}$ is obtained from $\eta^{(i)'}$ by lowering the rightmost unlowered $+p$ -transition, and finally $\eta^{(t)'}$ has breadth 0.

2. Consider again the sequence of hybrid semiruns

$$\eta^{(0)}, \eta^{(1)}, \dots, \eta^{(s)}, \eta^{(0)'}, \eta^{(1)'}, \dots, \eta^{(t)'}$$

of the $(+p, -p)$ -process (dually $(-p, +p)$ -process):

- (a) If $t > 0$, then observe that $\eta^{(t-1)'}$ and breadth 1 and contains precisely one unlowered transition, namely an unlowered $-p$ -transition (dually $+p$ -transition).
- (b) If however $t = 0$, then we claim that every prefix (dually suffix) of $\phi(\eta^{(0)'}) = \phi(\eta^{(s)})$ contains at least as many occurrences of [(dually occurrences of]) as occurrences of] (dually occurrences of [). Indeed, it follows immediately from the fact that each $\eta^{(i)}$ is obtained from $\eta^{(i-1)}$ by lowering a $+p$ -transition (dually a $-p$ -transition) either by shifting infixes and cutting out certain infixes ζ' for which $\phi(\zeta')$ contains as many occurrences of [as of], or by lowering a $+p$ -transition (dually $-p$ -transition) together with an unlowered $-p$ -transition (dually $+p$ -transition) to the right (dually to the left).
- (c) If $\phi(\pi)$ a priori contains strictly more occurrences of] than of [one can — by applying the $(+p, -p)$ -lowering process — obtain a sequence of hybrid semiruns

$$\eta^{(0)}, \eta^{(1)}, \dots, \eta^{(s)}, \eta^{(0)'}, \eta^{(1)'}, \dots, \eta^{(t)'}$$

where $t > 0$, all $\eta^{(i)}$ and $\eta^{(j)'}$ approximate π with respect to level B (and are therefore, as remarked above by bearing in mind that π is B -hill, in particular both min-rising and max-falling $(B - \Upsilon_C - \Gamma_C - 1)$ -embeddings of π with the same source and target configuration as π) and where the

breadth of $\eta^{(t-1)'}$ is 1. Dually, if $\phi(\pi)$ contains strictly more occurrences of [than of] one can — by applying the $(-p, +p)$ -lowering process — obtain a sequence of hybrid semiruns

$$\eta^{(0)}, \eta^{(1)}, \dots, \eta^{(s)}, \eta^{(0)'}, \eta^{(1)'}, \dots, \eta^{(t)'},$$

where $t > 0$, all $\eta^{(i)}$ and $\eta^{(j)'}$ approximate π with respect to level B (and are therefore both min-rising and max-falling $(B - \Upsilon_C - \Gamma_C - 1)$ -embeddings of π with the same source and target configuration as π) and where the breadth of $\eta^{(t-1)'}$ is 1.

7.1.2 π Contains a $\pm p$ -Transition Whose Source and Target Configuration Both Have Counter Value at Most $B + \Upsilon_C + \Gamma_C$

The presence of a $+p$ -transition (resp. $-p$ -transition) $q_i(z_i) \xrightarrow{\pi_i, N} q_{i+1}(z_{i+1})$ for which we have $\max\{z_i, z_{i+1}\} \leq B + \Upsilon_C + \Gamma_C$ implies

$z_{i+1} - (B + \Gamma_C) \leq \Upsilon_C$ (resp. $z_i - (B + \Gamma_C) \leq \Upsilon_C$), so the core problem is that in both cases it is not possible to apply the Bracket Lemma (Lemma 23) in the critical descending (resp. ascending) infix of such an unlowered transition. We thus have to find another way to compensate for lowering such transitions.

We next claim that firstly, any $+p$ -transition whose configurations both have a counter value at most $B + \Upsilon_C + \Gamma_C$ must be the first transition of π and secondly, any $-p$ -transition with the same property must be the last transition of π . Indeed, every $+p$ -transition $q_i(z_i) \xrightarrow{\pi_i, N} q_{i+1}(z_{i+1})$ that is not the first transition (i.e. $i > 0$) satisfies $z_i \geq B$ as π is a B -hill. As a consequence, we have $z_{i+1} \geq B + N > B + M_C > B + \Gamma_C + \Upsilon_C$, where the last inequality follows from M_C 's definition on page 15. Dually, if there exists a $-p$ -transition $q_i(z_i) \xrightarrow{\pi_i, N} q_{i+1}(z_{i+1})$ with $z_i \leq B + \Upsilon_C + \Gamma_C$ it must be the last transition $q_{n-1}(z_{n-1}) \xrightarrow{\pi_{n-1}, N} q_n(z_n)$ of π .

To finalize the proof it thus suffices to distinguish whether both the first transition of π is a $+p$ -transition with counter values at most $(B + \Upsilon_C + \Gamma_C)$ and the last transition of π is a $-p$ -transition with counter values at most $(B + \Upsilon_C + \Gamma_C)$, or this holds for precisely one of them. We thus distinguish these two cases, however in opposite order.

Case 1. The first transition $q_0(z_0) \xrightarrow{\pi_0, N} q_1(z_1)$ is a $+p$ -transition with counter values at most $(B + \Upsilon_C + \Gamma_C)$ and the last transition $q_{n-1}(z_{n-1}) \xrightarrow{\pi_{n-1}, N} q_n(z_n)$ is not a $-p$ -transition with counter values at most $(B + \Upsilon_C + \Gamma_C)$, or the first transition $q_0(z_0) \xrightarrow{\pi_0, N} q_1(z_1)$ is not a $+p$ -transition with counter values at most $(B + \Upsilon_C + \Gamma_C)$ and the last transition $q_{n-1}(z_{n-1}) \xrightarrow{\pi_{n-1}, N} q_n(z_n)$ is a $-p$ -transition with counter values at most $(B + \Upsilon_C + \Gamma_C)$.

We only treat the case when the first transition $q_0(z_0) \xrightarrow{\pi_0, N} q_1(z_1)$ is a $+p$ -transition with counter values at most $(B + \Upsilon_C + \Gamma_C)$ and the last transition

$q_{n-1}(z_{n-1}) \xrightarrow{\pi_{n-1}, N} q_{n-1}(z_{n-1})$ is not a $-p$ -transition with counter values at most $(B + \Upsilon_C + \Gamma_C)$, since the opposite case can be proven analogously.

Starting with $\eta^{(0)} = \pi$ we apply phase one of the $(+p, -p)$ -lowering process to π yielding a sequence of hybrid semiruns that all approximate π with respect to level B (and thus in particular — bearing in mind that π is B -hill — approximates π with respect to level $B - \Gamma_C - 1$)

$$\eta^{(0)}, \eta^{(1)}, \dots, \eta^{(s-1)}$$

in which (as above) $\eta^{(i)}$ is obtained from $\eta^{(i-1)}$ by lowering the rightmost unlowered $+p$ of $\eta^{(i-1)}$ however only until reaching the hybrid semirun $\eta^{(s-1)}$ that contains precisely one unlowered $+p$ -transition, namely the first transition

$q_0(z_0) \xrightarrow{\pi_0, N} q_1(z_1)$ of π , which has counter values at most $(B + \Upsilon_C + \Gamma_C)$ by assumption. It is important but straightforward to verify that despite the case we are in, it holds that $\eta^{(i)}$ approximates π with respect to level B (and also — bearing in mind that π is a B -hill — with respect to level $B - \Gamma_C - 1$) for all $i \in [1, s - 1]$.

Next, we will define a sequence of hybrid semiruns $\eta^{(s)} = \eta^{(0)'}, \eta^{(1)'}, \dots, \eta^{(t)'}$ in which $\eta^{(t)'}$ will be the desired $(N - \Gamma_C)$ -semirun as required by the lemma. For first defining $\eta^{(s)} = \eta^{(0)'}$ we make a case distinction for lowering the only $+p$ -transition $q_0(z_0) \xrightarrow{\pi_0, N} q_1(z_1)$ of $\eta^{(s-1)}$, which happens to have counter values at most $(B + \Upsilon_C + \Gamma_C)$ by assumption. For this assume $\eta^{(s-1)}$ has the following form

$$\eta^{(s-1)} = \alpha^{(s-1,0)} \beta^{(s-1,1)} \alpha^{(s-1,1)} \dots \beta^{(s-1,k_{s-1})} \alpha^{(s-1,k_{s-1})},$$

where we recall that $\beta^{(s-1,1)}$ equals $q_0(z_0) \xrightarrow{\pi_0, N} q_1(z_1)$. Observe that the critical descending infix of $\beta^{(s-1,1)}$ could possibly be empty, for instance if $z_1 \leq B + \Gamma_C$. We now make the following case distinction.

- In case the critical descending infix of $\beta^{(s-1,1)}$ contains an unlowered $-p$ -transition we define $\eta^{(s)}$ to be obtained from $\eta^{(s-1)}$ by lowering $\beta^{(s-1,1)}$ with the leftmost unlowered $-p$ -transition inside the critical descending infix as above. Thus, $\eta^{(s)}$ no longer contains any unlowered $+p$ -transition. It is again straightforward to verify that $\eta^{(s)}$ approximates π with respect to level B . Setting $\eta^{(0)'} = \eta^{(s)}$ we then construct the sequence $\eta^{(s)} = \eta^{(0)'}, \eta^{(1)'}, \dots, \eta^{(t)'}$ as usual, i.e. each $\eta^{(i)'}$ approximates π with respect to level B and is obtained from $\eta^{(i-1)'}$ by lowering the leftmost unlowered $-p$ -transition and where eventually the breadth of $\eta^{(i)'}$ is 0. Thus, as desired, the final $\eta^{(t)'}$ is an $(N - \Gamma_C)$ -semirun that is a min-rising and max-falling B -embedding of π that has the same source and target configuration as π . Since $\eta^{(t)'}$ has the same source and target configuration as π it follows that $\eta^{(t)'}$ is also a min-rising and max-falling $(B - \Upsilon_C - \Gamma_C - 1)$ -embedding of π as required by the lemma.
- In case the critical descending infix of $\beta^{(s-1,1)}$ does not contain any unlowered $-p$ -transition, we consider the shortest prefix ζ of the remaining suffix

$$\alpha^{(s-1,1)} \dots \beta^{(s-1,k_{s-1})} \alpha^{(s-1,k_{s-1})}$$

that ends in a configuration with counter value at most

$$\mathcal{L}' = z_1 - \Upsilon_C - 1$$

(where we recall that as above each $\alpha^{(i,j)}$ is viewed as a sequence of transitions). Indeed, we claim that ζ exists and moreover satisfies $\Delta(\zeta) < -\Upsilon_C$. Firstly, as π is a B -hill by assumption, we have that $z_1 - z_n > \Upsilon_C$. Secondly, since $\eta^{(s-1)}$ approximates π with respect to level B we have that $\eta^{(s-1)}$ ends in a configuration with counter value z_n . Thus, $\Delta(\alpha^{(s-1,1)} \dots \beta^{(s-1,k_{s-1})} \alpha^{(s-1,k_{s-1})}) = z_n - z_1 < -\Upsilon_C$ which implies that the prefix ζ exists and satisfies $\Delta(\zeta) < -\Upsilon_C$. We make the following final case distinction.

- In case ζ contains an unlowered $-p$ -transition, it must contain the leftmost unlowered $-p$ -transition, namely $\beta^{(s-1,2)}$. Similar as for the critical descending infix, we define $\eta^{(s)}$ to be obtained from $\eta^{(s-1)}$ by lowering $\beta^{(s-1,1)}$ together with $\beta^{(s-1,2)}$. Here it is important to note that $\eta^{(s)}$ is not necessarily a $(B - \Gamma_C)$ -embedding of π since we cannot rule out the existence of configurations appearing in $\alpha^{(s-1,1)}$ that have counter value B . Since $\eta^{(s-1)}$ was a B -embedding of the B -hill π with the same source and target configuration it follows however that $\eta^{(s)}$ is a $(B - \Gamma_C - 1)$ -embedding of π . Hence, $\eta^{(s)}$ approximates π with respect to level $B - \Gamma_C - 1$. Thus, $\eta^{(s)}$ no longer contains any unlowered $+p$ -transitions, however, possibly contains unlowered $-p$ -transitions. Recalling that $\eta^{(0)'} = \eta^{(s)}$ we define each of the remaining $\eta^{(i)'}$ to be obtained from $\eta^{(i-1)'}$ as usual but by retaining that each $\eta^{(i)'}$ approximates π with respect to level $B - \Gamma_C - 1$ (instead of level B). By construction, $\eta^{(0)'}$ has breadth 0 and thus is an $(N - \Gamma_C)$ -semirun that is a min-rising and max-falling $(B - \Gamma_C - 1)$ -embedding and hence — bearing in mind that π is a B -hill — in particular a min-rising and max-falling $(B - \Upsilon_C - \Gamma_C - 1)$ -embedding of π with the same source and target configuration as π .
- In case ζ does not contain any unlowered $-p$ -transition it follows that ζ is a prefix of $\alpha^{(s-1,1)}$, thus contains neither unlowered $+p$ -transitions nor unlowered $-p$ -transitions but possibly lowered ones. By an analogous reasoning as Point 2 of Remark 35 every occurrence of a lowered $-p$ -transition in $\alpha^{(s-1,1)}$ is preceded by a unique corresponding lowered $+p$ -transition again in $\alpha^{(s-1,1)}$. Thus, every prefix of $\phi(\zeta)$ contains at least as many occurrences of [as of]. Recalling that $\Delta(\zeta) < -\Upsilon_C$ we can hence apply the Bracket Lemma (Lemma 23) and the Depumping Lemma (Lemma 22) to ζ as in phase one. The final $\eta^{(s)}$ is obtained from $\eta^{(s-1)}$ by suitably shifting subsemiruns and cutting out certain subsemiruns whose ϕ -projection contains the same number of occurrences of [as of]. Similar as argued in the previous point it follows that $\eta^{(s)}$ approximates π with respect to level $B - \Gamma_C - 1$. Setting again $\eta^{(0)'} = \eta^{(s)}$ we define the sequence of hybrid semiruns $\eta^{(0)'}, \eta^{(1)'}, \dots, \eta^{(t)'}$ that all approximate π with respect to level $B - \Gamma_C - 1$ analogously as done

in the previous point. Again $\eta^{(t)'}$ is an $(N - \Gamma_C)$ -semirun that is a min-rising and max-falling $(B - \Gamma_C - 1)$ -embedding (and hence in particular a min-rising and max-falling $(B - \Upsilon_C - \Gamma_C - 1)$ -embedding of π with the same source and target configuration as π).

Remark 36 Our case (where the first transition of our B -hill is a $+p$ -transition with counter values at most $B + \Upsilon_C + \Gamma_C$ and the last transition is not a $-p$ -transition with counter values at most $(B + \Upsilon_C + \Gamma_C)$) allows the following “penultimate” process variants for our B -hill π (dual variants can be formulated in the case when π is B -valley):

1. The adjusted process here in Case 1 bears similar properties to those of the $(+p, -p)$ -lowering process seen in Remark 35. Specifically, if $\phi(\pi)$ contains strictly more occurrences of \rfloor than of \lceil one can obtain a sequence of hybrid semiruns

$$\eta^{(0)}, \eta^{(1)}, \dots, \eta^{(s)}, \eta^{(0)'}, \eta^{(1)'}, \dots, \eta^{(t-1)'},$$

where all $\eta^{(i)}$ and $\eta^{(j)'}$ approximate π with respect to level $B - \Gamma_C - 1$ (and are therefore — bearing in mind that π is a B -hill — in particular min-rising and max-falling $(B - \Upsilon_C - \Gamma_C - 1)$ -embeddings of π with the same source and target configuration as π) and where $\eta^{(t-1)'}$ has breadth 1 and contains precisely one unlowered $-p$ -transition.

2. Dually, the $(-p, +p)$ -lowering process mentioned in Remark 35, when applied to Case 1, is such that if $\phi(\pi)$ contains strictly more occurrences of \lceil than of \rfloor one can obtain a sequence of hybrid semiruns

$$\eta^{(0)}, \eta^{(1)}, \dots, \eta^{(s)}, \eta^{(0)'}, \eta^{(1)'}, \dots, \eta^{(t-1)'},$$

where all $\eta^{(i)}$ and $\eta^{(j)'}$ approximate π with respect to level $B - \Gamma_C - 1$ (and are therefore — bearing in mind that π is a B -hill — in particular min-rising and max-falling $(B - \Upsilon_C - \Gamma_C - 1)$ -embeddings of π with the same source and target configuration as π) and where $\eta^{(t-1)'}$ has breadth 1 and contains precisely one unlowered $+p$ -transition.

Case 2. The first transition $q_0(z_0) \xrightarrow{\pi_0, N} q_1(z_1)$ is a $+p$ -transition with counter values at most $(B + \Upsilon_C + \Gamma_C)$ and the last transition $q_{n-1}(z_{n-1}) \xrightarrow{\pi_{n-1}, N} q_{n-1}(z_{n-1})$ is a $-p$ -transition with counter values at most $(B + \Upsilon_C + \Gamma_C)$.

By our case we have that $z_0, z_n \leq B + \Upsilon_C + \Gamma_C - N \leq B + \Upsilon_C + \Gamma_C - M_C < B - \Upsilon_C - \Gamma_C - 1$, where the last inequality follows the definition of our constants on page 15.

Since π is a B -hill π is also a $(B - \Upsilon_C - \Gamma_C - 1)$ -hill. Moreover, obviously there are no $+p$ -transitions nor $-p$ -transitions in π whose source and target configuration both have counter value at most $B - 1$. Phrased differently, setting $B' = B - \Upsilon_C - \Gamma_C - 1$, we view π as a B' -hill that does not contain any $+p$ -transitions nor $-p$ -transitions whose source and target configuration have a counter value at most $(B' + \Upsilon_C + \Gamma_C)$. We can hence apply the $(+p, -p)$ -lowering process to π as described in the case of in Section 7.1.1 for B' instead of B , thus

yielding the sequence $\eta^{(0)}, \eta^{(1)}, \dots, \eta^{(s)}$ and $\eta^{(0)'}, \eta^{(1)'}, \dots, \eta^{(t)'}$ of hybrid semiruns that approximate π with respect to level B' and are therefore min-rising and max-falling B' -embeddings of π : note that we use the fact that they are indeed B' -embeddings as the construction in Section 7.1.1 guarantees rather than that they are $(B' - \Upsilon_C - \Gamma_C - 1)$ -embeddings. The final $\eta^{(t)'}$ is of breadth 0 and is hence a min-rising and max-falling $(N - \Gamma_C)$ -semirun that is a $(B - \Upsilon_C - \Gamma_C - 1)$ -embedding of π with the same source and target configuration as π , as required by the lemma.

**7.2 The Hill and Valley Lemma, Dependent on the Number of Occurrences
+p-Transitions as of -p-Transitions**

A closer look at the proof of Lemma 28 reveals that majority of occurrences of +p-transitions (resp. -p-transitions) implies the respective majority is preserved in the resulting $(N - \Gamma_C)$ -semirun.

Remark 37 The resulting $\eta^{(t)'}$ obtained from the B -hill (resp. B -valley) π satisfies the following:

- If $\phi(\pi)$ contains at least as many occurrences of [as of], then so does the resulting $(N - \Gamma_C)$ -semirun $\phi(\eta^{(t)'})$ satisfying Lemma 28.
- If $\phi(\pi)$ contains at least as many occurrences of] as of [, then so does the resulting $(N - \Gamma_C)$ -semirun $\phi(\eta^{(t)'})$ satisfying Lemma 28.

The following final remark stresses the fact that when our B -hill (resp. B -valley) π contains a number of occurrences of +p-transitions different from the number of -p-transitions, the lowering processes described in the previous section yield a penultimate hybrid semirun all but one of whose +p-transitions and -p-transitions are lowered. It is an immediate consequence of Point 2.c in Remarks 35 and 36.

Remark 38 Let π be an N -semirun that is a B -hill (dually a B -valley).

1. If $\phi(\pi)$ contains strictly more occurrences of] than of [one can obtain a sequence of hybrid semiruns

$$\eta^{(0)}, \eta^{(1)}, \dots, \eta^{(s)}, \eta^{(0)'}, \eta^{(1)'}, \dots, \eta^{(t-1)'}$$

in which $\eta^{(t-1)'}$ is a min-rising and max-falling $(B - \Upsilon_C - \Gamma_C - 1)$ -embedding (dually $(B + \Upsilon_C + \Gamma_C + 1)$ -embedding) of π with the same source and target configuration as π and where $\eta^{(t-1)'}$ has breadth 1 and contains precisely one unlowered -p-transition.

2. Analogously, if $\phi(\pi)$ contains strictly more occurrences of [than of] one can obtain a sequence of hybrid semiruns

$$\eta^{(0)}, \eta^{(1)}, \dots, \eta^{(s)}, \eta^{(0)'}, \eta^{(1)'}, \dots, \eta^{(t-1)'}$$

in which $\eta^{(t-1)'}$ is a min-rising and max-falling $(B - \Upsilon_C - \Gamma_C - 1)$ -embedding (dually $(B + \Upsilon_C + \Gamma_C + 1)$ -embedding) of π with the same source and target configuration as π and where $\eta^{(t-1)'}$ has breadth 1 and contains precisely one unlowered +p-transition.

8 The 5/6-Lemma

In this section, we introduce the 5/6-Lemma (Lemma 39), stating that any N -semirun with counter effect smaller than $5/6 \cdot N$ can be turned into an $(N - \Gamma_C)$ -semirun that is moreover an ℓ -embedding for all ℓ that are in distance at most $5/6 \cdot N$ from the counter values of the source and target configuration. It will be the main technical ingredient in the proof of the Small Parameter Theorem (Theorem 18). This section is devoted to proving the lemma, hereby making extensive use of the Hill and Valley Lemma (Lemma 28), the Depumping Lemma (Lemma 22), and the Bracket Lemma (Lemma 23) introduced in previous sections.

Recall that we have fixed a POCA $\mathcal{C} = (Q, P, R, q_{init}, F)$ with $P = \{p\}$ along with the constants $Z_C, \Gamma_C, \Upsilon_C, M_C$ on page 15.

Let us first introduce the 5/6-Lemma.

Lemma 39 (5/6-Lemma) *For all $N > M_C$ and all $\ell \in \mathbb{Z}$ and all N -semiruns π from $q_0(z_0)$ to $q_n(z_n)$ with $\text{VALUES}(\pi) \subseteq [0, 4N]$ satisfying $\max(z_0, z_n, \ell) - \min(z_0, z_n, \ell) \leq 5/6 \cdot N$ there exists an $(N - \Gamma_C)$ -semirun π' from $q_0(z_0)$ to $q_n(z_n)$ that is an ℓ -embedding of π such that $\text{VALUES}(\pi') \subseteq [\min(\pi) - \Gamma_C, \max(\pi) + \Gamma_C]$.*

Towards proving Lemma 39 let us fix

- some $N > M_C$,
- some $\ell \in \mathbb{Z}$,
- some N -semirun $\pi = q_0(z_0) \xrightarrow{\pi_0, N} q_1(z_1) \cdots \xrightarrow{\pi_{n-1}, N} q_n(z_n)$ from $q_0(z_0)$ to $q_n(z_n)$ satisfying $\text{VALUES}(\pi) \subseteq [0, 4N]$ and $\max(z_0, z_n, \ell) - \min(z_0, z_n, \ell) \leq 5/6 \cdot N$.

In order to prove the 5/6-Lemma we need to show the existence of some $(N - \Gamma_C)$ -semirun π' from $q_0(z_0)$ to $q_n(z_n)$ that is both an ℓ -embedding of π with $\text{VALUES}(\pi') \subseteq [\min(\pi) - \Gamma_C, \max(\pi) + \Gamma_C]$.

For this, let us define following two constants

$$B_{\min} = \min(z_0, z_n, \ell) - \Upsilon_C - 2\Gamma_C - 1 \quad \text{and} \quad B_{\max} = \max(z_0, z_n, \ell) + \Upsilon_C + 2\Gamma_C + 1$$

and observe that

$$\begin{aligned} B_{\max} - B_{\min} &= \max(z_0, z_n, \ell) - \min(z_0, z_n, \ell) + 2\Upsilon_C + 4\Gamma_C + 2 \\ &\leq 5/6 \cdot N + 2\Upsilon_C + 4\Gamma_C + 2 \\ &\leq 5/6 \cdot N + M_C/6 && (6) \\ &< N, && (7) \end{aligned}$$

where the penultimate inequality follows from the definitions of our constants on page 15.

We are particularly interested in subsemiruns of π that start and end in configurations with counter values in $[B_{\min} + 1, B_{\max} - 1]$. To categorize such subsemiruns into different types, we introduce the notion of crossing and doubly-crossing transitions.

Definition 40 A transition $q_i(z_i) \xrightarrow{\pi_i} q_{i+1}(z_{i+1})$ is called *crossing* if either

- $\pi_i = +p$ and we have $z_i < B_{\max} \leq z_{i+1}$ or $z_i \leq B_{\min} < z_{i+1}$, or
- $\pi_i = -p$ and we have $z_i > B_{\min} \geq z_{i+1}$ or $z_i \geq B_{\max} > z_{i+1}$.

If even moreover $z_i \leq B_{\min} \leq B_{\max} \leq z_{i+1}$ or $z_i \geq B_{\max} \geq B_{\min} \geq z_{i+1}$ we call π_i *doubly-crossing*.

We already refer to Fig. 10, where subsemiruns of a certain type (to be defined below) are depicted, some of whose transitions crossing transitions, some of whose are even doubly-crossing transitions.

Next, we introduce three particular types of subsemiruns of π starting and ending in configurations with counter values in $[B_{\min} + 1, B_{\max} - 1]$.

Definition 41 (Type I, II and III subsemiruns of π) A subsemirun $\pi[a, b]$ of π with source and target configuration in $Q \times [B_{\min} + 1, B_{\max} - 1]$ is

- of *Type I* if $\text{VALUES}(\pi[a, b]) \subseteq [B_{\min} + 1, B_{\max} - 1]$,
- of *Type II* if
 - $\text{VALUES}(\pi[a + 1, b - 1]) \cap [B_{\min} + 1, B_{\max} - 1] = \emptyset$, and
 - $\pi[a, b]$ does not contain any doubly-crossing transitions,
- of *Type III* if
 - $\text{VALUES}(\pi[a + 1, b - 1]) \cap [B_{\min} + 1, B_{\max} - 1] = \emptyset$, and
 - $\pi[a, b]$ contains at least one doubly-crossing transition.

Remark 42 All crossing transitions in a Type III semirun, except possibly the first or the last transition, are doubly-crossing.

Figure 10 shows an example of a Type II and of a Type III subsemirun.

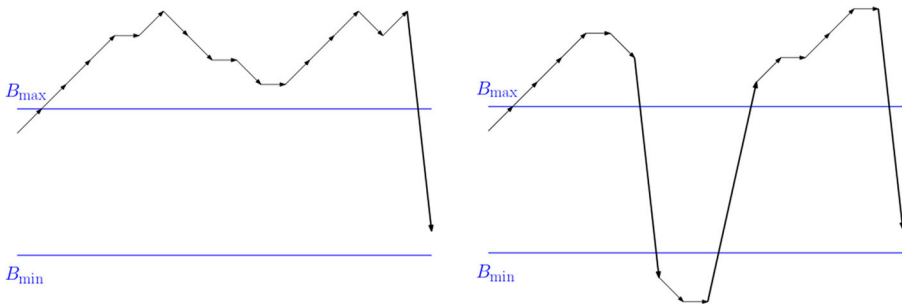


Fig. 10 On the left, an example of a Type II subsemirun. On the right, an example of a Type III subsemirun. Bold transitions are crossing, and the first two bold transitions of the figure on the right are moreover doubly crossing

The following lemma factorizes π into Type I, Type II and Type III subsemiruns, bearing in mind that both the source and target configuration of π have a counter value in $[B_{\min} + 1, B_{\max} - 1]$.

Lemma 43 *The N -semirun π can be factorized into Type I, Type II and Type III subsemiruns.*

Proof Let us first factorize π as

$$\pi = \pi[c_1, d_1]\pi[d_1, c_2]\pi[c_2, d_2]\pi[d_2, c_3] \cdots \pi[c_t, d_t], \tag{8}$$

where

- $\pi[c_i, d_i]$ are Type I and *maximal* (possibly empty), i.e. $\pi[c_i, d_i]$ is of Type I but neither $\pi[c_i - 1, d_i]$ nor $\pi[c_i, d_i + 1]$ is of Type I for all $i \in [1, t]$, and
- $\text{VALUES}(\pi[d_i + 1, c_{i+1} - 1]) \cap [B_{\min} + 1, B_{\max} - 1] = \emptyset$ for all $i \in [1, t - 1]$.

Now it suffices to show that each subsemirun $\pi[d_i, c_{i+1}]$ is either of Type II or of Type III. For this, let us make a case distinction on whether $\pi[d_i, c_{i+1}]$ contains a doubly-crossing transition or not.

For the first case, namely that $\pi[d_i, c_{i+1}]$ does contain a doubly-crossing transition, since $\text{VALUES}(\pi[d_i + 1, c_{i+1} - 1]) \cap [B_{\min} + 1, B_{\max} - 1] = \emptyset$, we have that $\pi[d_i, c_{i+1}]$ is of Type III by definition.

For the second case, namely that $\pi[d_i, c_{i+1}]$ does not contain any doubly-crossing transition, since $\text{VALUES}(\pi[d_i + 1, c_{i+1} - 1]) \cap [B_{\min} + 1, B_{\max} - 1] = \emptyset$ we have that $\pi[d_i, c_{i+1}]$ is of Type II by definition (Fig. 11). □

By Remark 26 in order to prove the existence of the desired $(N - \Gamma_C)$ -semirun it suffices to show it for Type I, Type II and Type III subsemiruns of π .

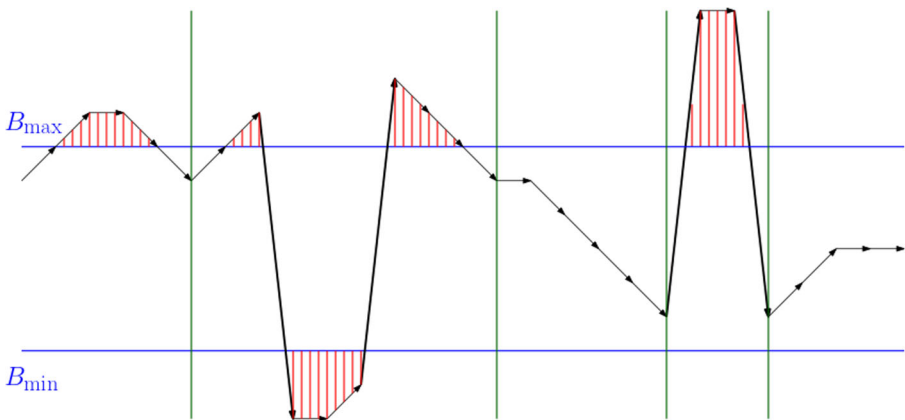


Fig. 11 In this figure, we provide an example factorization of a semirun π . A semirun π is divided into five subsemiruns, separated by vertical lines. The third and fifth subsemiruns are of Type I, the first and fourth subsemiruns are of Type II, and the second one is of Type III

Since Type I subsemiruns neither contain any $+p$ -transition nor any $-p$ -transition by (7), they are already $(N - \Gamma_C)$ -semiruns.

Let us now discuss the situation for Type II subsemiruns of π . If a Type II subsemirun is already a Type I subsemirun we are done as above. In case a Type II subsemirun ρ is not of Type I we first claim that ρ is either a B_{\min} -valley or a B_{\max} -hill. Indeed, if ρ is of Type II but not of Type I one can factorize ρ as

$$\rho = p_0(x_0) \xrightarrow{\rho_0, N} p_1(x_1) \cdots \xrightarrow{\rho_{m-1}, N} p_m(x_m),$$

where

1. $m \geq 2$,
2. $x_0, x_m \in [B_{\min} + 1, B_{\max} - 1]$, and
3. either $x_i \in [0, B_{\min}]$ for all $i \in [1, m - 1]$ or $x_i \in [B_{\max}, 4N]$ for all $i \in [1, m - 1]$,

where Point 3 follows from the absence of doubly-crossing transitions.

First assume that $x_i \in [B_{\max}, 4N]$ for all $i \in [1, m - 1]$. In this case any $+p$ -transition (resp. $-p$ -transition) ends (resp. starts) in a configuration with counter value strictly larger than $B_{\min} + N$. Due to the definition of our constants on page 15 we have

$$\begin{aligned} x_0 + \Upsilon_C, x_n + \Upsilon_C &< B_{\max} + \Upsilon_C \\ &= B_{\min} + (B_{\max} - B_{\min}) + \Upsilon_C \\ &\leq B_{\min} + 5/6 \cdot N + 3\Upsilon_C + 4\Gamma_C + 2 \\ &< B_{\min} + 5/6 \cdot N + M_C/6 \\ &< B_{\min} + N, \end{aligned}$$

hence ρ is a B_{\max} -hill.

Secondly, in case $x_i \in [0, B_{\min}]$ for all $i \in [1, m - 1]$ it can analogously be shown that ρ is B_{\min} -valley.

The existence of the desired $(N - \Gamma_C)$ -semirun ρ' that is an ℓ -embedding of the Type II semirun ρ with the same source and target configuration as ρ follows immediately from the following claim, which itself (with a short justification below) is a consequence of the Hill and Valley Lemma (Lemma 28); thanks to the fact that the Hill and Valley Lemma guarantees the resulting $(N - \Gamma_C)$ -semiruns to be min-rising and max-falling, we can even guarantee $\text{VALUES}(\rho') \subseteq [\min(\rho), \max(\rho)]$.

Claim 2 *For every N -semirun ρ that is either a B -hill with $B \geq \ell + \Upsilon_C + \Gamma_C + 1$ or a B -valley with $B \leq \ell - \Upsilon_C - \Gamma_C - 1$, there exists an $(N - \Gamma_C)$ -semirun that is both a min-rising and max-falling ℓ -embedding of ρ with same source and target configuration as ρ .*

That the Hill and Valley Lemma produces an ℓ -embedding that has the same source and target configuration is important here. Indeed, generally speaking if ρ is any B -hill and ρ' is any k -embedding of ρ with the same source and target configuration as ρ and where $k < B$, then ρ' is also a k' -embedding of ρ for all $k' < k$.

Dually, if ρ is any B -valley and ρ' is a k -embedding of ρ with with the same source and target configuration as ρ and where $k > B$, then ρ' is also an k' -embedding of ρ for all $k' > k$.

For the rest of this section it now suffices to prove that for every Type III subsemirun ρ of π there exists an $(N - \Gamma_C)$ -semirun ρ' that is an ℓ -embedding of ρ with the same source and target configuration as ρ and that moreover satisfies $\text{VALUES}(\rho') \subseteq [\min(\rho) - \Gamma_C, \max(\rho) + \Gamma_C]$.

8.1 Lowering Type III Subsemiruns

For the rest of the section let us fix a Type III subsemirun ρ of π . Let us factorize ρ by its crossing semitransitions, i.e. as

$$\rho = \alpha^{(0)}\beta^{(1)}\alpha^{(1)} \dots \beta^{(n)}\alpha^{(n)},$$

where $\beta^{(1)}, \dots, \beta^{(n)}$ is an enumeration of the crossing semitransitions of ρ and each $\alpha^{(i)}$ is a (possibly empty) N -subsemirun of ρ . It is worth mentioning that, indeed abusing notation, for the rest of this section we refer to n as the number of crossing semitransitions of ρ , rather than the number of transitions of our original N -run π .

An example factorization is shown in Fig. 10, where the crossing transitions are depicted in bold. We remark that the only crossing transitions of ρ that are not doubly-crossing can possibly only be the first or the last one (or both).

We intend to now factorize ρ , if possible, into hills and valleys. In order to do this let us first introduce the notions of B -hill candidate and B -valley candidate.

Definition 44 Let

$$\chi = p_0(x_0) \overset{\chi_{0,N}}{\dashrightarrow} p_1(x_1) \dots \overset{\chi_{m-1,N}}{\dashrightarrow} p_m(x_m)$$

be an N -semirun. We say χ is a B -hill candidate if $x_0, x_m < B$ and $x_i \geq B$ for all $i \in [1, m - 1]$, respectively a B -valley candidate if $x_0, x_m > B$ and $x_i \leq B$ for all $i \in [1, m - 1]$.

Note that every B -hill is a B -hill candidate but not vice versa, since being a B -hill moreover requires $+p$ -transitions to end at a configuration with counter value strictly larger than $x_n + \Upsilon_C$ and $-p$ -transitions to start at a configuration with counter value strictly larger than $x_0 + \Upsilon_C$. A similar remark applies to B -valleys and B -valley candidates.

For the rest of this section we assume *without loss of generality* that the crossing transition β_1 is a $+p$ -transition. The case when β_1 is $-p$ -transition can be proven analogously.

It follows that if the number n of crossing transitions is even, then there is a unique factorization

$$\rho = \alpha^{(0)}\sigma^{(1)}\alpha^{(2)}\sigma^{(2)}\alpha^{(4)}\sigma^{(3)}\alpha^{(6)} \dots \sigma^{(n/2)}\alpha^{(n)}, \tag{9}$$

where $\sigma^{(i)} = \beta^{(2i-1)}\alpha^{(2i-1)}\beta^{(2i)}$ is a B_{\max} -hill candidate, $\beta^{(2i-1)}$ is a $+p$ -transition and $\beta^{(2i)}$ is a $-p$ -transition for all $i \in [1, n/2]$. Indeed, this immediately follows

from the definition of crossing transitions and the fact that $\alpha^{(2i-1)}$ does not contain any configuration with counter value strictly less than B_{\max} .

Therefore our proof makes first a case distinction on the parity of the number n of crossing transitions.

Case A: The number of crossing transitions n is even.

Our proof next makes a case distinction on the number of B_{\max} -hill candidates $\sigma^{(i)}$ in the factorization (9) that are in fact B_{\max} -hills.

Case A.1: All of the B_{\max} -hill candidates $\sigma^{(i)}$ in (9) are in fact B_{\max} -hills.

Since each $\sigma^{(i)} = \beta^{(2i-1)}\alpha^{(2i-1)}\beta^{(2i)}$ from (9) is a B_{\max} -hill, to each $\sigma^{(i)}$ we can apply Claim 2 claim embedding and obtain an $(N - \Gamma_{\mathcal{C}})$ -semirun $\widehat{\sigma^{(i)}}$ that is both a min-rising and max-falling ℓ -embedding of $\sigma^{(i)}$ with the same source and target configuration as $\sigma^{(i)}$. Thus, it remains to show the same for $\alpha^{(2i)}$ for each $i \in [0, n/2]$. We do this separately for $\alpha^{(0)}$, $\alpha^{(n)}$ and finally for those $\alpha^{(2i)}$, where $i \in [1, n/2 - 1]$.

Let us first show it for $\alpha^{(0)}$. The proof for $\alpha^{(n)}$ is completely analogous. If $\alpha^{(0)}$ is empty (which would imply that $\beta^{(1)}$ is crossing but not doubly-crossing), there is nothing to show. Let us therefore assume that $\alpha^{(0)}$ is not empty. It follows that $\beta^{(1)}$ must be a doubly-crossing $+p$ -transition by Remark 42. Since $\beta^{(1)}$ is the first crossing transition (even doubly-crossing) and a $+p$ -transition and moreover ρ is of Type III one can factorize $\alpha^{(0)}$ as

$$\alpha^{(0)} = \alpha^{(0,0)}\sigma^{(0,1)}\alpha^{(0,1)} \dots \sigma^{(0,k)},$$

where $\alpha^{(0,j)}$ satisfies $\text{VALUES}(\alpha^{(0,j)}) \subseteq [B_{\max} - N, B_{\min} + 1]$ for all $j \in [0, k]$ and $\sigma^{(0,j)}$ is a $(B_{\max} - N - 1)$ -valley candidate for all $j \in [1, k]$. It immediately follows that each $\alpha^{(0,j)}$ does not contain any $+p$ -transition nor any $-p$ -transition, and is hence already an $(N - \Gamma_{\mathcal{C}})$ -semirun. Finally we claim that each $\sigma^{(0,j)}$ is in fact a $(B_{\max} - N - 1)$ -valley. Indeed, firstly the target configuration of each $+p$ -transition in $\sigma^{(0,j)}$ has a counter value at most B_{\min} and hence a source configuration with counter value at most $B_{\min} - N < B_{\max} - N - 1 - \Upsilon_{\mathcal{C}}$, where the inequality follows from definition of B_{\min} and B_{\max} from page 37. Secondly and analogously, the source configuration of each $-p$ -transition in $\sigma^{(0,j)}$ has a counter value of at most B_{\min} and hence a target configuration with counter value at most $B_{\min} - N < B_{\max} - N - 1 - \Upsilon_{\mathcal{C}}$. Thus, to each $\sigma^{(0,j)}$ we can apply Claim 2 to obtain an $(N - \Gamma_{\mathcal{C}})$ -semirun $\widehat{\sigma^{(0,j)}}$ that is a min-rising and max-falling ℓ -embedding of $\sigma^{(0,j)}$ with the same source and target configuration as $\sigma^{(0,j)}$. Hence, by appropriately concatenationg the $\alpha^{(0,j)}$ with the $\widehat{\sigma^{(0,j)}}$ we obtain the desired $(N - \Gamma_{\mathcal{C}})$ -semirun that is an ℓ -embedding of $\alpha^{(0)}$.

It now only remains and suffices to show that for each $\alpha^{(2i)}$ with $i \in [1, n/2 - 1]$, that there exists an $(N - \Gamma_{\mathcal{C}})$ -semirun that is a min-rising and max-falling ℓ -embedding of $\alpha^{(2i)}$ with same source and target configuration as $\alpha^{(2i)}$. Again by Remark 42 any such $\alpha^{(2i)}$ succeeds $\sigma^{(i)} = \beta^{(2i-1)}\alpha^{(2i-1)}\beta^{(2i)}$ and thus succeeds the doubly-crossing transition $\beta^{(2i)}$ and analogously precedes $\sigma^{(i+1)} = \beta^{(2i+1)}\alpha^{(2i+1)}\beta^{(2i+2)}$ and thus precedes the doubly-crossing $\beta^{(2i+1)}$. Therefore, analogously as done for $\alpha^{(0)}$, one can factorize $\alpha^{(2i)}$ as

$$\alpha^{(2i)} = \alpha^{(2i,0)}\sigma^{(2i,1)}\alpha^{(2i,1)} \dots \sigma^{(2i,k)}\alpha^{(2i,k)}$$

for some $k \geq 0$, where $\alpha^{(2i,j)}$ satisfies $\text{VALUES}(\alpha^{(2i,j)}) \subseteq [B_{\max} - N, B_{\min}]$ (and is thus already an $(N - \Gamma_C)$ -semirun) for all $j \in [0, k]$ and $\sigma^{(2i,j)}$ is a $(B_{\max} - N - 1)$ -valley candidate that is in fact a $(B_{\max} - N - 1)$ -valley for all $j \in [1, k]$.

Case A.2: All but one of the B_{\max} -hill candidates $\sigma^{(i)}$ in (9) are in fact B_{\max} -hills.
 Recall the factorization

$$\rho = \alpha^{(0)}\sigma^{(1)}\alpha^{(2)}\sigma^{(2)}\alpha^{(4)}\sigma^{(3)}\alpha^{(6)} \dots \sigma^{(n/2)}\alpha^{(n)}$$

from (9) where each $\sigma^{(i)} = \beta^{(2i-1)}\alpha^{(2i-1)}\beta^{(2i)}$ is a B_{\max} -hill candidate for all $i \in [1, n/2]$.

First let us show that, in case one such B_{\max} -hill candidate is not a B_{\max} -hill, then it must be either $\sigma^{(1)}$ or $\sigma^{(n/2)}$. For every of the remaining $j \in [2, n/2 - 1]$ we have that $\sigma^{(j)} = \beta^{(2j-1)}\alpha^{(2j-1)}\beta^{(2j)}$ is such that $\beta^{(2j-1)}$ and $\beta^{(2j)}$ are both doubly-crossing, implying that $\sigma^{(j)}$ is a B_{\max} -hill: indeed, both the source and target configuration of $\sigma^{(j)}$ have a counter value at most $B_{\min} < B_{\max} - \Upsilon_C$, which is sufficient since every $+p$ -transition (resp. $-p$ -transition) of $\sigma^{(j)}$ ends (resp. starts) in a configuration with counter value at least B_{\max} .

Let us assume without loss of generality that $\sigma^{(1)} = \beta^{(1)}\alpha^{(1)}\beta^{(2)}$ is the only B_{\max} -hill candidate that is not a B_{\max} -hill, the case when $\sigma^{(n/2)}$ is not a B_{\max} -hill can be treated analogously.

Clearly, by the above reasoning, either $\beta^{(1)}$ or $\beta^{(2)}$ must not be doubly-crossing. Without loss of generality let us assume that the first crossing transition $\beta^{(1)}$ is not doubly-crossing (for $\beta^{(2)}$ not doubly-crossing implies $n = 2$ by definition of Type III and Remark 42; this case is thus included in the dual case when $\sigma^{(n/2)}$ is not a B_{\max} -hill and the last crossing transition $\beta^{(n)}$ is not doubly-crossing).

Remarking that $\alpha^{(0)}$ must be empty by our case, one can now factorize our N -semirun ρ as

$$\rho = \left(\beta^{(1)}\alpha^{(1)}\right)\beta^{(2)}\left(\alpha^{(2)}\sigma^{(2)}\alpha^{(4)} \dots \sigma^{(n/2)}\alpha^{(n)}\right),$$

where $\beta^{(2)}$ is a $-p$ -transition. For finishing this case we will proceed as follows.

1. Firstly we show the existence of an $(N - \Gamma_C)$ -semirun that is both a min-rising and max-falling ℓ -embedding of $\beta^{(1)}\alpha^{(1)}$ with the same source and target configuration as $\beta^{(1)}\alpha^{(1)}$.
2. Secondly, let us assume that $\beta^{(2)}$ is an N -semirun from $q(x)$ to $q'(y)$, say, and moreover that $\alpha^{(2)}\sigma^{(2)}\alpha^{(4)} \dots \sigma^{(n/2)}\alpha^{(n)}$ is an N -semirun from $q'(y)$ to $q''(z)$, say. Noting that $\beta^{(2)} = q(x) \xrightarrow{-p, N} q'(y)$, we explicitly lower $\beta^{(2)}$ into the $(N - \Gamma_C)$ -semirun $q(x) \xrightarrow{-p, N - \Gamma_C} q'(y + \Gamma_C)$, which is — since $\beta^{(2)}$ is doubly-crossing — obviously both a min-rising and max-falling ℓ -embedding of $\beta^{(2)}$ from $q(x)$ to $q'(y + \Gamma_C)$. Finally, we show the existence of an $(N - \Gamma_C)$ -semirun that is an ℓ -embedding of $\alpha^{(2)}\sigma^{(2)}\alpha^{(4)} \dots \sigma^{(n/2)}\alpha^{(n)}$ from $q'(y + \Gamma_C)$ to $q''(z)$ all of whose counter values lie in $[\min(\rho) - \Gamma_C, \max(\rho) + \Gamma_C]$.

Let us first show Point 1. Since $\beta^{(1)}$ is not doubly-crossing and $\sigma^{(1)} = \beta^{(1)}\alpha^{(1)}\beta^{(2)}$ is a B_{\max} -hill candidate that is not a B_{\max} -hill, the only reason for the latter is the existence of a $-p$ -transition τ such that the counter values of the

source configuration of τ and $\sigma^{(1)}$ have an absolute difference at most Υ_C . Such a transition τ must have a source configuration with counter value in the interval $[B_{\max}, B_{\max} + \Upsilon_C - 1]$ and $\sigma^{(1)}$ must then necessarily have a source configuration with a counter value in the interval $[B_{\max} - \Upsilon_C, B_{\max} - 1]$. Such a violation can only happen for $\tau = \beta^{(2)}$. As a consequence, the target configuration $q(x)$ of $\alpha^{(1)}$ has a counter value inside $[B_{\max}, B_{\max} + \Upsilon_C - 1]$. Recalling that $\beta^{(1)}$ is not doubly-crossing one can (analogously as has been done in Case A.1) factorize $\beta^{(1)}\alpha^{(1)}$ as

$$\beta^{(1)}\alpha^{(1)} = \chi^{(1)}\xi^{(1)} \dots \chi^{(k)}\xi^{(k)},$$

for some $k \geq 0$, where each $\chi^{(i)}$ is a $(B_{\min} + N + 1)$ -hill and each $\xi^{(i)}$ satisfies $\text{VALUES}(\xi^{(i)}) \subseteq [B_{\max}, B_{\min} + N]$. Again, analogously as has been done in Case A.1, to each of the $\chi^{(i)}$ we can apply Claim 2 to turn them into a suitable min-rising and max-falling $(N - \Gamma_C)$ -semirun that is an ℓ -embedding of $\chi^{(i)}$ with the same source and target configuration as $\chi^{(i)}$, whereas each of the $\xi^{(i)}$ are already $(N - \Gamma_C)$ -semiruns since they do not contain any $+p$ -transitions nor $-p$ -transitions.

Let us finally show Point 2. Consider the remaining factorization

$$\gamma = \alpha^{(2)}\sigma^{(2)}\alpha^{(4)} \dots \sigma^{(n/2)}\alpha^{(n)} \tag{10}$$

from $q'(y)$ to $q''(z)$. We need prove the existence of an $(N - \Gamma_C)$ -semirun from $q'(y + \Gamma_C)$ to $q''(z)$ that is an ℓ -embedding of γ and whose counter values lie in $[\min(\gamma) - \Gamma_C, \max(\gamma) + \Gamma_C]$.

We first claim that $\Delta(\gamma) > \Upsilon_C$. Since $x \in [B_{\max}, B_{\max} + \Upsilon_C - 1]$ it follows that the counter value y of γ 's source configuration $q'(y)$ satisfies $y \in [B_{\max} - N, B_{\max} + \Upsilon_C - 1 - N]$. Moreover, the target configuration $q''(z)$ of γ is the target configuration of our Type III N -semirun ρ , thus $z \in [B_{\min} + 1, B_{\max} - 1]$. Hence by the definition of our constants on page 15 we have

$$\begin{aligned} \Delta(\gamma) &\geq B_{\min} + 1 - (B_{\max} + \Upsilon_C - 1 - N) \\ &> N - (B_{\max} - B_{\min}) - \Upsilon_C \\ &\stackrel{(6)}{\geq} N - (5/6 \cdot N + 2\Upsilon_C + 4\Gamma_C + 2) - \Upsilon_C \\ &= N/6 - 2\Upsilon_C - 4\Gamma_C - 2 - \Upsilon_C \\ &> M_C/6 - 2\Upsilon_C - 4\Gamma_C - 2 - \Upsilon_C \\ &= M_C/6 - 4\Upsilon_C - 4\Gamma_C - 2 + \Upsilon_C \\ &> (M_C/6 - 4(\Upsilon_C + \Gamma_C + 1)) + \Upsilon_C \\ &> \Upsilon_C. \end{aligned}$$

Recall that each $\sigma^{(i)}$ is a B_{\max} -hill for all $i \in [2, n/2]$ by our case. Analogously, as has been done in Case A.1, for each $i \in [1, n/2]$ one can factorize $\alpha^{(2i)}$ as

$$\alpha^{(2i)} = \alpha^{(2i,0)}\sigma^{(2i,1)} \dots \sigma^{(2i,k_i)}\alpha^{(2i,k_i)},$$

where $\alpha^{(2i,j)}$ satisfies $\text{VALUES}(\alpha^{(2i,j)}) \subseteq [B_{\max} - N, B_{\min} + 1]$ for each $j \in [0, k_i]$ and $\sigma^{(2i,j)}$ is a $(B_{\max} - N - 1)$ -valley for each $j \in [1, k_i]$: more precisely for the final $\alpha^{(n)}$ we have $\text{VALUES}(\alpha^{(n)}) \subseteq [B_{\max} - N, B_{\min} + 1]$, however for all $i \in [1, n/2 - 1]$ we have $\alpha^{(2i)} \subseteq [B_{\max} - N, B_{\min}]$.

It is important to remark that each $\alpha^{(2i,j)}$ is already an $(N - \Gamma_C)$ -semirun since it does not contain any $+p$ -transition nor any $-p$ -transition. The following remark summarizes the factorization of γ .

Remark 45 Our N -semirun γ from $q'(y)$ to $q''(z)$ can be written as

$$\gamma = \alpha^{(2)}\sigma^{(2)}\alpha^{(4)} \dots \sigma^{(n/2)}\alpha^{(n)} = \alpha^{(2)} \left(\prod_{i=2}^{n/2} \sigma^{(i)} \alpha^{(2i,0)} \left(\prod_{j=1}^{k_i} \sigma^{(2i,j)} \alpha^{(2i,j)} \right) \right), \tag{11}$$

where

1. each $\widehat{\sigma}^{(i)}$ is a B_{\max} -hill,
2. each $\sigma^{(2i,j)}$ is a $(B_{\max} - N - 1)$ -valley,
3. each $\alpha^{(2i,j)}$ is already an $(N - \Gamma_C)$ -semirun,
4. $\Delta(\gamma) > \Upsilon_C$, and
5. every configuration in γ (except for possibly the target configuration $q''(z)$) has a counter value in $[0, B_{\min}] \cup [B_{\max}, 4N]$ whose absolute difference with ℓ is thus strictly larger than Γ_C (recall the definition of B_{\min} and B_{\max} of page 37).

We can now obtain suitable $(N - \Gamma_C)$ -semiruns with the same source and target configuration for any of the above hills and valleys by applying the Hill and Valley Lemma.

Remark 46 By applying the Hill and Valley Lemma (Lemma 28) we obtain the following.

1. For each of the B_{\max} -hills $\sigma^{(i)}$ there exists an $(N - \Gamma_C)$ -semirun $\widehat{\sigma}^{(i)}$ that is both a min-rising and max-falling $(B_{\max} - \Upsilon_C - \Gamma_C - 1)$ -embedding of $\sigma^{(i)}$ from the same source and target configuration as $\sigma^{(i)}$. In particular, since $\sigma^{(i)}$ is a B_{\max} -hill and $B_{\max} - \Upsilon_C - \Gamma_C - 1 > \ell + \Gamma_C$ it follows that $\widehat{\sigma}^{(i)}$ is in fact an ℓ -embedding of $\sigma^{(i)}$ all of whose configurations have a counter value whose absolute difference with ℓ is strictly larger than Γ_C (except for the exotic case when $i = n/2$ and γ in fact ends with $\sigma^{(i)}$, and hence the last configuration of $\sigma^{(i)}$ happens to be the last configuration $q''(z)$ of γ ; recalling that $z \in [B_{\min} + 1, B_{\max} - 1]$).
2. For each of the $(B_{\max} - N - 1)$ -valleys $\sigma^{(2i,j)}$ there exists an $(N - \Gamma_C)$ -semirun $\widehat{\sigma}^{(2i,j)}$ that is both a min-rising and max-falling $(B_{\max} - N - 1 + \Upsilon_C + \Gamma_C + 1)$ -embedding of $\sigma^{(2i,j)}$ with the same source and target configuration as $\sigma^{(2i,j)}$. In particular, since $\sigma^{(2i,j)}$ is a $(B_{\max} - N - 1)$ -valley and

$$B_{\max} - N + \Upsilon_C + \Gamma_C \stackrel{(7)}{<} B_{\min} + \Upsilon_C + \Gamma_C \leq \ell - \Gamma_C$$

(where the last inequality follows from the definition of B_{\min} on page 37), it follows that $\widehat{\sigma}^{(2i,j)}$ is in fact an ℓ -embedding of $\sigma^{(2i,j)}$ all of whose configurations have a counter value whose absolute difference with ℓ is strictly larger than

Γ_C (except, similar as above, for the exotic case when $i = n/2$, $j = k_i$ and γ in fact ends with $\sigma^{(2i,j)}$, and hence the last configuration $\sigma^{(2i,j)}$ happens to be the last configuration $q''(z)$ of γ).

It is worth pointing out that applying the remark immediately would only yield the existence of an $(N - \Gamma_C)$ -semirun γ' that is both a min-rising and max-falling ℓ -embedding of γ with the same source configuration $q'(y)$ and the same target configuration $q''(z)$ as γ such that $\text{VALUES}(\gamma') \subseteq [\min(\gamma) - \Gamma_C, \max(\gamma) + \Gamma_C]$. However we need to show the existence of such an ℓ -embedding rather from $q'(y + \Upsilon_C)$ to $q''(z)$. For this, we make a final case distinction on whether among the B_{\max} -hills $\sigma^{(i)}$ and the $(B_{\max} - N - 1)$ -valleys $\sigma^{(2i,j)}$ there exists one whose ϕ -projection contains strictly more occurrences of \lceil as occurrences of \rfloor .

- *Case A.2.i: Among the B_{\max} -hills $\sigma^{(i)}$ and the $(B_{\max} - N - 1)$ -valleys $\sigma^{(2i,j)}$ there exists one whose ϕ -projection contains strictly more occurrences of \lceil as occurrences of \rfloor .*

Without loss of generality let us assume that there exists some $s \in [2, n/2]$ such that $\sigma^{(s)} = \beta^{(2s-1)}\alpha^{(2s-1)}\beta^{(2s)}$ is a B_{\max} -hill for which $\phi(\sigma^{(s)})$ contains strictly more occurrences of \lceil as occurrences of \rfloor — the case when there is a $(B_{\max} - N - 1)$ -valley $\sigma^{(2s,j)}$ for which $\phi(\sigma^{(2s,j)})$ has the above property can be proven analogously.

Assume $\sigma^{(s)} = \beta^{(2s-1)}\alpha^{(2s-1)}\beta^{(2s)}$ has source configuration $r_1(x_1)$ and target configuration $r_2(x_2)$, say. Since $\beta^{(2s-1)}$ was surely neither the first nor the last crossing transition of ρ (recall that $s \geq 2$), it follows that $\beta^{(2s-1)}$ is doubly-crossing by Remark 42, and therefore $x_1 \leq B_{\min}$. Recalling the notion of hybrid semirun (Definition 29) we now apply Point 2 of Remark 38 to our B_{\max} -hill $\sigma^{(s)}$ and obtain a hybrid semirun η

- whose source configuration is $r_1(x_1)$ and whose target configuration is $r_2(x_2)$,
- that is both a min-rising and max-falling $(B_{\max} - \Upsilon_C - \Gamma_C - 1)$ -embedding of $\sigma^{(s)}$, and
- that has breadth 1 and contains precisely one unlowered $+p$ -transition.

From the above and the fact that $\sigma^{(s)}$ is a B_{\max} -hill the following remark follows.

Remark 47 All configurations of η (except possibly the target configuration $r_2(x_2)$ in the exotic case when γ ends with $\sigma^{(s)}$) have a counter value whose absolute difference with ℓ is strictly larger than Γ_C . Moreover one can write η as $\eta = \alpha\beta\alpha'$, where for some intermediate configurations $r'_1(x'_1)$ and $r'_2(x'_2)$ we have that

- α is an $(N - \Gamma_C)$ -semirun from $r_1(x_1)$ to $r'_1(x'_1)$,
- β is an N -semirun $r'_1(x'_1) \xrightarrow{+p,N} r'_2(x'_2)$ that is a $+p$ -transition, i.e. $x'_2 = x'_1 + N$, and
- α' is an $(N - \Gamma_C)$ -semirun from $r'_2(x'_2)$ to $r_2(x_2)$.

Let $\widehat{\beta}$ denote the lowering of β , i.e. $\widehat{\beta}$ is the $(N - \Gamma_C)$ -semirun $r'_1(x'_1) \xrightarrow{+p, N-\Gamma_C} r'_2(x'_2 - \Gamma_C)$. By Remark 47 it follows that the $(N - \Gamma_C)$ -semirun

$$\theta = ((\alpha\widehat{\beta}) + \Gamma_C) \alpha'$$

from $r'_1(x_1 + \Gamma_C)$ to $r_2(x_2)$ is an ℓ -embedding of η . Bearing in mind our factorization of γ from Remark 11 and taking into account Remark 46 we obtain that

$$\widetilde{\gamma^{(1)}} = \left(\left(\widehat{\alpha^{(2)}} \left(\prod_{i=2}^{s-1} \widehat{\sigma^{(i)}} \alpha^{(2i,0)} \left(\prod_{j=1}^{k_i} \widehat{\sigma^{(2i,j)}} \alpha^{(2i,j)} \right) \right) \right) + \Gamma_C \right) \theta$$

is an $(N - \Gamma_C)$ -semirun from $q'(y + \Gamma_C)$ to $r'_2(x'_2)$ that is an ℓ -embedding of γ 's prefix N -semirun

$$\gamma^{(1)} = \alpha^{(2)} \left(\prod_{i=2}^{s-1} \sigma^{(i)} \alpha^{(2i,0)} \left(\prod_{j=1}^{k_i} \sigma^{(2i,j)} \alpha^{(2i,j)} \right) \right) \sigma^{(s)}$$

from $q'(y)$ to $r'_2(x'_2)$ satisfying $\text{VALUES}(\widetilde{\gamma^{(1)}}) \subseteq [\min(\gamma^{(1)}), \max(\gamma^{(1)}) + \Gamma_C]$. Moreover we have by Remark 46 that

$$\widehat{\gamma^{(2)}} = \alpha^{(2s,0)} \left(\prod_{j=1}^{k_s} \widehat{\sigma^{(2i,j)}} \alpha^{(2i,j)} \right) \left(\prod_{i=s+1}^{n/2} \widehat{\sigma^{(i)}} \alpha^{(2i,0)} \left(\prod_{j=1}^{k_i} \widehat{\sigma^{(2i,j)}} \alpha^{(2i,j)} \right) \right)$$

is an $(N - \Gamma_C)$ -semirun from $r'_2(x'_2)$ to $q''(z)$ that is both a min-rising and max-falling ℓ -embedding of γ 's remaining suffix N -semirun

$$\gamma^{(2)} = \alpha^{(2s,0)} \left(\prod_{j=1}^{k_s} \sigma^{(2i,j)} \alpha^{(2i,j)} \right) \left(\prod_{i=s+1}^{n/2} \sigma^{(i)} \alpha^{(2i,0)} \left(\prod_{j=1}^{k_i} \sigma^{(2i,j)} \alpha^{(2i,j)} \right) \right)$$

from $r'_2(x'_2)$ to $q''(z)$. Altogether $\gamma' = \widetilde{\gamma^{(1)}} \widehat{\gamma^{(2)}}$ is the desired $(N - \Gamma_C)$ -semirun from $q'(y + \Gamma_C)$ to $q''(z)$ that is an ℓ -embedding of $\gamma = \gamma^{(1)} \gamma^{(2)}$ with $\text{VALUES}(\gamma') \subseteq [\min(\gamma) - \Gamma_C, \max(\gamma) + \Gamma_C]$.

- *Case A.2.ii: Among the B_{\max} -hills $\sigma^{(i)}$ and the $(B_{\max} - N - 1)$ -valleys $\sigma^{(2i,j)}$ all have a ϕ -projection that contains at least as many occurrences of] as occurrences of [.*

Observe that by Remark 46 the $(N - \Gamma_C)$ -semirun

$$\widehat{\gamma} = \alpha^{(2)} \left(\prod_{i=2}^{n/2} \widehat{\sigma^{(i)}} \alpha^{(2i,0)} \left(\prod_{j=1}^{k_i} \widehat{\sigma^{(2i,j)}} \alpha^{(2i,j)} \right) \right)$$

from $q'(y)$ to $q''(z)$ is both a min-rising and max-falling ℓ -embedding of γ all of whose configurations (except for possibly the target configuration $q''(z)$) have a counter value whose absolute difference with ℓ is strictly larger than Γ_C . Yet we need to show the existence of some $(N - \Gamma_C)$ -semirun γ that is

an ℓ -embedding of γ from $q'(y + \Gamma_C)$ to $q''(z)$ that satisfies $\text{VALUES}(\gamma') \subseteq [\min(\gamma) - \Gamma_C, \max(\gamma) + \Gamma_C]$.

By Remark 37 all of the lowered $(N - \Gamma_C)$ -semiruns $\widehat{\sigma}^{(i)}$ and $\widehat{\sigma}^{(2i,j)}$ mentioned in Remark 46 contain at least as many occurrences of [as of] or, vice versa, at least as many occurrences of] as of [, if $\sigma^{(i)}$ does, respectively if $\sigma^{(2i,j)}$ does.

Thus, by our case we obtain that every $\phi(\widehat{\sigma}^{(i)})$ and $\phi(\widehat{\sigma}^{(2i,j)})$ contains at least as many occurrences of] as occurrences of [.

Recalling that neither $\alpha^{(2)}$ nor any of the $\alpha^{(2i,j)}$ contain any $+p$ -transitions nor $-p$ -transitions (and thus have all a ϕ -projection ε) it follows that $\phi(\widehat{\gamma})$ contains at least as many occurrences of] as occurrences of [.

Since $\Delta(\gamma) > \Upsilon_C$ by Point 4 of Remark 11, and thus $\Delta(\widehat{\gamma}) > \Upsilon_C$, there exists a subsemirun $\widehat{\gamma}[c, d]$ satisfying $\Delta(\widehat{\gamma}[c, d]) > \Upsilon_C$ and $\phi(\widehat{\gamma}[c, d]) \in \Lambda_8$ by Lemma 23. By now applying Lemma 22 there exists an $(N - \Gamma_C)$ -semirun χ satisfying

- $\Delta(\chi) = \Delta(\widehat{\gamma}[c, d]) - \Gamma_C$ and
- $\chi = \widehat{\gamma}[c, d] - I_1 - I_2 \cdots - I_h$ for pairwise disjoint intervals $I_1, \dots, I_h \subseteq [c, d]$ such that $\phi(\widehat{\gamma}[I_i]) \in \Lambda_{16}$ and $\Delta(\widehat{\gamma}[I_i]) > 0$ for all $i \in [1, h]$.

Note that from the definition of χ and the fact that all intermediate configurations of $\widehat{\gamma}$ have counter values whose absolute difference with ℓ is strictly larger than Γ_C it follows that $\chi + \Gamma_C$ is an ℓ -embedding of $\widehat{\gamma}[c, d]$ that has the same target configuration as $\widehat{\gamma}[c, d]$ and that satisfies $\text{VALUES}(\chi + \Gamma_C) \subseteq [\min(\widehat{\gamma}[c, d]) - \Gamma_C, \max(\widehat{\gamma}[c, d]) + \Gamma_C]$. Analogously, it follows that $\delta = (\widehat{\gamma}[0, c] + \Gamma_C)$ ($\chi + \Gamma_C$) is an $(N - \Gamma_C)$ -semirun from $q'(y + \Gamma_C)$ to the same target configuration as $\widehat{\gamma}[0, d]$ that is an ℓ -embedding of $\widehat{\gamma}[0, d]$ and that satisfies $\text{VALUES}(\delta) \subseteq [\min(\gamma[0, d]) - \Gamma_C, \max(\gamma[0, d]) + \Gamma_C]$.

Finally it follows that

$$\gamma' = (\widehat{\gamma}[0, c] + \Gamma_C) (\chi + \Gamma_C) \widehat{\gamma}[d, |\widehat{\gamma}|]$$

is the desired $(N - \Gamma_C)$ -semirun from $q'(y + \Gamma_C)$ to $q''(z)$ that is an ℓ -embedding of $\widehat{\gamma}$ and hence of γ that satisfies $\text{VALUES}(\gamma') \subseteq [\min(\gamma) - \Gamma_C, \max(\gamma) + \Gamma_C]$.

Case A.3: All but at least two of the B_{\max} -hill candidates $\sigma^{(i)}$ in (9) are in fact B_{\max} -hills.

Recall the factorization

$$\rho = \alpha^{(0)}\sigma^{(1)}\alpha^{(2)}\sigma^{(2)}\alpha^{(4)}\sigma^{(3)}\alpha^{(6)} \dots \sigma^{(n/2)}\alpha^{(n)}$$

from (9) where each $\sigma^{(i)} = \beta^{(2i-1)}\alpha^{(2i-1)}\beta^{(2i)}$ is a B_{\max} -hill candidate for all $i \in [1, n/2]$. By our case we must have $n \geq 4$.

By a similar reasoning as in Case A.2 one can show that the B_{\max} -hill candidate that are not B_{\max} -hills must be precisely the two subsemiruns $\sigma^{(1)}$ and $\sigma^{(n/2)}$. In particular there cannot be strictly more than two B_{\max} -hill candidates in (9) that are not in fact B_{\max} -hills. Moreover, as already reasoned in Case A.2, neither $\beta^{(1)}$ nor

$\beta^{(n)}$ is doubly-crossing. Thus $\alpha^{(0)}$ and $\alpha^{(n)}$ are empty. Hence, one can now factorize our N -semirun ρ as

$$\rho = \left(\beta^{(1)}\alpha^{(1)}\right)\beta^{(2)}\alpha^{(2)}\beta^{(3)}\alpha^{(3)} \dots \beta^{(n-1)}\left(\alpha^{(n-1)}\beta^{(n)}\right) .$$

For finishing this case we will show the existence

1. of an $(N - \Gamma_C)$ -semirun that is both a min-rising and max-falling ℓ -embedding of the semirun $\beta^{(1)}\alpha^{(1)}$ with the same source and target configuration as $\beta^{(1)}\alpha^{(1)}$,
2. of an $(N - \Gamma_C)$ -semirun that is both a min-rising and max-falling ℓ -embedding of the semirun $\alpha^{(n-1)}\beta^{(n)}$ with the same source and target configuration as $\alpha^{(n-1)}\beta^{(n)}$, and
3. of an $(N - \Gamma_C)$ -semirun that is both a min-rising and max-falling ℓ -embedding of the semirun $\beta^{(2)}\alpha^{(2)}\beta^{(3)}\alpha^{(3)} \dots \beta^{(n-1)}$ with same source and target configuration.

Points 1 and 2 are proven analogously as Point 1 from Case A.2. For proving Point 3, we consider a different factorization

$$\beta^{(2)}\alpha^{(2)}\beta^{(3)}\alpha^{(3)} \dots \beta^{(n-1)} = \tau^{(1)}\alpha^{(3)}\tau^{(2)}\alpha^{(5)} \dots \tau^{((n-2)/2)},$$

where $\tau^{(i)} = \beta^{(2i)}\alpha^{(2i)}\beta^{(2i+1)}$ is a B_{\min} -valley candidate for all $i \in [1, (n - 2)/2]$. Since $\beta^{(2i)}$ and $\beta^{(2i+1)}$ have to be doubly crossing for all $i \in [1, (n - 2)/2]$, $\tau^{(i)}$ is in fact a B_{\min} -valley for all $i \in [1, (n - 2)/2]$ by a similar reasoning as used in Case A.2 to show that the B_{\max} -hill candidate that is not a B_{\max} -hill must be $\sigma^{(1)}$ or $\underline{\sigma}^{(n/2)}$.

We can apply Claim 2 to each $\tau^{(i)}$ and obtain an $(N - \Gamma_C)$ -semirun $\overline{\tau}^{(i)}$ that is both a min-rising and max-falling ℓ -embedding of $\tau^{(i)}$ with the same source and target configuration. Thus, it only remains to show the same for $\alpha^{(2i+1)}$ for each $i \in [1, (n - 2)/2]$. This is done analogously as in Case A.1 when proving the same for each $\alpha^{(2i)}$ for each $i \in [0, n/2]$.

Case B: The number of crossing transitions n is odd.

Recall that we had assumed without loss of generality that $\beta^{(1)}$ is a $+p$ -transition. Since n is odd one can consider the following first factorization

$$\rho = \alpha^{(0)}\sigma^{(1)}\alpha^{(2)}\sigma^{(2)}\alpha^{(4)}\sigma^{(3)}\alpha^{(6)} \dots \sigma^{(\lfloor n/2 \rfloor)}\alpha^{(n-1)}\beta^{(n)}\alpha^{(n)}, \quad (12)$$

where $\sigma^{(i)} = \beta^{(2i-1)}\alpha^{(2i-1)}\beta^{(2i)}$ is a B_{\max} -hill candidate, $\beta^{(2i-1)}$ is a $+p$ -transition, $\beta^{(2i)}$ is a $-p$ -transition for all $i \in [1, \lfloor n/2 \rfloor]$, and $\beta^{(n)}$ is a $+p$ -transition; as well as the following second factorization

$$\rho = \alpha^{(0)}\beta^{(1)}\alpha^{(1)}\tau^{(1)}\alpha^{(3)}\tau^{(2)}\alpha^{(5)}\tau^{(3)} \dots \tau^{(\lfloor n/2 \rfloor)}\alpha^{(n)}, \quad (13)$$

where β_1 is a $+p$ -transition, $\tau^{(i)} = \beta^{(2i)}\alpha^{(2i)}\beta^{(2i+1)}$ is a B_{\min} -valley candidate, $\beta^{(2i)}$ is a $-p$ -transition and $\beta^{(2i+1)}$ is a $+p$ -transition for all $i \in [1, \lfloor n/2 \rfloor]$. Indeed, this— as for the Case A factorization (9)— immediately follows from the definition of crossing transitions and the fact that neither $\alpha^{(2i-1)}$ (resp. $\alpha^{(2i)}$) contains any configuration with counter value strictly less than B_{\max} (resp. strictly larger than B_{\min}) for all $i \in [1, \lfloor n/2 \rfloor]$.

Our proof next will make a case distinction on the number of B_{\max} -hill candidates $\sigma^{(i)}$ in the factorization (12) that are in fact B_{\max} -hills and on the number of B_{\min} -valley candidates $\tau^{(i)}$ in the factorization (13) that are in fact B_{\min} -valleys.

Case B.1: All of the B_{\max} -hill candidates $\sigma^{(i)}$ in (12) are in fact B_{\max} -hills or all of the B_{\min} -valley candidates $\tau^{(i)}$ in (13) are in fact B_{\min} -valleys.

Let us assume without loss of generality that all of the B_{\max} -hill candidates in (12) are in fact B_{\max} -hills. The case when all B_{\min} -valley candidates in (13) are in fact B_{\min} -valleys can be proven analogously. Each N -semirun $\sigma^{(i)}$ can hence be turned into an $(N - \Gamma_C)$ -semirun $\widehat{\sigma}^{(i)}$ that is both a min-rising and max-falling ℓ -embedding of $\sigma^{(i)}$ with same source and target configuration as $\sigma^{(i)}$ according to Claim 2. Moreover, the same holds for $\alpha^{(2i)}$ for all $i \in [0, \lfloor n/2 \rfloor - 1]$, as seen in Case A.1. Thus it remains to deal with the subsemiruns $\alpha^{(n-1)}$, $\beta^{(n)}$ and $\alpha^{(n)}$.

We make a final case distinction on the target configuration of the dangling $+p$ -transition $\beta^{(n)}$.

- *Case B.1.i:* $\beta^{(n)}$ has a target configuration with a counter value strictly larger than $B_{\max} + \Upsilon_C$.

Then clearly $\beta^{(n)}\alpha^{(n)}$ is a B_{\max} -hill as well since $\alpha^{(n)}$ contains no configurations with counter value strictly less than B_{\max} besides its last one. The N -semirun $\beta^{(n)}\alpha^{(n)}$ can hence be turned into an $(N - \Gamma_C)$ -semirun that is both a min-rising and max-falling ℓ -embedding with the same source and target configuration according to Claim 2. Moreover, the same holds for $\alpha^{(n-1)}$, as analogously proven for $\alpha^{(2i)}$ for all $i \in [0, \lfloor n/2 \rfloor]$ in Case A.1. The concatenation of these two ℓ -embeddings yields an $(N - \Gamma_C)$ -semirun that is a min-rising and max-falling ℓ -embedding of $\alpha^{(n-1)}\beta^{(n)}\alpha^{(n)}$ with the same source and target configuration.

- *Case B.1.ii:* $\beta^{(n)}$ has a target configuration with a counter value strictly less than B_{\max} .

It immediately follows that $\beta^{(n)}$ is crossing but not doubly-crossing, thus $\alpha^{(n)}$ is empty. The remaining $\alpha^{(n-1)}\beta^{(n)}$ can thus be factorized as

$$\alpha^{(n-1)}\beta^{(n)} = \xi^{(1)}\chi^{(1)} \dots \xi^{(k)}\chi^{(k)},$$

where each $\chi^{(i)}$ is a $(B_{\max} - N - 1)$ -valley and each $\xi^{(i)}$ satisfies $\text{VALUES}(\xi^{(i)}) \subseteq [B_{\max} - N, B_{\min} + 1]$, using a similar factorization as for proving Point 1 in Case A.2. The N -semirun $\alpha^{(n-1)}\beta^{(n)}$ can hence be turned into an $(N - \Gamma_C)$ -semirun that is both a min-rising and max-falling ℓ -embedding with same source and target configuration. Recalling that $\alpha^{(n)}$ is empty, the above embedding is an $(N - \Gamma_C)$ -semirun that is both a min-rising and max-falling ℓ -embedding of $\alpha^{(n-1)}\beta^{(n)}\alpha^{(n)}$ with same source and target configuration.

- *Case B.1.iii:* $\beta^{(n)}$ has a target configuration with counter value in $[B_{\max}, B_{\max} + \Upsilon_C]$.

Thus, the source configuration of $\beta^{(n)}$ has a counter value in $[B_{\max} - N, B_{\max} + \Upsilon_C - N]$. One finishes this case analogously as Points 1 and 2 in Case A.2:

1. Firstly, one shows the existence of an $(N - \Gamma_C)$ -semirun that is both a min-rising and max-falling ℓ -embedding of $\alpha^{(n)}$ with the same source and target configuration as $\alpha^{(n)}$ as follows: one factorizes $\alpha^{(n)}$ into $(N - \Gamma_C)$ -semiruns that have all counter values in $[B_{\max} - 1, B_{\min} + N]$ and into $(B_{\min} + N + 1)$ -hills.

2. Secondly, let us assume that $\beta^{(n)}$ is an N -semirun from $q'(y)$ to $q''(z)$ and that moreover $\alpha^{(0)}\sigma^{(1)}\alpha^{(2)} \dots \sigma^{(\lfloor n/2 \rfloor)}\alpha^{(n-1)}$ is an N -semirun from $q(x)$ to $q'(y)$. Stipulating that $\beta^{(n)} = q'(y) \xrightarrow{+p, N} q''(z)$, we explicitly lower $\beta^{(n)}$ into the $(N - \Gamma_C)$ -semirun $q'(y + \Gamma_C) \xrightarrow{+p, N - \Gamma_C} q''(z)$, which is — since $\beta^{(n)}$ is doubly-crossing — obviously both a min-rising and max-falling ℓ -embedding of $\beta^{(n)}$ from $q'(y + \Gamma_C)$ to $q''(z)$. Then one shows the existence of an $(N - \Gamma_C)$ -semirun that is an ℓ -embedding of $\alpha^{(0)}\sigma^{(1)}\alpha^{(2)} \dots \sigma^{(\lfloor n/2 \rfloor)}\alpha^{(n)}$ from $q(x)$ to $q'(y + \Gamma_C)$ with configurations all of whose counter values lie in the interval $[\min(\rho) - \Gamma_C, \max(\rho) + \Gamma_C]$ as follows: one subfactorizes each of the $\alpha^{(2i)}$ into $(N - \Gamma_C)$ -semiruns that have a counter values in $[B_{\max} - N, B_{\min}]$ and into $(B_{\max} - N - 1)$ -valleys and by recalling that $\sigma^{(i)}$ is a B_{\max} -hill for all $i \in [1, \lfloor n/2 \rfloor]$.

Case B.2: Not all of the B_{\max} -hill candidates $\sigma^{(i)}$ in (12) are in fact B_{\max} -hills and not all of the B_{\min} -valley candidates $\tau^{(i)}$ in (13) are in fact B_{\min} -valleys.

Since they all start and end with a doubly-crossing transition we remark that $\sigma^{(i)}$ is in fact a B_{\max} -hill for all $i \in [2, \lfloor n/2 \rfloor]$ and $\tau^{(i)}$ is in fact a B_{\min} -valley for all $i \in [1, \lfloor n/2 \rfloor - 1]$. Hence our case implies that $\sigma^{(1)}$ is in fact not a B_{\max} -hill and that $\tau^{(\lfloor n/2 \rfloor)}$ is in fact not a B_{\min} -valley. As in Case A.2, $\beta^{(1)}$ and $\beta^{(n)}$ are hence not doubly-crossing, and hence $\alpha^{(0)}$ and $\alpha^{(n)}$ are empty.

By definition of a Type III semirun, ρ contains at least one doubly-crossing transition and thus $n \geq 3$. Since the $+p$ -transition $\beta^{(1)}$ is not doubly-crossing (and therefore ends at a counter value strictly larger than $B_{\min} + N$) but $\beta^{(2)}$ is, it follows that the only reason for $\sigma^{(1)} = \beta^{(1)}\alpha^{(1)}\beta^{(2)}$ not to be a B_{\max} -hill is that the $-p$ -transition $\beta^{(2)}$ has a source configuration with a counter value in $[B_{\max}, B_{\max} + \Upsilon_C]$ and hence a target configuration with counter value in $[B_{\max} - N, B_{\max} + \Upsilon_C - N]$, similarly as seen in Case A.2. Analogously, the only reason for $\tau^{(\lfloor n/2 \rfloor)} = \beta^{(n-1)}\alpha^{(n-1)}\beta^{(n)}$ not to be a B_{\min} -valley is that the doubly-crossing $-p$ -transition $\beta^{(n-1)}$ has a target configuration with counter value in $[B_{\min} - \Upsilon_C, B_{\min}]$.

Recalling that $\sigma^{(\lfloor n/2 \rfloor)} = \beta^{(n-2)}\alpha^{(n-2)}\beta^{(n-1)}$, for finishing this case we will apply an analogous reasoning as in Case A.2:

1. Firstly, one shows the existence of an $(N - \Gamma_C)$ -semirun that is both a min-rising and max-falling ℓ -embedding of $\beta^{(1)}\alpha^{(1)}$ with the same source and target configuration as $\beta^{(1)}\alpha^{(1)}$.
2. Secondly, let us assume that $\beta^{(2)}$ is an N -semirun from $q(x)$ to $q'(y)$ and that moreover $\alpha^{(2)}\sigma^{(2)}\alpha^{(4)} \dots \sigma^{(\lfloor n/2 \rfloor)}$ is an N -semirun from $q'(y)$ to $q''(z)$, with $y \in [B_{\max} - N, B_{\max} + \Upsilon_C - N]$ and $z \in [B_{\min} - \Upsilon_C, B_{\min}]$. Noting that $\beta^{(2)} = q(x) \xrightarrow{-p, N} q'(y)$, we explicitly lower $\beta^{(2)}$ into the $(N - \Gamma_C)$ -semirun $q(x) \xrightarrow{-p, N - \Gamma_C} q'(y + \Gamma_C)$, which is — since $\beta^{(2)}$ is doubly-crossing — obviously both a min-rising and max-falling ℓ -embedding of $\beta^{(2)}$ from $q(x)$ to $q'(y + \Gamma_C)$. Then one shows, as done in Case 2.A, the existence of an $(N - \Gamma_C)$ -semirun that is an ℓ -embedding of $\alpha^{(2)}\sigma^{(2)}\alpha^{(4)} \dots \sigma^{(\lfloor n/2 \rfloor)}$ all of whose counter

values lie in $[\min(\rho) - \Gamma_C, \max(\rho) + \Gamma_C]$ from $q'(y + \Gamma_C)$ to $q''(z)$ by sub-factorizing each of the $\alpha^{(2i)}$ into $(N - \Gamma_C)$ -semiruns that have counter values in $[B_{\max} - N, B_{\min}]$ and into $(B_{\max} - N - 1)$ -valleys, by recalling that $\sigma^{(i)}$ is a B_{\max} -hill for all $i \in [2, \lfloor n/2 \rfloor]$, and that

$$\begin{aligned} z - y &\geq N - (B_{\max} - B_{\min}) - 2\Upsilon_C \\ &\stackrel{(6)}{>} N - (5/6 \cdot N + 2\Upsilon_C + 4\Gamma_C + 2) - 2\Upsilon_C \\ &= N/6 - (5\Upsilon_C - 4\Gamma_C - 2) + \Upsilon_C \\ &> M_C/6 - (5\Upsilon_C - 4\Gamma_C - 2) + \Upsilon_C \\ &> \Upsilon_C. \end{aligned}$$

3. Finally (analogously as Point 1) one shows the existence of an $(N - \Gamma_C)$ -semirun that is both a min-rising and max-falling ℓ -embedding of $\alpha^{(n-1)}\beta^{(n)}$ with the same source and target configuration as $\alpha^{(n-1)}\beta^{(n)}$.

9 Proof of the Small Parameter Theorem

This section is devoted to proving the Small Parameter Theorem (Theorem 18).

For proving this let us fix some $N > M_C$ and some accepting N -run π in \mathcal{C} with $\text{VALUES}(\pi) \subseteq [0, 4N]$ of the form

$$\pi = r_0(x_0) \xrightarrow{\pi_{0,N}} r_1(x_1) \cdots \xrightarrow{\pi_{n-1,N}} r_n(x_n)$$

with $r_n \in F$. We will assume that accepting runs in \mathcal{C} end with counter value 0 and hence, that $x_n = x_0 = 0$. We do not lose generality by making this assumption. Indeed, from every POCA \mathcal{C} , one can build a POCA \mathcal{C}' with all its accepting runs ending in configuration with counter value 0 such that, for all $N \in \mathbb{N}$, there exists an accepting N -run in \mathcal{C} with values in $[0, 4N]$ if, and only if, there exists an accepting N -run in \mathcal{C}' with values in $[0, 4N]$. This is clear when one considers the construction \mathcal{C}' obtained from \mathcal{C} by adding two control states r_- and r_f such that every final control state of \mathcal{C} has a ≥ 0 rule leading to r_- , r_- has a -1 rule that is a loop, and finally a $a = 0$ rule to r_f , the only final state of \mathcal{C}' .

Starting from the accepting N -run π , we need to prove the existence of an accepting $(N - \Gamma_C)$ -run in \mathcal{C} . For every $a, b \in \mathbb{Q}$ with $a < b$ we define $[a, b) = \{c \in \mathbb{Q} \mid a \leq c < b\}$ and $(a, b] = \{c \in \mathbb{Q} \mid a < c \leq b\}$.

Since $\frac{N}{3} < N - \Gamma_C$, as $\Gamma_C < \frac{2M_C}{3} < \frac{2N}{3}$ by definition of the constants on page 15, the following claim is clear.

Claim 3 Every subrun ρ of π with $\text{VALUES}(\rho) \subseteq [0, \frac{N}{3})$ is already an $(N - \Gamma_C)$ -run.

We can therefore uniquely factorize π as

$$\pi = \rho^{(0)}\sigma^{(1)}\rho^{(1)} \cdots \sigma^{(m)}\rho^{(m)}, \tag{14}$$

where each $\rho^{(j)}$ satisfies $\text{VALUES}(\rho^{(j)}) \subseteq [0, \frac{N}{3})$ and each $\sigma^{(j)}$ is some subrun $\pi[c, d]$ with $x_c < \frac{N}{3}$, $x_d < \frac{N}{3}$ and $x_k \geq \frac{N}{3}$ for all $k \in [c + 1, d - 1]$, where $[c + 1, d - 1] \neq \emptyset$.

To finish the proof of the Small Parameter Theorem (Theorem 18), by Claim 3 it thus suffices to prove the following statement for the rest of this section.

For every $N > M_C$ and every N -run

$$\sigma = q_0(z_0) \xrightarrow{\sigma_{0,N}} q_1(z_1) \cdots \xrightarrow{\sigma_{m-1,N}} q_m(z_m)$$

satisfying $\text{VALUES}(\sigma) \subseteq [0, 4N]$, $z_0, z_m < \frac{N}{3}$ and $z_i \geq \frac{N}{3}$ for all $i \in [1, m - 1]$, there exists an $(N - \Gamma_C)$ -run from $q_0(z_0)$ to $q_m(z_m)$.

Let σ be such an N -run. Let us first assume that $z_i \geq N$ for some $i \in [1, m - 1]$; the case $z_i < N$ for all $i \in [1, m - 1]$ will be treated later. By this assumption, one can uniquely factorize σ — as seen in Fig. 12 — as

$$\sigma = \alpha \sigma[a, a + 1] \beta \sigma[b, b + 1] \gamma, \tag{15}$$

where, for some $a, b \in [0, m - 1]$,

- $\alpha = \sigma[0, a]$ is the maximal prefix of σ satisfying $\text{VALUES}(\alpha) \subseteq [0, N]$, in particular the transition $q_a(z_a) \xrightarrow{\sigma_{a,N}} q_{a+1}(z_{a+1})$ satisfies $z_a \in [0, N]$ and $z_{a+1} \in [N, 4N]$,
- $\gamma = \sigma[b + 1, m]$ is the maximal suffix of σ satisfying $\text{VALUES}(\gamma) \subseteq [0, N]$, i.e. the transition $q_b(z_b) \xrightarrow{\sigma_{b,N}} q_{b+1}(z_{b+1})$ satisfies $z_b \in [N, 4N]$ and $z_{b+1} \in [0, N]$, and

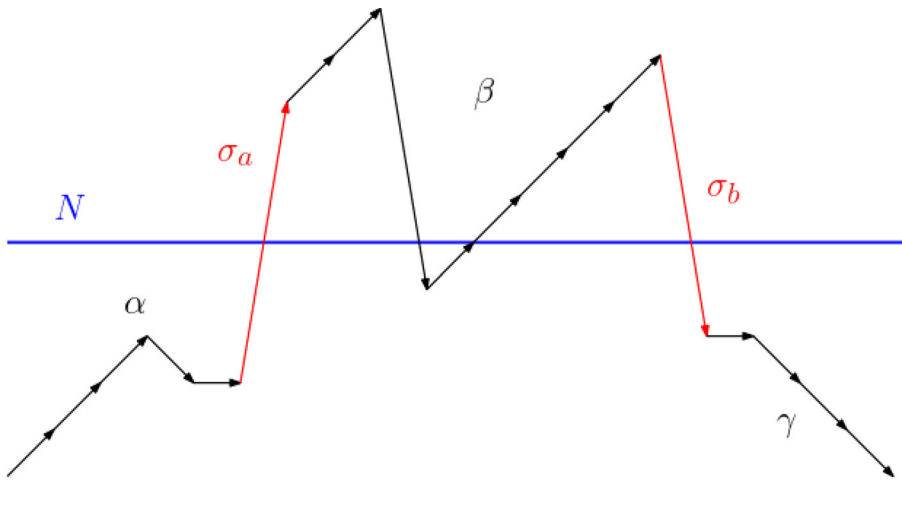


Fig. 12 Illustration of the factorization (15)

- $\beta = \sigma[a + 1, b]$ is the remaining infix of σ (note that $a + 1 = b$ is possible).

We will apply the 5/6-Lemma (Lemma 39) to one of the subruns

$$\beta, \quad \sigma[a, a + 1] \beta, \quad \beta \sigma[b, b + 1], \quad \text{or} \quad \sigma[a, a + 1] \beta \sigma[b, b + 1],$$

hereby showing the existence of a suitable $(N - \Gamma_C)$ -semirun with same source and target configuration, respectively. We then shift this $(N - \Gamma_C)$ -semirun by $-\Gamma_C$ to obtain a suitable $(N - \Gamma_C)$ -run. To which of the subruns we will choose to apply the 5/6-Lemma will depend on the counter values z_a, z_{a+1}, z_b and z_{b+1} . For deciding this, we make a case distinction on which of the five intervals $\left\{ \left[\frac{iN}{3}, \frac{(i+1)N}{3} \right) : i \in [1, 5] \right\}$ they lie in, respectively.

Before the above-mentioned distinction on z_a, z_{a+1}, z_b and z_{b+1} we first claim that one can turn the possible resulting prefixes α and $\alpha\sigma[a, a + 1]$ and possible suffixes $\sigma[b, b + 1]\gamma$ and γ into $(N - \Gamma_C)$ -runs separately. The following claim tells us when these latter prefixes (resp. suffixes) can be turned into $(N - \Gamma_C)$ -runs whose target (resp. source) configuration has been shifted down by Γ_C .

Claim 4 (Possible lowering of the prefixes and suffixes)

1. If $z_{a+1} \in [N, \frac{5N}{3})$, then there exists an $(N - \Gamma_C)$ -run from $q_0(z_0)$ to $q_{a+1}(z_{a+1} - \Gamma_C)$.
2. If $z_a \in [\frac{N}{3} + \Upsilon_C, N)$, then there exists an $(N - \Gamma_C)$ -run from $q_0(z_0)$ to $q_a(z_a - \Gamma_C)$.
3. If $z_b \in [N, \frac{5N}{3})$, then there exists an $(N - \Gamma_C)$ -run from $q_b(z_b - \Gamma_C)$ to $q_m(z_m)$.
4. If $z_{b+1} \in [\frac{N}{3} + \Upsilon_C, N)$, then there exists an $(N - \Gamma_C)$ -run from $q_{b+1}(z_{b+1} - \Gamma_C)$ to $q_m(z_m)$.

We postpone the proof of Claim 4 to the end of this section but refer to Fig. 16 for an illustration of Points 1 and 2.

We can use Point (1) or Point (2) of the claim to turn the possible resulting prefixes $\alpha\sigma[a, a + 1]$ or α respectively into $(N - \Gamma_C)$ -runs with target configuration shifted down by Γ_C . Symmetrically, we can use Point (3) or Point (4) of the claim to turn the possible resulting suffixes $\sigma[b, b + 1]\gamma$ or γ respectively into $(N - \Gamma_C)$ -runs with source configuration shifted down by Γ_C . The claim will rely on the Depumping Lemma (Lemma 22) and on the fact that a transition with operation $+p$ or $-p$ has an absolute counter effect of N in an N -run but $N - \Gamma_C$ in an $(N - \Gamma_C)$ -run.

Let us for the moment assume $z_i \geq N$ for some $i \in [1, m - 1]$ along with the factorization (15) of σ and Claim 4.

Assuming Claim 4 we conclude the proof by treating the following exhaustive cases on the positions of z_{a+1} and z_b separately.

Case 1. $z_{a+1}, z_b \in [N, \frac{5N}{3})$, cf. Figure 13.

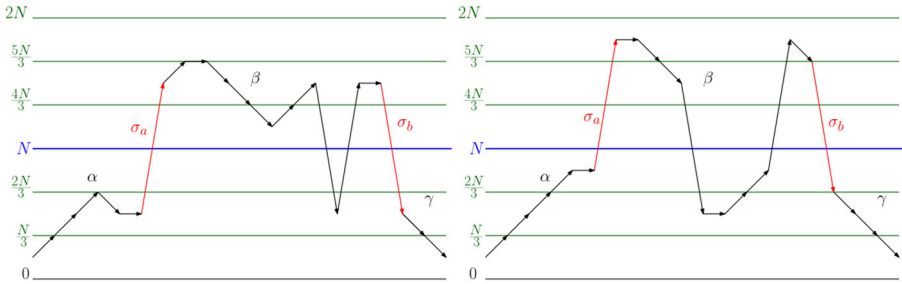


Fig. 13 Illustration of Case 1, i.e. $z_{a+1}, z_b \in [N, \frac{5N}{3})$, on the left, and Case 2, i.e. $z_{a+1}, z_b \in [\frac{5N}{3}, 2N)$, on the right

Recall that $\beta = \sigma[a + 1, b]$ as defined in (15) is an N -run from $q_{a+1}(z_{a+1})$ to $q_b(z_b)$ satisfying $\text{VALUES}(\beta) \subseteq [\frac{N}{3}, 4N]$. We view β as an N -semirun. We consider $\ell = N$ and observe that

$$\max(z_{a+1}, z_b, \ell) - \min(z_{a+1}, z_b, \ell) < \frac{5N}{3} - N = \frac{2N}{3} \leq \frac{5N}{6}.$$

Hence we can apply the 5/6-Lemma (Lemma 39) to β : there exists an $(N - \Gamma_C)$ -semirun $\hat{\beta}$ from $q_{a+1}(z_{a+1})$ to $q_b(z_b)$ that is an N -embedding of β with $\text{VALUES}(\hat{\beta}) \subseteq [\min(\beta) - \Gamma_C, \max(\beta) + \Gamma_C]$. Since moreover $\frac{N}{3} - 2\Gamma_C > \max(\text{Consts}(\mathcal{C}))$, from M_C 's definition on page 15, and because $\min(\beta) \geq N/3$, it follows that $\hat{\beta} - \Gamma_C$, the shifting of $\hat{\beta}$ by $-\Gamma_C$, is in fact an $(N - \Gamma_C)$ -run from $p(z_{a+1} - \Gamma_C)$ to $q_b(z_b - \Gamma_C)$. It thus remains to show the existence of an $(N - \Gamma_C)$ -run from $q_0(z_0)$ to $q_{a+1}(z_{a+1} - \Gamma_C)$ and one from $q_b(z_b - \Gamma_C)$ to $q_m(z_m)$: the former follows from Point (1) of Claim 4, and the latter follows from Point (3) of Claim 4.

Case 2. $z_{a+1}, z_b \in [\frac{5N}{3}, 2N)$, cf. Figure 13.

It follows that $z_a, z_{b+1} \in [\frac{2N}{3}, N)$, and that σ_a and σ_b must be a $+p$ and $-p$ respectively. We apply the 5/6-Lemma (Lemma 39) to

$$\sigma[a, a + 1] \beta \sigma[b, b + 1]$$

with $\ell = N$, then shift the output by $-\Gamma_C$. Then we apply Point (2) of Claim 4 claim fix and Point (4) of Claim 4.

Case 3. $z_{a+1} \in [N, \frac{4N}{3})$ and $z_b \in [\frac{5N}{3}, 2N)$, cf. Figure 14.

It follows that $z_{b+1} \in [\frac{2N}{3}, N)$. We apply the 5/6-Lemma (Lemma 39) to

$$\beta \sigma[b, b + 1]$$

with $\ell = N$, then shift the output by $-\Gamma_C$. Then we apply Point (1) of Claim 4 and Point (4) of Claim 4.

Case 4. $z_{a+1} \in [\frac{5N}{3}, 2N)$ and $z_b \in [N, \frac{4N}{3})$, cf. Figure 14.

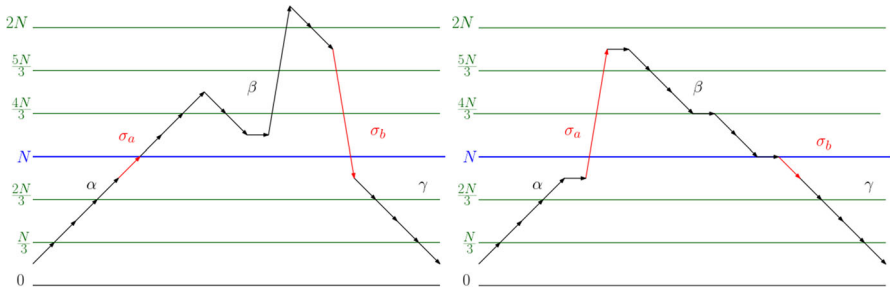


Fig. 14 Illustration of Case 3, i.e. $z_{a+1} \in [N, \frac{4N}{3})$ and $z_b \in [\frac{5N}{3}, 2N)$, on the left, and Case 4, i.e. $z_{a+1} \in [\frac{5N}{3}, 2N)$ and $z_b \in [N, \frac{4N}{3})$, on the right

It follows that $z_a \in [\frac{2N}{3}, N)$. We apply the 5/6-Lemma (Lemma 39) to

$$\sigma[a, a + 1] \beta$$

with $\ell = N$, then shift the output by $-\Gamma_C$. Then we apply Point (2) of Claim 4 and Point (3) of Claim 4.

Case 5. $z_{a+1} \in [\frac{4N}{3}, \frac{5N}{3})$ and $z_b \in [\frac{5N}{3}, 2N)$, cf. Figure 15.

It follows $z_{b+1} \in [\frac{2N}{3}, N)$, and that σ_a and σ_b must be $a + p$ and $-p$ respectively. We distinguish whether $z_a \in [\frac{N}{3}, \frac{N}{2})$ or not.

Case 5.A. $z_a \notin [\frac{N}{3}, \frac{N}{2})$.

It follows $z_a \in [\frac{N}{2}, N)$. We apply the 5/6-Lemma (Lemma 39) to

$$\sigma[a, a + 1] \beta \sigma[b, b + 1]$$

with $\ell = N$, then shift the output by $-\Gamma_C$. Then we apply Point (2) of Claim 4 and Point (4) of Claim 4.

Case 5.B. $z_a \in [\frac{N}{3}, \frac{N}{2})$.

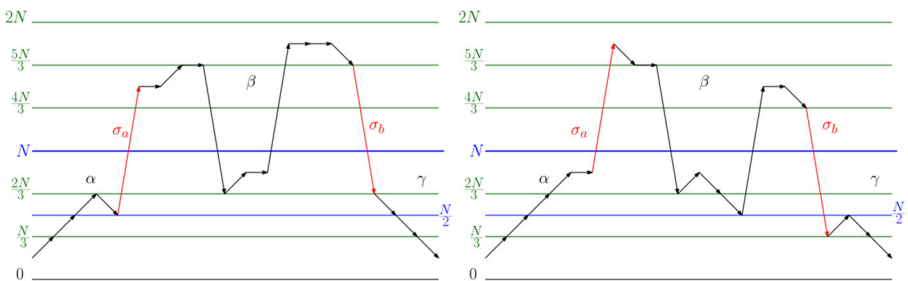


Fig. 15 Illustration of Case 5, i.e. $z_{a+1} \in [\frac{4N}{3}, \frac{5N}{3})$ and $z_b \in [\frac{5N}{3}, 2N)$, on the left (with moreover $z_a \notin [\frac{N}{3}, \frac{N}{2})$), and Case 6, i.e. $z_{a+1} \in [\frac{5N}{3}, 2N)$ and $z_b \in [\frac{4N}{3}, \frac{5N}{3})$, on the right (with moreover $z_{b+1} \in [\frac{N}{3}, \frac{N}{2})$)

It follows $z_{a+1} \in [\frac{4N}{3}, \frac{3N}{2})$. We apply the 5/6-Lemma (Lemma 39) to

$$\beta \sigma[b, b + 1]$$

with $\ell = N$, then shift the output by $-\Gamma_C$. Then we apply Point (1) of Claim 4 and Point (4) of Claim 4.

Case 6. $z_{a+1} \in [\frac{5N}{3}, 2N)$ and $z_b \in [\frac{4N}{3}, \frac{5N}{3})$, cf. Figure 15.

It follows $z_a \in [\frac{2N}{3}, N)$, and that σ_a and σ_b must be a $+p$ and $-p$ respectively. We distinguish whether $z_{b+1} \in [\frac{N}{3}, \frac{N}{2})$ or not.

Case 6.A. $z_{b+1} \notin [\frac{N}{3}, \frac{N}{2})$.

It follows $z_{b+1} \in [\frac{N}{2}, \frac{2N}{3})$. We apply the 5/6-Lemma (Lemma 39) to

$$\sigma[a, a + 1] \beta \sigma[b, b + 1]$$

with $\ell = N$, then shift the output by $-\Gamma_C$. Then we apply Point (2) of Claim 4 and Point (4) of Claim 4.

Case 6.B. $z_{b+1} \in [\frac{N}{3}, \frac{N}{2})$.

It follows $z_b \in [\frac{4N}{3}, \frac{3N}{2})$. We apply the 5/6-Lemma (Lemma 39) to

$$\sigma[a, a + 1] \beta$$

with $\ell = N$, then shift the output by $-\Gamma_C$. Then we apply Point (2) of Claim 4 and Point (3) of Claim 4.

It remains to provide the proof of the Claim 4 before discussing the remaining case when $z_i < N$ for all $i \in [1, m - 1]$

Proof of Claim 4 Let us only prove Points (1) and (2). Points (3) and (4) can be proven in a symmetrical manner as Points (1) and (2). Let us first prove Point (1), so let us assume that $z_{a+1} \in [N, \frac{5N}{3})$. We refer to Fig. 16 for an example of such a situation. Recall that $\alpha = \sigma[0, a]$, $z_0 < \frac{N}{3}$ and $z_i \in [\frac{N}{3}, N)$ for all $i \in [1, a]$.

We first factorize α , as seen in Fig. 17, as

$$\alpha = \left(\prod_{i=1}^t \chi_i \right) \zeta,$$

where

- each χ_i is a subrun of α that either
 - a) starts in a configuration with counter value strictly less than $N - \Gamma_C - \Upsilon_C$ and ends in the first next configuration with counter value at least $N - \Gamma_C$, or conversely
 - b) starts in a configuration with counter value at least $N - \Gamma_C$ and ends in the first next configuration with counter value strictly less than $N - \Gamma_C - \Upsilon_C$, and

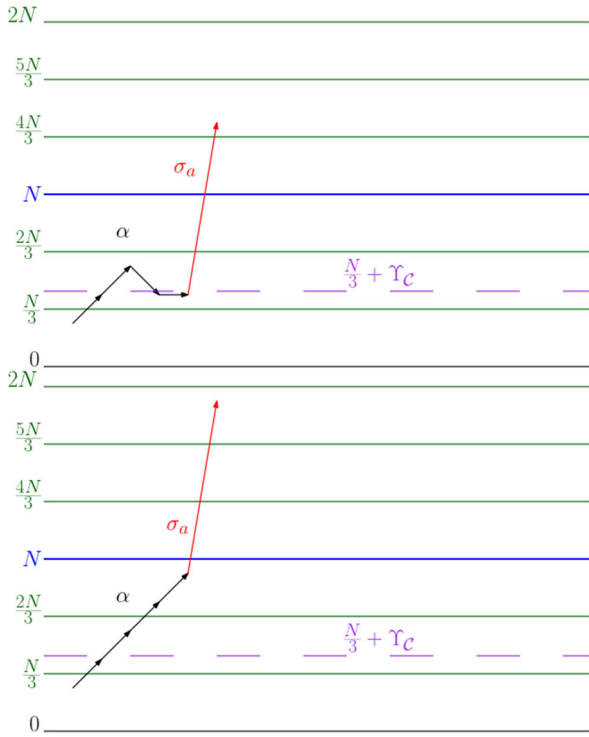


Fig. 16 Claim 4: Examples for Point 1 (above) and Point 2 (below)

- the (possibly empty) suffix ζ 's prefixes are neither of form a) nor b), i.e. $\text{VALUES}(\zeta) \subseteq [0, N - \Gamma_C]$ or $\text{VALUES}(\zeta) \subseteq [N - \Gamma_C - \Upsilon_C, N]$.

First observe that α and hence in particular $\chi_1, \dots, \chi_t, \zeta$ all do not contain any $+p$ -transition nor any $-p$ -transition, and that $|\Delta(\chi_i)| > \Upsilon_h$ for all $i \in [1, t]$ by definition.

Next observe that $t = 0$ is possible; in this case we have $\alpha = \zeta$ and $\text{VALUES}(\alpha) \subseteq [0, N - \Gamma_C]$.

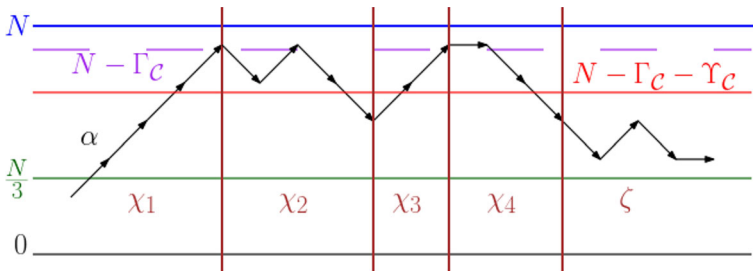


Fig. 17 Illustration of the factors $\chi_1, \chi_2, \chi_3, \chi_4$ and ζ of α

We will however first treat the case $t > 0$, the case $t = 0$ will be treated later. It follows from $z_0 < \frac{N}{3}$ that χ_1 must be of type a); more generally, χ_i is of type a) for all odd $i \in [1, t]$ and of type b) for all even $i \in [1, t]$. Since $z_0 < \frac{N}{3}$, observe that if for α 's last counter value z_a we have $z_a \in [N - \Gamma_C, N)$, then t must be odd, and, similarly (but not entirely dually), if for α 's last counter value z_a we have $z_a \in [\frac{N}{3}, N - \Gamma_C - \Upsilon_C)$, then t must be even.

In the following we prefer to write α as $\alpha = \alpha[0, a]$ rather than $\sigma[0, a]$. It is important to recall that $z_0 < \frac{N}{3}$ and $z_s \in [\frac{N}{3}, N)$ for all $s \in [1, a]$.

Let

$$q_0(z_0) = q_{j_1}(z_{j_1}) \xrightarrow{\chi_1} q_{j_2}(z_{j_2}) \xrightarrow{\chi_2} q_{j_3}(z_{j_3}) \cdots \xrightarrow{\chi_{t-1}} q_{j_t}(z_{j_t}) \xrightarrow{\chi_t} q_{j_{t+1}}(z_{j_{t+1}}) \xrightarrow{\zeta} q_{j_{t+2}}(z_{j_{t+2}}).$$

Note that $j_{t+1} = j_{t+2}$ is possible if ζ is empty. In the following, we will first show how to turn any χ_i of type a) (resp. b)) into an $(N - \Gamma_C)$ -run with target (resp. source) configuration shifted down by Γ_C , and then we make a case distinction on how to end the proof based on the parity of t .

Subclaim 1 *Let $i \in [1, t]$ be odd. Then there exists an $(N - \Gamma_C)$ -run $\widehat{\chi}_i$ from $q_{j_i}(z_{j_i})$ to $q_{j_{i+1}}(z_{j_{i+1}} - \Gamma_C)$.*

Proof of Subclaim 1 Indeed, $\phi(\chi_i) = \varepsilon \in \Lambda_8$, as α contains neither $+p$ -transitions nor $-p$ -transitions. Since moreover $\Delta(\chi_i) > \Upsilon_C$ we can now apply Lemma 22 to χ_i (viewed as an N -semirun) and obtain an N -semirun $\widehat{\chi}_i$ with $\Delta(\widehat{\chi}_i) = \Delta(\chi_i) - \Gamma_C$ that is such that $\widehat{\chi}_i = \alpha[j_i, j_{i+1}] - I_1 - I_2 \cdots - I_k$ for pairwise disjoint intervals $I_1, \dots, I_k \subseteq [j_i, j_{i+1}]$ such that

- $\phi(\alpha[I_h]) \in \Lambda_{16}$,
- $\Delta(\alpha[I_h]) \in \mathbb{Z}C$ and $\Delta(\alpha[I_h]) > 0$ for all $h \in [1, k]$.

Recall $\text{VALUES}(\alpha[1, a]) \subseteq [\frac{N}{3}, N)$ and $\frac{N}{3} - \Gamma_C > \frac{M_C}{3} - \Gamma_C > \Gamma_C > \max(\text{Consts}(C))$, where the inequalities follows from M_C 's and Γ_C 's definition on page 15. It follows that $\widehat{\chi}_i$ has all its counter values (except for the first one) in $[\frac{N}{3} - \Gamma_C, N - \Gamma_C)$. Moreover, the first transition's operation must be a $+1$ update and therefore cannot be a test, and hence $\widehat{\chi}_i$ is an $(N - \Gamma_C)$ -run from $q_{j_i}(z_{j_i})$ to $q_{j_{i+1}}(z_{j_{i+1}} - \Gamma_C)$. □

Subclaim 2 *Let $i \in [1, t]$ be even. Then there exists an $(N - \Gamma_C)$ -run $\widehat{\chi}_i$ from $q_{j_i}(z_{j_i} - \Gamma_C)$ to $q_{j_{i+1}}(z_{j_{i+1}})$.*

Proof of Subclaim 2. Analogously, by use of Lemma 22, for $i \in [1, t]$ even, there exists an $(N - \Gamma_C)$ -semirun χ' from $q_{j_i}(z_{j_i})$ to $q_{j_{i+1}}(z_{j_{i+1}} + \Gamma_C)$, from which we obtain an $(N - \Gamma_C)$ -semirun $\widehat{\chi}_i = \chi' - \Gamma_C$ from $q_{j_i}(z_{j_i} - \Gamma_C)$ to $q_{j_{i+1}}(z_{j_{i+1}})$ by shifting χ' by $-\Gamma_C$. Moreover, as $\text{VALUES}(\alpha[1, a]) \subseteq [\frac{N}{3}, N)$ and $\frac{N}{3} - \Gamma_C > \Gamma_C > \max(\text{Consts}(C))$ as seen in the proof of Subclaim 1, $\widehat{\chi}_i$ is an $(N - \Gamma_C)$ -run as required. □

To finish the proof of the existence of an $(N - \Gamma_C)$ -run from $q_0(z_0)$ to $q_{a+1}(z_{a+1} - \Gamma_C)$ we make a case distinction on the parity of t .

Assume first that the parity of t is odd. By applying Subclaims 1 and 2 to the runs χ_1, \dots, χ_t appropriately we obtain the $(N - \Gamma_C)$ -run

$$\widehat{\chi}_1 \cdots \widehat{\chi}_t$$

from $q_{j_1}(z_{j_1})$ to $q_{j_{t+1}}(z_{j_{t+1}} - \Gamma_C)$. Since t is odd we have that χ_t is of type a), $z_{j_{t+1}} \in [N - \Gamma_C, N)$ and $\text{VALUES}(\zeta) \subseteq [N - \Gamma_C - \Upsilon_C, N)$. As $(N - \Gamma_C - \Upsilon_C) - \Gamma_C > (M_C - \Gamma_C - \Upsilon_C) - \Gamma_C > \max(\text{Consts}(\mathcal{C}))$, following from M_C 's, Γ_C 's and Υ_C 's definition on page 15, and $\phi(\alpha) = \varepsilon$, it follows that

$$\widehat{\chi}_1 \cdots \widehat{\chi}_t (\zeta - \Gamma_C)$$

is an $(N - \Gamma_C)$ -run from $q_{j_1}(z_{j_1})$ to $q_{j_{t+2}}(z_{j_{t+2}} - \Gamma_C)$. Recall that $z_{a+1} < \frac{5N}{3}$ by case assumption and also recall that $N > M_C$. Since $\text{VALUES}(\zeta) \subseteq [N - \Gamma_C - \Upsilon_C, N)$ we have $z_a = z_{j_{t+2}} \geq N - \Gamma_C - \Upsilon_C$ and hence $0 < \Delta(\sigma, a) < \frac{5N}{3} - (N - \Gamma_C - \Upsilon_C) \leq \frac{2N}{3} + \Gamma_C + \Upsilon_C < \frac{2N}{3} + \frac{M_C}{3} < N$, where the penultimate inequality follows from the definition of M_C on page 15. Hence, as σ_a is not a test nor a $+p$ -transition we have that

$$\widehat{\chi}_1 \cdots \widehat{\chi}_t (\zeta - \Gamma_C) (\sigma[a, a + 1] - \Gamma_C)$$

is an $(N - \Gamma_C)$ -run from $q_{j_1}(z_{j_1}) = q_0(z_0)$ to $q_{j_{t+2}}(z_{j_{t+2}} - \Gamma_C) = q_{a+1}(z_{a+1} - \Gamma_C)$ as required.

Let us now treat the case when t is even. It follows $\text{VALUES}(\zeta) \subseteq [0, N - \Gamma_C)$, in particular $z_a \in [0, N - \Gamma_C)$. Again,

$$\widehat{\chi}_1 \cdots \widehat{\chi}_t$$

is an $(N - \Gamma_C)$ -run from $q_{j_1}(z_{j_1}) = q_0(z_0)$ to $q_{j_{t+1}}(z_{j_{t+1}})$. Since $z_{a+1} \geq N$ and $z_a < N - \Gamma_C$ it follows that σ_a is a $+p$ -transition, in particular $\Delta(\sigma, a) > \Gamma_C$. Thus,

$$\widehat{\chi}_1 \cdots \widehat{\chi}_t \zeta \tau,$$

where $\tau = q_a(z_a) \xrightarrow{\sigma_a \cdot N - \Gamma_C} q_{a+1}(z_{a+1} - \Gamma_C)$ with $\Delta(\tau) = \Delta(\sigma, a) - \Gamma_C = N - \Gamma_C$, is an $(N - \Gamma_C)$ -run from $q_{j_1}(z_{j_1}) = q_0(z_0)$ to $q_{j_{t+2}}(z_{j_{t+2}} - \Gamma_C) = q_{a+1}(z_{a+1} - \Gamma_C)$, as required.

It remains to discuss the case when $t = 0$. This case can be proven analogously. Indeed, from $t = 0$ it follows immediately that $\alpha = \zeta$ and $\text{VALUES}(\zeta) \subseteq [0, N - \Gamma_C)$ and the proof is analogous as the case when $t > 0$ and when t is even.

Let us now sketch the proof of Point (2) of Claim 4. Let us assume $z_a \in [\frac{N}{3} + \Upsilon_C, N)$. Similarly as in Point (1) we can factorize α as $\alpha = (\prod_{i=1}^t \chi_i) \zeta$ and Subclaims 1 and 2 hold again.

If t is odd, then by Subclaims 1 and 2 we have that the run $(\prod_{i=1}^t \widehat{\chi}_i) (\zeta - \Gamma_C)$, stipulating that $\widehat{\chi}_i$ is the of Subclaims 1 and 2 respectively (depending on the parity of i), is the desired $(N - \Gamma_C)$ -run from $q_0(z_0)$ to $q_a(z_a - \Gamma_C)$.

If t is even, then again by Subclaims 1 and 2 we have that $\xi = (\prod_{i=1}^t \widehat{\chi}_i) \zeta$ is an $(N - \Gamma_C)$ -run from $q_0(z_0)$ to $q_a(z_a)$, where again $\widehat{\chi}_i$ is defined as above. By definition the run ξ does not contain any $+p$ -transitions nor $-p$ -transitions, thus $\phi(\xi) = \varepsilon \in \Lambda_8$. By construction also the run ξ has all counter values, besides the

first, above $\frac{N}{3} - \Gamma_C > \Gamma_C + \max(\text{Consts}(C))$. Moreover, as $z_a \geq \frac{N}{3} + \Upsilon_C$ and $z_0 < \frac{N}{3}$, we have $\Delta(\xi) > \Upsilon_C$. We can thus apply Lemma 22 to ξ , obtaining an $(N - \Gamma_C)$ -run ξ' from $q_0(z_0)$ to $q_a(z_a - \Gamma_C)$, as required. \square

We now conclude the proof of our statement by treating the only remaining case, the case when σ is such that $z_i < N$ for all $i \in [1, m - 1]$. In this case we can factorize σ as $\sigma = \prod_{i=1}^t \chi_i \zeta$ similarly as done in the proof of Point (1) of Claim 4, where t is even, and analogously prove that $\prod_{i=1}^t \widehat{\chi}_i \zeta$ is an $(N - \Gamma_C)$ -run from $q_0(z_0)$ to $q_m(z_m)$, where $\widehat{\chi}_i$ is the output of Subclaim 1 and 2 respectively (depending on the parity of i).

10 Conclusion

In this paper we have shown that the reachability problem for parametric timed automata with two parametric clocks and one parameter is complete for exponential space.

For the lower bound proof, inspired by [13, 14], we made use of two results from complexity theory. First, we made use of a serializability characterization of EXPSpace from [13] which is a padded version of the serializability characterization of PSPACE from [20], which in turn has its roots in Barrington's Theorem [7]. Second, we made use of a result of Chiu, Davida, Litow that states that numbers in Chinese remainder representation can be translated into binary representation in NC^1 (and thus in logarithmic space). We are convinced that it is worthwhile to develop a suitable programming language that serves as a unifying framework in that it provides an interface for proving lower bounds for various problems involving automata. In a sense, we have developed the corresponding interface "by hand" when defining how parametric timed automata can compute functions (Definition 6).

For the EXPSpace upper bound we first followed the approach of Bundala and Ouaknine [10] by providing an exponential time translation from reachability in parametric timed automata with two parametric clocks and one parameter (i.e. (2, 1)-PTA) to reachability in parametric one-counter automata (POCA) over one parameter, yet on a slightly less expressive POCA model as introduced in [10]. We then studied the reachability in POCA with one parameter p . Our main result, the Small Parameter Theorem (Theorem 18), states that such a parametric one-counter automaton (POCA) has an accepting run all of whose counter values lie in $[0, 4 \cdot p]$ if, and only if, there exists such an accepting run for some p that is at most exponential in the size of the POCA. Since the translation from (2, 1)-PTA to POCA is computable in exponential time, this gives a doubly exponential upper bound on the parameter value of the original (2, 1)-PTA and hence an EXPSpace upper bound for reachability in (2, 1)-PTA (Corollary 19).

In proving the Small Parameter Theorem we introduced the notion of semiruns and gave several techniques for manipulating them. The Depumping Lemma 22 allowed us to construct from semiruns with large absolute counter effect new semiruns with a smaller absolute counter effect. The Bracket Lemma 23 allowed us

to find in semiruns having a sufficiently large absolute counter effect and satisfying some majority condition on the number of occurrences of $+p$ -transitions and $-p$ -transitions some subsemirun that has again a large absolute counter effect and moreover some equal bracketing properties. Our Hill and Valley Lemma (Lemma 28) allowed to turn for sufficiently large N any N -semirun that is either a hill or a valley into an N' -semirun for some $N' < N$. Our 5/6-Lemma (Lemma 39) allowed to turn for sufficiently large N any N -semirun with an absolute counter effect of at most $5/6N$ into an N' -semirun for some $N' < N$.

We hope that extensions of our techniques provide a line of attack for finally showing decidability (and the precise complexity) of reachability in parametric timed automata with two parametric clocks over an arbitrary number of parameters (i.e. $(2, *)$ -PTA). For these however, it seems that the reduction to POCA indeed requires the presence of the above-mentioned $+ [0, p]$ -transitions. When analyzing runs in the corresponding more general POCA model that in turn also involves an arbitrary number of parameters, it will become necessary to “de-scale” semiruns in the following sense. Already in the presence of two parameters one can see that it becomes necessary to decrease the value of both parameters simultaneously proportionally: for instance one can build a $(2, 2)$ -PTA for which reachability holds only if the first parameter is a multiple of the second parameter.

Appendix A Proof of the Reduction (Theorem 16)

In this appendix, we concern ourselves with proving the reduction from parametric timed automata with two parametric clocks and one parameter to parametric one-counter automata with one parameter, which was stated in the main theorem of Section 4, Theorem 16.

A.1 Overview of the Proof of the Reduction

In this section, we recall the main theorem of Section 4, and provide its proof overview.

Let us first recall Theorem 16.

Theorem 48 (*Theorem 16*) *The following is computable in exponential time:*

INPUT: A $(2, 1)$ -PTA \mathcal{A} .

OUTPUT: A POCA \mathcal{C} over one parameter

such that

1. *for all $N \in \mathbb{N}$ all accepting N -runs π in \mathcal{C} satisfy $\text{VALUES}(\pi) \subseteq [0, 4 \cdot \max(N, |\mathcal{C}|)]$, and*
2. *reachability holds for \mathcal{A} if, and only if, reachability holds for \mathcal{C} .*

A more general (but strictly speaking incomparable) result involving two parametric clocks but an arbitrary number of parameters instead of only one has already been proven in [10], however with a different POCA formalism: Bundala and Ouaknine’s

model for POCA differs in that it contains operations that allow to nondeterministically add to the counter a value that lies in $[0, p]$. By restricting ourselves to the case of only one parameter p , we will prove in a thorough analysis that we no longer need such operations in the construction.

As in [10] we follow the following proof strategy:

- In Section A, we reduce the reachability problem of a parametric timed automaton $\mathcal{A} = (Q_{\mathcal{A}}, \Omega_{\mathcal{A}}, P, R_{\mathcal{A}}, q_{\mathcal{A}}, F_{\mathcal{A}})$ — in our setting later with two parametric clocks — to the reachability problem of a so-called *parametric 0/1 timed automaton* $\mathcal{B} = (Q_{\mathcal{B}}, \Omega_{\mathcal{B}}, P, R_{\mathcal{B},0}, R_{\mathcal{B},1}, q_{\mathcal{B}}, F_{\mathcal{B}})$, where $\Omega_{\mathcal{B}} \subseteq \Omega_{\mathcal{A}}$ contains only the non-parametric clocks of $\Omega_{\mathcal{A}}$, and $\text{Consts}(\mathcal{B}) = \{0\}$.
- In Section A we present the region abstraction technique introduced by Alur and Dill in [2] to mimic region-restricted runs (runs inside a region) of parametric 0/1 timed automata with one parameter by arithmetic progressions.
- Finally, we present the final step of the reduction in Section A, where it is shown how to use the above-mentioned technique to mimic reset-free region-restricted runs in \mathcal{B} , and furthermore how to provide a construction in order to mimic resets in \mathcal{B} . The precise construction itself mainly deviates from [10] in the gadget construction for resets.

A.2 How to Remove Non-Parametric Clocks and Non-Parametric Guards

In this subsection we show how non-parametric guards and non-parametric clocks can be eliminated from parametric timed automata. Initially introduced in [3] we define the notion of parametric 0/1 timed automata: these are essentially parametric timed automata in which each rule dictates whether a unit of time passes or not. Alur, Henzinger and Vardi have already shown in [3] how the reachability problem for parametric timed automata can be reduced to the reachability problem for parametric 0/1 timed automata that do not contain any non-parametric clocks. We will provide in Lemma 50 below an analogous reduction by not only eliminating all non-parametric clocks, but also all non-parametric guards (except for empty guards).

A *parametric 0/1 timed automaton* (0/1-PTA for short) is a tuple

$$\mathcal{B} = (Q, \Omega, P, R_0, R_1, q_{init}, F),$$

where $\mathcal{B}_i = (Q, \Omega, P, R_i, q_{init}, F)$ is a PTA for all $i \in \{0, 1\}$. For simplicity we define its *size* as $|\mathcal{B}| = |\mathcal{B}_0| + |\mathcal{B}_1|$. Analogously, a clock $\omega \in \Omega$ is *parametric* if it is parametric in \mathcal{B}_0 or in \mathcal{B}_1 . We analogously denote the constants of \mathcal{B} by $\text{Consts}(\mathcal{B})$ and its configurations by $\text{Conf}(\mathcal{B})$.

Definition 49 For each $i \in \{0, 1\}$, each parameter valuation $\mu : P \rightarrow \mathbb{N}$ and each $(\delta, t) \in R_i \times \mathbb{N}$ with $\delta = (q, g, U, q') \in R_i$, we define the binary relation $\xrightarrow{\delta, i, \mu}$ over $\text{Conf}(\mathcal{B})$ as $q(v) \xrightarrow{\delta, i, \mu} q'(v')$ if $v + i \models_{\mu} g$, $v'(u) = 0$ for all $u \in U$ and $v'(\omega) = v(\omega) + i$ for all $\omega \in \Omega \setminus U$.

As expected, we write $q(v) \xrightarrow{\mu} q'(v')$ if $q(v) \xrightarrow{\delta, i, \mu} q'(v')$ for some $i \in \{0, 1\}$, and some $\delta \in R_i$. The notions of a (reset-free) μ -run (resp. N -run) and when reachability holds for \mathcal{B} are also defined as expected.

The convention used in this and the following subsections is that parametric 0/1 timed automata are denoted by \mathcal{B} . The main result of this subsection is the following lemma, stated slightly less general in [3] in that there is no requirement $\text{Consts}(\mathcal{B}) = \{0\}$.

Lemma 50 ([3]) *The following is computable in exponential time:*

INPUT: A PTA $\mathcal{A} = (Q_{\mathcal{A}}, \Omega_{\mathcal{A}}, P, R_{\mathcal{A}}, q_{\mathcal{A}}, F_{\mathcal{A}})$.

OUTPUT: A 0/1-PTA $\mathcal{B} = (Q_{\mathcal{B}}, \Omega_{\mathcal{B}}, P, R_{\mathcal{B},0}, R_{\mathcal{B},1}, q_{\mathcal{B}}, F_{\mathcal{B}})$, where $\Omega_{\mathcal{B}} \subseteq \Omega_{\mathcal{A}}$ contains precisely the parametric clocks of $\Omega_{\mathcal{A}}$, $\text{Consts}(\mathcal{B}) = \{0\}$, and such that reachability holds for \mathcal{A} , if, and only if, reachability holds for \mathcal{B} .

We adjust the proof from [3]. While the idea of the construction remains the same, ours slightly deviates in that we explicitly have $\text{Consts}(\mathcal{B}) = \{0\}$, i.e. we remove all non-parametric guards of the form $\omega \bowtie c$ with $c \neq 0$ as well as all non-parametric clocks.

Proof Let us assume without loss of generality that \mathcal{A} contains at least one parametric clock and let us fix one such clock x . We define the empty guard g_{ϵ} as $g_{\epsilon} = x \geq 0$ and observe that this guard is always satisfied. Let $c_{\max} = \max(\text{Consts}(\mathcal{A}))$ denote the largest constant appearing in \mathcal{A} . Note that once the value assigned to a clock ω by a valuation v is strictly above c_{\max} , the precise value $v(\omega)$ is no longer of importance, merely the fact that $v(\omega)$ exceeds c_{\max} is relevant. Since we work with discrete time configurations, the value assigned to ω is always a non-negative integer. We will eliminate all non-parametric clocks of $\Omega_{\mathcal{A}}$ by storing in the state space of \mathcal{B} the values of clocks up to $c_{\max} + 1$, where $c_{\max} + 1$ will stand for any value greater c_{\max} . Moreover we eliminate all non-empty non-parametric guards by also storing in the state space of \mathcal{B} the values of parametric clocks in the same fashion. Formally, we define $\Omega_{\mathcal{B}} = \{\omega \in \Omega_{\mathcal{A}} \mid \omega \text{ is parametric}\}$, $Q_{\mathcal{B}} = Q_{\mathcal{A}} \times [0, c_{\max} + 1]^{\Omega_{\mathcal{A}}}$, P is the same in both automata, $F_{\mathcal{B}} = F_{\mathcal{A}} \times [0, c_{\max} + 1]^{\Omega_{\mathcal{A}}}$, and $q_{\mathcal{B}} = (q_{\mathcal{A}}, v_0)$, where $v_0(\omega) = 0$ for all $\omega \in \Omega_{\mathcal{A}}$.

We ensure that the stored clocks progress simultaneously with the remaining parametric clocks by exploiting the fact that the rules dictate whether or not time elapses, and build the rules of \mathcal{B} such that the $+1$ rules correspond to the progress of time in \mathcal{A} whereas the $+0$ rules correspond to using a rule in \mathcal{A} . Formally,

- for every $q \in Q_{\mathcal{A}}$, $v \in [0, c_{\max} + 1]^{\Omega_{\mathcal{A}}}$, we introduce a rule of the form $((q, v), g_{\epsilon}, \emptyset, (q, v'))$ in $R_{\mathcal{B},1}$, where $v'(\omega) = \min\{v(\omega) + 1, c_{\max} + 1\}$ for all $\omega \in \Omega_{\mathcal{A}}$,

- for every $(q, g, U, q') \in R_{\mathcal{A}}$ with $g \in \mathcal{G}(\Omega_{\mathcal{B}}, P)$ a parametric guard, every $v \in [0, c_{\max} + 1]^{\Omega_{\mathcal{A}}}$ we introduce a rule $((q, v), +0, g, U', (q', v')) \in R_{\mathcal{B},0}$, where v' is obtained from v except for assigning 0 to every clock in U and $U' = U \cap \Omega_{\mathcal{B}}$ is the subset of parametric clocks of U , and
- for every $(q, g, U, q') \in R_{\mathcal{A}}$ with $g \in \mathcal{G}(\Omega_{\mathcal{A}}, P)$ a non-parametric guard, every $v \in [0, c_{\max} + 1]^{\Omega_{\mathcal{A}}}$ such that $v \models g$, we introduce a rule $((q, v), +0, g_{\epsilon}, U', (q', v')) \in R_{\mathcal{B},0}$, where v' is obtained from v except for assigning 0 to every clock in U and $U' = U \cap \Omega_{\mathcal{B}}$ is the subset of parametric clocks of U .

□

For the remaining subsections, let us fix a PTA $\mathcal{A} = (Q_{\mathcal{A}}, \Omega_{\mathcal{A}}, P, R_{\mathcal{A}}, q_{\mathcal{A}}, F_{\mathcal{A}})$ with two parametric clocks x and y , and with $P = \{p\}$. Let us also fix the 0/1-PTA $\mathcal{B} = (Q_{\mathcal{B}}, \Omega_{\mathcal{B}}, P, R_{\mathcal{B},0}, R_{\mathcal{B},1}, q_{\mathcal{B}}, F_{\mathcal{B}})$ produced by Theorem 50 applied to PTA \mathcal{A} , and recall that \mathcal{B} satisfies

- $P = \{p\}$,
- $\Omega_{\mathcal{B}} = \{x, y\}$, where x and y are parametric,
- $\text{Consts}(\mathcal{B}) = \{0\}$, and
- reachability holds for \mathcal{A} if, and only if, reachability holds for \mathcal{B} .

A.3 Capturing Reset-Free Runs via the Region Abstraction Technique

In this section we perform another preliminary construction before providing the proof of Theorem 16. We build parametric one-counter automata without tests and with updates only in $\{+0, +1\}$ that can mimic the behavior of parametric 0/1 timed automata with two parametric clocks and one parameter inside a reset-free run having only clocks valuations in a certain set. We first simply remove rules resetting at least one clock. We then show how to remove non-empty guards from parametric 0/1 timed automata taking inspiration from the region abstraction technique for timed automata first introduced in [2]. The technique appears already in the proofs of reduction from parametric timed automata with two clocks to parametric one-counter automata given in [16, 17] (for empty sets of parameters) and in [10]. We refer to [6] for further discussions on the region abstraction technique.

Recall that our fixed 0/1-PTA \mathcal{B} satisfies $P = \{p\}$, $\Omega_{\mathcal{B}} = \{x, y\}$, where x and y are parametric, and $\text{Consts}(\mathcal{B}) = \{0\}$.

Let us now explain the set of regions. For any valuation μ that assigns to our only parameter p the value N we prefer to write \models_N instead of \models_{μ} . Moreover, we prefer to view clock valuations $v : \{x, y\} \rightarrow \mathbb{N}$ as pairs $(v(x), v(y))$. Sets of clock valuations will correspondingly be denoted as subsets of $\mathbb{N} \times \mathbb{N}$. The regions are essentially, when assigning N to the one parameter p , maximal subsets of $\mathbb{N} \times \mathbb{N}$ equivalent with regards to the sets of guards of \mathcal{B} their valuations satisfy. In other words, the regions we define are equivalence classes for the relation \sim_N , where $v \sim_N v'$ if for all possible guards g of \mathcal{B} we have $v \models_N g$ if, and only if, $v' \models_N g$. Since the latter guards can only compare (using comparisons $<, \leq, =, \geq, >$) the clock valuations

against values from the set $\{0, N\}$, it follows that \sim_N has at most the following 16 equivalence classes, each of which we call *region* in the following:

$(0, 0), (0, N), (N, 0), (N, N)$

to respectively denote the singleton sets $\{(0, 0)\}, \{(0, N)\}, \{(N, 0)\}, \{(N, N)\}$,

$(0, 0) \leftrightarrow (0, N), (N, 0) \leftrightarrow (N, N), (0, N) \leftrightarrow (0, +\infty), (N, N) \leftrightarrow (N, +\infty)$,

to respectively denote the sets

$\{(0, i) \mid 0 < i < N\}, \{(N, i) \mid 0 < i < N\}, \{(0, i) \mid i > N\}, \{(N, i) \mid i > N\}$,

$(0, 0) \leftrightarrow (N, 0), (0, N) \leftrightarrow (N, N), (N, 0) \leftrightarrow (+\infty, 0), (N, N) \leftrightarrow (+\infty, N)$,

to respectively denote the sets

$\{(i, 0) \mid 0 < i < N\}, \{(i, N) \mid 0 < i < N\}, \{(i, 0) \mid i > N\}, \{(i, N) \mid i > N\}$,

LOWER-LEFT, UPPER-LEFT, LOWER-RIGHT, UPPER-RIGHT

to respectively denote the sets

$\{(i, j) \mid 0 < i, j < N\}, \{(i, j) \mid 0 < i < N, j > N\}, \{(i, j) \mid i > N, 0 < j < N\}, \{(i, j) \mid i, j > N\}$.

We refer to Fig. 18 for an illustration of the different regions.

As expected, for every guard g of \mathcal{B} and every region \mathcal{R} we write $\mathcal{R} \models_N g$ if $v \models_N g$ for all $v \in \mathcal{R}$. For each region \mathcal{R} we say a run $q_0(v_0) \xrightarrow{\delta_1, i_1, \mu} q_1(v_1) \dots \xrightarrow{\delta_n, i_n, \mu} q_n(v_n)$ of \mathcal{B} is \mathcal{R} -restricted if $v_j \in \mathcal{R}$ for all $j \in [0, n]$.

We remark that, in any \mathcal{R} -restricted run in \mathcal{B} , the set of guards being satisfied or not are the same for all configurations appearing in it. Thus, the set of guards that

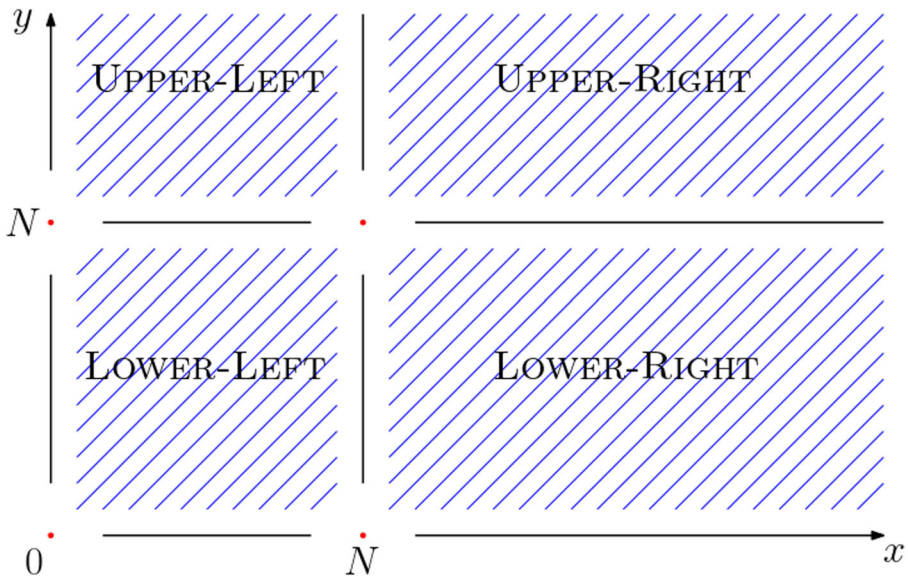


Fig. 18 An illustration of the different regions

are satisfied only depend on the region and not the particular configurations of the \mathcal{R} -restricted run. We simply write $\mathcal{R} \models g$ when a region \mathcal{R} satisfies guard g .

We use this property to remove guards from the parametric 0/1 timed automaton \mathcal{B} while still mimicking reset-free \mathcal{R} -restricted runs.

For each region \mathcal{R} we introduce the *region automaton* $\mathcal{B}_{\mathcal{R}}$ obtained from \mathcal{B} instantiating all comparisons appropriately and by removing all rules that reset some clock. We fix g_{ϵ} to be the empty guard $x \geq 0$. Formally, the automaton $\mathcal{B}_{\mathcal{R}}$ is the 0/1-PTA obtained from \mathcal{B} by

- removing all rules (q, g, U, q') with $U \neq \emptyset$,
- removing all rules (q, g, \emptyset, q') for which $\mathcal{R} \not\models g$, and
- replacing all rules (q, g, \emptyset, q') for which $\mathcal{R} \models g$ by $(q, g_{\epsilon}, \emptyset, q')$.

The following lemma is immediate.

Lemma 51 *From the 0/1-PTA $\mathcal{B} = (Q_{\mathcal{B}}, \{x, y\}, \{p\}, R_{\mathcal{B},0}, R_{\mathcal{B},1}, q_{\mathcal{B}}, F_{\mathcal{B}})$ with $\text{Consts}(\mathcal{B}) = \{0\}$ one can compute in polynomial time (in $|\mathcal{B}|$) the sixteen 0/1-PTA $\{\mathcal{B}_{\mathcal{R}} \mid \mathcal{R} \text{ is a region}\}$ such that for all $N \in \mathbb{N}$, all regions \mathcal{R} , and all configurations $q(v)$ and $q'(v')$ for which $v, v' \in \mathcal{R}$ the following are equivalent:*

- *There exists an \mathcal{R} -restricted reset-free N -run from $q(v)$ to $q'(v')$ in \mathcal{B} .*
- *There exists an N -run from $q(v)$ to $q'(v')$ in $\mathcal{B}_{\mathcal{R}}$.*

A.3.1 Capturing Reset-Free Runs via Arithmetic Progressions

A *one-counter automaton* is a POCA $\mathcal{C} = (Q, P, R, q_{init}, F)$ with $P = \emptyset$, and with only > 0 , ≥ 0 , and $= 0$ tests. As $P = \emptyset$, we write $q(z) \rightarrow q'(z')$ instead of $q(z) \xrightarrow{\mu} q'(z')$ for one-counter automata.

Given a one-counter automaton \mathcal{C} and two of its control states q and q' we define the set $\Pi(\mathcal{C}, q, q')$ of counter values that configurations in control state q' can have from runs starting in $q(0)$:

$$\Pi(\mathcal{C}, q, q') = \{v \in \mathbb{N} \mid q(0) \rightarrow^* q'(v)\}.$$

For all $a \geq 0$ and $b \geq 1$ we define the arithmetic progression $a + b\mathbb{N}$ as $a + b\mathbb{N} = \{a + b \cdot n \mid n \in \mathbb{N}\}$. The following theorem is an immediate consequence of a result by To analyzing the succinctness between unary finite automata and arithmetic progressions [25].

Theorem 52 (Theorem 2 in [25]) *Let $\mathcal{C} = (Q, \emptyset, R, q_{init}, F)$ be a one-counter automaton with $+0, +1$ updates only. Then for every two control states $q, q' \in Q$ one can compute in polynomial time a set $\{(a_j, b_j) \in \mathbb{N}^2 \mid j \in [1, r]\}$ such that $\Pi(\mathcal{C}, q, q') = \bigcup_{1 \leq j \leq r} a_j + b_j\mathbb{N}$, where moreover $r \in O(|Q|^2)$, $a_j \in O(|Q|^2)$, and $b_j \in O(|Q|)$ for all $j \in [1, r]$.*

We remark that Theorem 52 also holds in the presence of transitions that decrement the counter, cf. Lemma 6 in [15].

Remark 53 Let \mathcal{R} be a region and let $\mathcal{B}_{\mathcal{R}} = (Q_{\mathcal{B}_{\mathcal{R}}}, \{x, y\}, \{p\}, R_{\mathcal{B}_{\mathcal{R},0}}, R_{\mathcal{B}_{\mathcal{R},1}}, q_{\mathcal{B}_{\mathcal{R}},init}, F_{\mathcal{B}_{\mathcal{R}}})$ be the 0/1-PTA for \mathcal{R} . Then all rules in $R_{\mathcal{B}_{\mathcal{R},0}} \cup R_{\mathcal{B}_{\mathcal{R},1}}$ have as guard the empty guard g_{ε} . Let $\widehat{\mathcal{B}}_{\mathcal{R}} = (Q_{\mathcal{B}_{\mathcal{R}}}, \emptyset, R, q_{\mathcal{B}_{\mathcal{R}},init}, F_{\mathcal{B}_{\mathcal{R}}})$ be the one-counter automaton, where

$$R = \{(q, +i, q') \mid (q, g_{\varepsilon}, \emptyset, q') \in R_{\mathcal{B}_{\mathcal{R},i}}, i \in \{0, 1\}\}$$

only contains +0 and +1 updates and does not contain any = 0-tests, Then for all $(k, \ell) \in \mathbb{N} \times \mathbb{N}$, all $q, q' \in Q$, and all $n \in \mathbb{N}$ the following are equivalent:

- There is a run from $q(k, \ell)$ to $q'(k + n, \ell + n)$ in $\mathcal{B}_{\mathcal{R}}$.
- There is a run from $q(0)$ to $q'(n)$ in $\widehat{\mathcal{B}}_{\mathcal{R}}$.

Notably, every run from $q(k, \ell)$ to $q'(k', \ell')$ in $\mathcal{B}_{\mathcal{R}}$ satisfies $k' = k + n$ and $\ell' = \ell + n$ for some $n \in \mathbb{N}$.

We apply Theorem 52 to all one-counter automata $\widehat{\mathcal{B}}_{\mathcal{R}}$ from Remark 5. This yields the following characterization.

Lemma 54 *From the 0/1-PTA $\mathcal{B} = (Q_{\mathcal{B}}, \{x, y\}, \{p\}, R_{\mathcal{B},0}, R_{\mathcal{B},1}, q_{\mathcal{B}}, F_{\mathcal{B}})$ for every two control states $q, q' \in Q_{\mathcal{B}}$, for all regions \mathcal{R} one can compute in polynomial time (in $|\mathcal{B}|$) a set $\{(a_j, b_j) \in \mathbb{N}^2 \mid j \in [1, r]\}$ such that for all $N, t \in \mathbb{N}$ and all $v, v + t \in \mathcal{R}$ the following are equivalent:*

- *There exists a reset-free \mathcal{R} -restricted N -run from $q(v)$ to $q'(v + t)$ in \mathcal{B} .*
- *$t \in \bigcup_{1 \leq j \leq r} a_j + b_j \mathbb{N}$.*

Moreover, $r \in O(|Q_{\mathcal{B}}|^2)$, $a_j \in O(|Q_{\mathcal{B}}|^2)$, and $b_j \in O(|Q_{\mathcal{B}}|)$ for all $j \in [1, r]$.

A.4 Proof of Theorem 16: Construction of \mathcal{C}

Let us recall the fixed 0/1-PTA $\mathcal{B} = (Q_{\mathcal{B}}, \Omega_{\mathcal{B}}, \{p\}, R_{\mathcal{B}}, q_{\mathcal{B}}, F_{\mathcal{B}})$ the 0/1-PTA obtained from \mathcal{A} by Theorem 50, and recall that \mathcal{B} satisfies

- $P = \{p\}$,
- $\Omega_{\mathcal{B}} = \{x, y\}$ and x and y are parametric, and
- $\text{Consts}(\mathcal{B}) = \{0\}$.

Recall also the set of regions of \mathcal{B} defined in Section A. We want to construct some POCA $\mathcal{C} = (Q_{\mathcal{C}}, \{p\}, R_{\mathcal{C}}, q_{\mathcal{C}}, F_{\mathcal{C}})$ such that reachability holds for \mathcal{B} if, and only if, reachability holds for \mathcal{C} and moreover for all $N \in \mathbb{N}$, every accepting N -run π in \mathcal{C} satisfies $\text{VALUES}(\pi) \subseteq [0, 4 \cdot \max(N, |\mathcal{C}|)]$.

The to be constructed POCA \mathcal{C} (again over one parameter that will be evaluated to the same value as the only parameter of \mathcal{B}) will test whether an accepting N -run exists in \mathcal{B} by using the definitions of regions and Lemma 54 from the last subsection, but also using additional gadgets to mimic the reset of a clock inside a particular region.

In what follows we denote the current value of the counter of \mathcal{C} by z . For the time being in our construction z can be negative: we will later show how to obtain non-negativity and the required restriction that all N -runs π of \mathcal{C} satisfy $\text{VALUES}(\pi) \subseteq [0, 4N]$.

The idea of the reduction is to factorize any possible accepting N -run into maximal reset-free subruns. We will use the current counter value z of \mathcal{C} to store the clock valuation difference $v(x) - v(y)$, thus initially 0. We remark that between two consecutive resets, the difference $v(x) - v(y)$ stays the same throughout, but after some clock of $\Omega_{\mathcal{B}}$ (either x or y) is reset, this particular reset clock will be equal to zero but not necessarily the other one. The counter of \mathcal{C} therefore needs to be modified accordingly. As expected, we construct \mathcal{C} in such a way that after a reset of y , the counter value z equals $v(x)$, and after a reset of x the counter value z equals $-v(y)$. See Fig. 19 for an idea of the relationship between $v(x)$, $v(y)$ and z along the curve of the clock values.

Notice that once the value of a clock becomes strictly larger than N , its exact value is irrelevant to any future parametric comparison in \mathcal{B} , hence one only needs to remember that its value is strictly larger than N . Thus, our counter z will only track the values $v(x)$ and $v(y)$ up to N and possibly remember which of the two clock values exceeds N . Therefore, when a reset occurs and we store the value of the other clock in the counter, if it exceeds this N we can and will replace it by $N + 1$, and if it is strictly below $-N$, we can and will replace it by $-N - 1$. Let us therefore assume for now that the value of the counter z following the last reset is in this interval $[-N - 1, N + 1]$. Initially this is surely true as initially the value of the counter is 0. We will show how to provide this invariant on the next reset assuming it holds on the last reset.

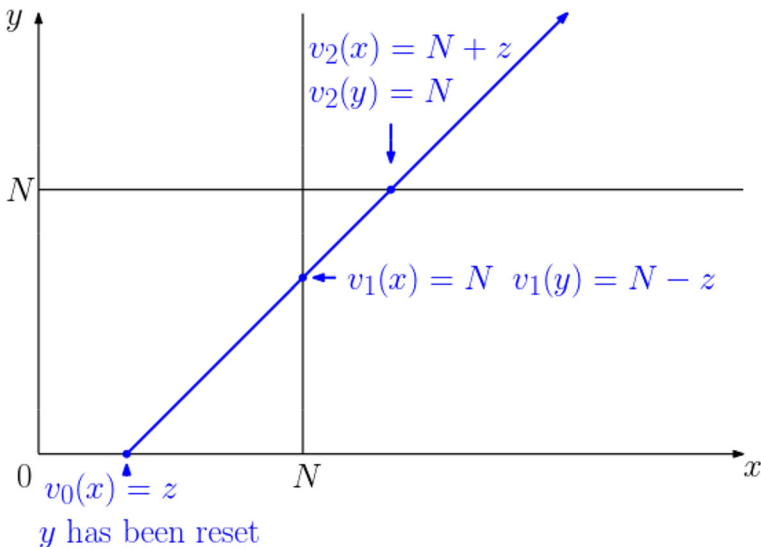


Fig. 19 Curve of the clock values after a reset of clock y . Initially the difference z between the values of x and y is equal to the value of x

Recall the definition of regions from Section A. Let us assume a subrun $q(v) \xrightarrow{N} q'(v') \xrightarrow{N}^* q''(v'') \xrightarrow{N} q'''(v''')$ starting and ending by a reset of at least one of the two clocks $\{x, y\}$ and where $q'(v') \xrightarrow{N}^* q''(v'')$ is reset-free. We want \mathcal{C} to be able to check whether such a run can exist.

For rest of the proof let us assume without loss of generality that y is reset along $q(v) \xrightarrow{N} q'(v')$, where the latter configuration can hence be written as $q'(z, 0)$, as we want the counter z to store the value of x .

The POCA \mathcal{C} guesses

- the regions $\mathcal{R}_0, \mathcal{R}_1, \dots, \mathcal{R}_l$ visited and the order in which they are visited, where here by convention \mathcal{R}_k denotes the region assumed to be the k -th visited region,
- the control states s_0, \dots, s_l when each region is visited for the first time,
- the control states q_0, \dots, q_l when each region is visited for the last time,
- the control state q'' in which the next reset of \mathcal{B} occurs, and
- which clock is going to be reset next (either x or y).

Note that there are only a finite number of regions. Our POCA \mathcal{C} then checks that the sequence $\mathcal{R}_0, \mathcal{R}_1, \dots, \mathcal{R}_l$ is valid, retaining the counter value z .

First \mathcal{C} checks that $(z, 0)$ lies in \mathcal{R}_0 i.e. that z is equal to 0 if $\mathcal{R}_0 = (0, 0)$, strictly between 0 and N if $\mathcal{R}_0 = (0, 0) \leftrightarrow (N, 0)$, equal to N if $\mathcal{R}_0 = (N, 0)$ and strictly above N if $\mathcal{R}_0 = (N, 0) \leftrightarrow (+\infty, 0)$. and moreover checks that the guessed regions are adjacent, and that the regions can be visited in the guessed order.

Then \mathcal{C} checks reachability within each individual region using Lemma 54 as follows. To each region \mathcal{R}_k one can associate a set $\{(a_{k,j}, b_{k,j}) \in \mathbb{N}^2 \mid j \in [1, r_k]\}$ obtained by Lemma 54. This allows \mathcal{C} to check, for every $k < l$, for every $v \in \mathcal{R}_k$, $v + t \in \mathcal{R}_k$, reachability of $q_k(v + t)$ from $s_k(v)$ in the region \mathcal{R}_k by checking whether or not $t \in \bigcup_{1 \leq j \leq r} a_j + b_j \mathbb{N}$. In order to check reachability inside a region \mathcal{R}_k of the form (α, β) or $(\alpha, \beta) \leftrightarrow (\gamma, \eta)$ for $\alpha, \beta \in \{0, N\}$, and $\gamma, \eta \in \{0, N, +\infty\}$, it suffices to check that $\bigcup_{1 \leq j \leq r} a_{k,j} + b_{k,j} \mathbb{N}$ contains 0, as the clock values cannot both increment and remain inside these regions, i.e. for any such \mathcal{R}_k , for all $v \in \mathcal{R}_k$, $v + t \in \mathcal{R}_k$ implies that $t = 0$. Indeed, one can check easily check whether $0 \in \bigcup_{1 \leq j \leq r} a_{k,j} + b_{k,j} \mathbb{N}$ by computing $\{(a_{k,j}, b_{k,j}) \in \mathbb{N}^2 \mid j \in [1, r_k]\}$, which can be done in polynomial time in $|\mathcal{B}|$.

Now, to check that an N -run exists in \mathcal{B} in a given region \mathcal{R}_k of the form LOWER-LEFT, LOWER-RIGHT, UPPER-LEFT or UPPER-RIGHT, the automaton \mathcal{C} furthermore distinguishes whether the computation in the region \mathcal{R}_k starts on the left side or on the bottom side, and whether the computation in the region \mathcal{R}_k ends on the right side or on the top side, and uses the semilinearity property to check that the value added to the clocks is indeed in $\bigcup_{1 \leq j \leq r} a_{k,j} + b_{k,j} \mathbb{N}$. Note that the first configuration of LOWER-LEFT is necessarily of the form $s_k(z + 1, 1)$ as y has been assumed to be the last clock to be reset, the first configuration of LOWER-RIGHT is of the form $s_k(z + 1, 1)$ or $s_k(N + 1, N + 1 - z)$, depending on whether it has been

reached from the bottom or from the left corner (or possibly both), and finally note that UPPER-LEFT cannot be reached if y was the last clock to be reset.

Thus, to check reachability inside \mathcal{R}_k , our POCA \mathcal{C} guesses an offset $a = a_{k,j}$ and a period $b = b_{k,j}$ among the generators of $\{(a_{k,j}, b_{k,j}) \in \mathbb{N}^2 \mid j \in [1, r_k]\}$ that it will use to reach q_k . Secondly we define four gadgets in order to handle the three regions possibly traversed, namely LOWER-LEFT, LOWER-RIGHT, and UPPER-RIGHT.

Case 1. Checking reachability in the LOWER-LEFT region.

Here the region is necessarily reached from bottom side as y was the last clock to be reset. Moreover, as clocks progress at the same rate, the region is necessarily exited in the right corner (or both in the right and upper corner). Here \mathcal{C} checks that $q_k(N-1, N-1-z)$ is reachable from $s_k(z+1, 1)$, i.e. \mathcal{C} checks that $(N-1)-(z+1) \in a + b\mathbb{N}$ which in turn is equivalent to checking if $z + 2 - N + a = -n \cdot b$ for some $n \in \mathbb{N}$. Figure 19 for an illustration of the trajectories of the counter values. In order to restore the value z the POCA \mathcal{C} does this by a carefully chosen gadget shown in Fig. 20. Since $(z + 1, 1) \in \text{LOWER-LEFT}$ it follows $z \in [0, N - 2]$, thus the counter value along the gadget stays inside the interval $[-(N - 2), \max(N, a)]$.

Case 2. Checking reachability in the LOWER-RIGHT region when reached from the bottom side.

Here the region is necessarily exited in the top side, and we will show how \mathcal{C} can check that $q_k(N + z - 1, N - 1)$ is reachable from $s_k(z + 1, 1)$ and then restore z . Indeed, since y was the last clock that was reset, due our convention $z \in [-(N + 1), N + 1]$ and by our case we must have $z + 1 \in \{N + 1, N + 2\}$, and therefore $z \in \{N, N + 1\}$. Our POCA distinguishes the two cases $z = N$ and $z = N + 1$ explicitly as follows. To check that that $q_k(N + z - 1, N - 1)$ is reachable from $s_k(z + 1, 1)$ we need to test if $N - 2 \in a + b\mathbb{N}$. Our POCA \mathcal{C} first tests if z equals N or if z equals $N + 1$, then does the test by a carefully chosen sequence of operations that allow to restore the counter value $z \in \{N, N + 1\}$ as can be seen in the gadget in Fig. 21. Since $(z + 1, 1) \in \text{LOWER-RIGHT}$ the counter value along the gadget stays inside the interval $[-(a + 2), N + 1]$.

Case 3. Checking reachability in the LOWER-RIGHT region when reached from the left side.

Here the region is necessarily exited in the top side, and \mathcal{C} checks that $q_k(N + z - 1, N - 1)$ is reachable from $s_k(N + 1, N + 1 - z)$, i.e. \mathcal{C} checks that $(z + N - 1) - (N + 1) \in a + b\mathbb{N}$ or equivalently if $z - 2 \in a + b\mathbb{N}$. Since $(N + 1, N + 1 - z) \in \text{LOWER-RIGHT}$ it follows $z \in [0, N]$. Again by a carefully chosen sequence of operations that allow to restore the counter value $z \in [0, N]$ we can realize this test as seen in the gadget in Fig. 22. Since $(N + 1, N + 1 - z) \in \text{LOWER-RIGHT}$ the counter value along the gadget stays inside the interval $[-(a + 2), N]$.

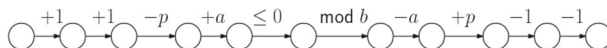


Fig. 20 Gadget testing reachability for Case 1

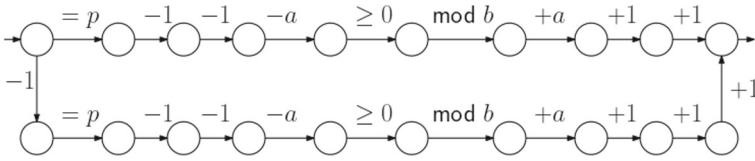


Fig. 21 Gadget testing reachability for Case 2

No other region is reachable from UPPER-RIGHT. Moreover, if y was among the last clocks to be reset, as the clocks valuations increment at the same rate, region UPPER-LEFT is not reachable. Thus the three treated above cases conclude the question of reachability inside a region. Next, in order to test whether or not it is possible to reach \mathcal{R}_{k+1} in state s_{k+1} from \mathcal{R}_k and state q_k , we check whether or not in \mathcal{B} there exists some $+1$ rule of the form $(q_k, g, \emptyset, s_{k+1})$ such that $\mathcal{R}_k \models g$ (and there is hence a corresponding rule in $\mathcal{B}_{\mathcal{R}_k}$).

To finish the construction our POCA \mathcal{C} needs to be able to simulate clock resets in an N -run in \mathcal{B} . The process will depend on the guessed region \mathcal{R}_l in which the reset is assumed to occur. For \mathcal{R}_l of the form (α, β) with $\alpha, \beta \in \{0, N\}$, the precise value of each clock is known: if x is the next clock to be reset, then the new counter value should be $-v(y)$, i.e. $-\beta$, and if y is the next clock to be reset, then the new counter value should be $v(x)$, i.e. α . For \mathcal{R}_l of the form $(\alpha, \beta) \leftrightarrow (\gamma, \beta)$, with $\alpha, \beta \in \{0, N\}$, and with $\gamma \in \{0, N, +\infty\}$, the precise value of each clock again is known: if x is the next clock to be reset, then the new counter value should be $-v(y)$, i.e. $-\beta$. If y is the next clock to be reset, then the new counter value should be $v(x)$, which, when z is the value of x when y was last reset, is equal to z plus the value of y , i.e. $z + \beta$. If z has absolute value at most N , then $z + N$ has absolute value at most $2 \cdot N$. We thus test whether or not the absolute value of $z + \beta$'s exceeds $N + 1$ or not, and, if it is the case, we set it to $N + 1$ before performing any other operation.

The case when \mathcal{R}_l is of the form $(\alpha, \beta) \leftrightarrow (\alpha, \delta)$ with $\alpha, \beta \in \{0, N\}$, and with $\delta \in \{0, N, +\infty\}$ is only possible if $\alpha = \delta = N$ (we refer to Fig. 19) and is done as follows. The case when y is the next clock to be reset is again easy, we set the new counter value to N . If x is the next clock to be reset, then the new counter value should be $-v(y)$. To do so, observe that $v(y)$, when z was the value of x when y was last reset, is equal to $N - z$, thus the new counter value should be $-(N - z) = z - N$. If already $z = -(N + 1)$, we do not add anything. Since $z \in [0, N + 1]$ by our case the new counter value has absolute value at most N .

Observe that since we have assumed without loss of generality that y was the last clock to be reset, we cannot have a reset inside the region UPPER-LEFT. Thus, it remains to simulate resets in the regions LOWER-LEFT, LOWER-RIGHT, and UPPER-RIGHT. For this observe that the precise value of each clock is not known, but it is feasible to nondeterministically guess the value of the clocks when the reset

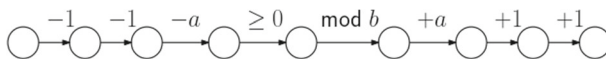
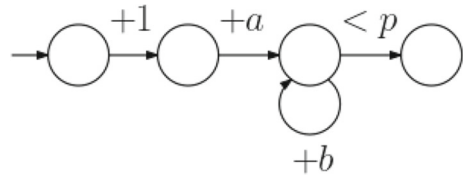


Fig. 22 Gadget testing reachability for Case 3

Fig. 23 A gadget implementing a reset of clock y in the Case 1



occurs, based on the region and whether it was reached from the bottom side or the left side. This case distinction allows us to know the exact starting clock valuation v_l of the \mathcal{R}_l -restricted run preceding the reset. From this, we guess an element t of $\bigcup_{1 \leq j \leq r_l} a_{l,j} + b_{l,j}\mathbb{N}$ to increment the clock valuation by t in such a way that $v_l + t \in \mathcal{R}_l$. We will distinguish which of the two clocks x and y will be reset next.

Case 1. Simulating resets in the LOWER-LEFT region.

Let us first discuss the case when y (and only y) is the next clock to be reset. In this case \mathcal{C} nondeterministically guesses a configuration $q(z + 1 + \delta, 1 + \delta)$ with $z + 1 + \delta \leq N - 1$ reachable from $s_l(z + 1, 1)$, i.e. $\delta \in \bigcup_{1 \leq j \leq r_l} a_{l,j} + b_{l,j}\mathbb{N}$. To do that \mathcal{C} adds a number of the form $1 + a + b \cdot n$ for some $n \in \mathbb{N}$ to the counter and checks that it is at most $N - 1$, as seen in Fig. 23. We remark that counter values along this gadget stay inside $[0, N - 1]$.

Let us now discuss the case when x (and only x) is the next clock to be reset. In this case \mathcal{C} nondeterministically establishes a counter value of the form $-\delta - 1$ such that $-(\delta + 1) \geq z - N + 1$, where $\delta = a + b \cdot n$ for some $n \in \mathbb{N}$, as seen in Fig. 24. We remark that the counter values along this gadget stay inside $[-(N - 1), N - 1]$.

The case when x and y are next to be reset simultaneously can be done analogously by setting the new counter to 0 and is not discussed in detail here.

Case 2. Simulating resets in the LOWER-RIGHT region when reached from the left side.

Let us first discuss the case when y (and only y) is the next clock to be reset. In this case our POCA \mathcal{C} nondeterministically guesses a configuration $q(N + 1 + \delta, N + 1 + \delta - z)$ with $N + 1 + \delta - z \leq N - 1$ reachable from $s_l(N + 1, N + 1 - z)$, i.e. where $\delta \in \bigcup_{1 \leq j \leq r_l} a_{l,j} + b_{l,j}\mathbb{N}$ is of the form $a + b \cdot n$ with $a, b, n \in \mathbb{N}$. Then \mathcal{C} will have counter value $N + 1 + \delta > N$, and thus \mathcal{C} sets the counter value to $N + 1$. To do that, \mathcal{C} works as seen in Fig. 25. We remark that the counter values along this gadget stay inside $[-1, 2N]$.

Let us now discuss the case when x (and only x) is the next clock to be reset. In this case our POCA \mathcal{C} establishes the new counter value $z - \delta - N - 1$, realized by the gadget seen in Fig. 26. We remark that the counter values along this gadget stay inside $[-(N - 1), N + 1]$.

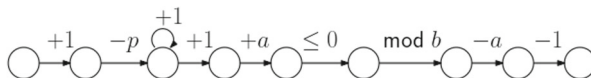


Fig. 24 A gadget implementing a reset of clock x in the Case 1

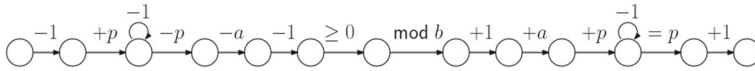


Fig. 25 A gadget implementing a reset of clock y in the Case 2

The case when x and y are next to be reset simultaneously can be done analogously by setting the new counter to 0 and is not discussed in detail here.

Case 3. Simulating resets in the LOWER-RIGHT region when reached from the bottom side.

Let us first discuss the case when y (and only y) is the next clock to be reset. In this case our POCA \mathcal{C} nondeterministically guesses a configuration $s_l(z + 1 + \delta, 1 + \delta)$ with $1 + \delta \leq N - 1$ reachable from $s_l(z + 1, 1)$. We need to check that there exists $\delta \in \bigcup_{1 \leq j \leq r_l} a_{l,j} + b_{l,j}\mathbb{N}$ which moreover satisfies the inequality $1 + \delta \leq N - 1$, or equivalently $z + 1 + \delta \leq N - 1 + z$. Moreover, as by assumption $z \leq N + 1$, and moreover $(z + 1, 1) \in \text{LOWER-RIGHT}$, we must have $z \in \{N, N + 1\}$. Our POCA distinguishes the two cases $z = N$ and $z = N + 1$ explicitly similarly as checking reachability in the LOWER-RIGHT region when reached from the bottom side. The gadget can be found in Fig. 27. We remark that the counter values along this gadget stay inside $[1, 2N]$.

Let us now discuss the case when x (and only x) is the next clock to be reset. The gadget can be found in Fig. 28. We remark that the counter values along this gadget stay inside $[-(N - 1), N + 1]$.

The case when x and y are next to be reset simultaneously can be done analogously by setting the new counter to 0 and is not discussed in detail here.

Case 4. Simulating resets in the UPPER-RIGHT region.

Here by definition of the region the values of the clocks are above $N + 1$ and hence again their precise value is not relevant, only the existence of a way to reach the configuration when the reset occurs. Here we precompute in our reduction whether $\bigcup_{1 \leq j \leq r_l} a_{l,j} + b_{l,j}\mathbb{N}$ is not empty, and then set the counter to $N + 1$ (if y is to be reset) and to $-(N + 1)$ (if x is next to be reset) and to 0 if both are to be reset.

We notice that for each gadget implementation for testing reachability inside a region and for implementing the resets of clock x , clock y or both simultaneously, the value of the counter stays inside the interval $[-2 \cdot \max(a + 2, N), 2 \cdot \max(a + 2, N)]$, where a is the value of the offset used in the gadget.

Checking reachability and simulating resets when x was the last clock to be reset, instead of y , works again in a symmetrical way and can be dually shown to be such that the value of the counter stays inside the same interval. Testing reachability of

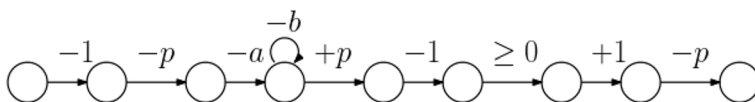


Fig. 26 A gadget implementing a reset of clock x in the Case 2

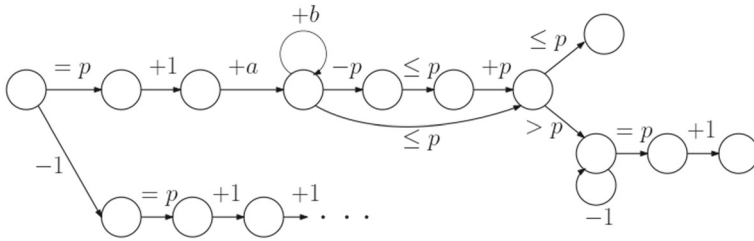


Fig. 27 A gadget implementing a reset of clock y in the Case 3 with details for the case $z = N$. The ... corresponds to the case $z = N + 1$ and works the same way

a guessed final state inside a region works the same way as the implementation of a reset in the region, with \mathcal{C} guessing a final control state in which the computation ends instead of a control state in which the next reset occurs.

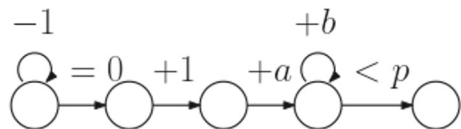
Finally we show how to achieve non-negativity. First, our final automaton checks whether or not the value N is greater than $(2 + c_{\max})$, where c_{\max} is the maximal of all offsets $a_{k,j}$ and all periods $b_{k,j}$ used in any gadget. Then, fixing $u = \max(c_{\max} + 2, N)$, we transition into a new POCA obtained from the POCA described above (the construction where we allowed the counter to take negative values) by first adding two $+u$ gadgets before entering the initial state, as seen in Fig. 29. Furthermore, any comparison operation $\leq p$ (resp. $\leq c$) is replaced by a gadget as seen in Fig. 29, using an appropriate adjusted gadget for $\leq (2 \cdot u)$ comparison. Comparisons of the form $> p, = p, < p$, and $\leq p$ (resp. $> c, = c, < c$, and $\leq c$) are performed in an analogous manner.

Finally, for any modulo test, to simulate a $\text{mod } b$ rule, we have two parallel branches,

- firstly a $\geq (2 \cdot u)$ comparison followed by determining the residual modulo b of the current counter value, say r_1 , using the state space (by repeatedly subtracting at most b from the counter, performing $\text{mod } b$, then adding the same amount as subtracted), then subtracting u , then determining the new residual modulo b , say r_2 , keeping track of it using the state space too (by repeatedly subtracting at most b to the counter, then performing $\text{mod } b$, and then adding the same amount as subtracted),
- secondly a $\leq (2 \cdot u)$ comparison, followed by a similar gadget but where instead of using a $-u$ operation, we use a $+u$ operation and instead of subtracting at most b , adding at most b .

We then compare the two residual r_1 and r_2 stored in the state space, and check whether or not $r_1 - 2 \cdot (r_1 - r_2)$, the residual the counter value would have had without the $2 \cdot u$ offset, is equal to 0 (in the state space), before restoring the counter value to

Fig. 28 A gadget implementing a reset of clock x in the Case 3



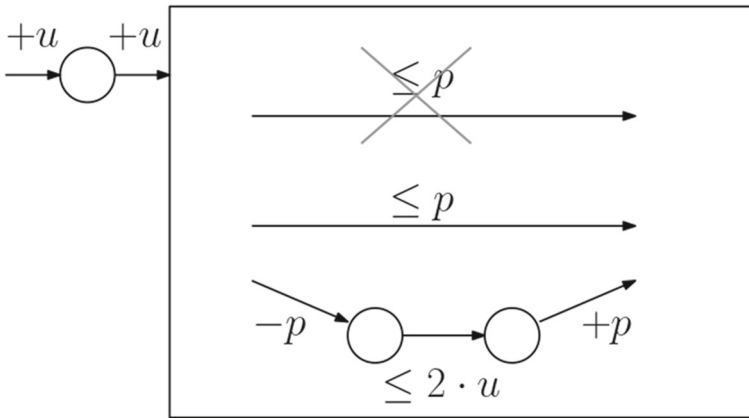


Fig. 29 A gadget for adjusting a $\leq p$ -test when initially offsetting the counter by $2 \cdot u$

the value it had before entering the gadget. Notice that this enforces that the value of the counters stays between 0 and $4 \cdot (\max(N, (2 + c_{\max})))$, and by observing that $|C| \geq 2 + c_{\max}$, this enforces that the counter value stays between 0 and $4 \cdot (\max(N, |C|))$.

Nomenclature A , Alphabet; A^* , Set of words over alphabet A ; \mathcal{A} , Parametric timed automaton; B , Binary word variable; \mathcal{B} , 0/1-Parametric timed automaton; C , Parametric one-counter automaton; D , Deterministic finite automaton; F , Set of final control states; f, g , Functions; \mathcal{G} , Set of guards; g , Guard; I, J , Binary word variables; i, j , Integers; K , Language in *LOGSPACE*; L , Language; m, n , Integers; \mathcal{M} , Turing machine; N , Value assigned to a parameter; op , Operation in a parametric one-counter automaton; P , Set of parameters; p , Parameter; Q , Set of control states; q, s , Control states; R , Rules (for transitions in PTA/POCA); t , Integer (time duration); v , Clock valuation; w , Word; x, y , Parametric clocks; z , Counter value; Z_C , Constant defined on page 15; Γ_C , Constant defined on page 15; Υ_C , Constant defined on page 15; M_C , Constant defined on page 15; ϑ , Non-parametric clock; α, β, γ , (Sub)semiruns; χ_L , Characteristic function of a language; δ , Transition function (of a DFA) or transition-rule of a PTA; η , Hybrid semirun; Λ , Regular language; μ , Parameter valuation; Ω , Set of clocks; ω , Clock; ϕ , Projection of semiruns; π , Run; σ, ρ , (Sub)semiruns; τ, χ , (Sub)semiruns; ε , Empty word.

Acknowledgements We thank Benedikt Bollig and Karin Quaas for discussions and feedback.

Funding Open Access funding enabled and organized by Projekt DEAL.

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